

# 68

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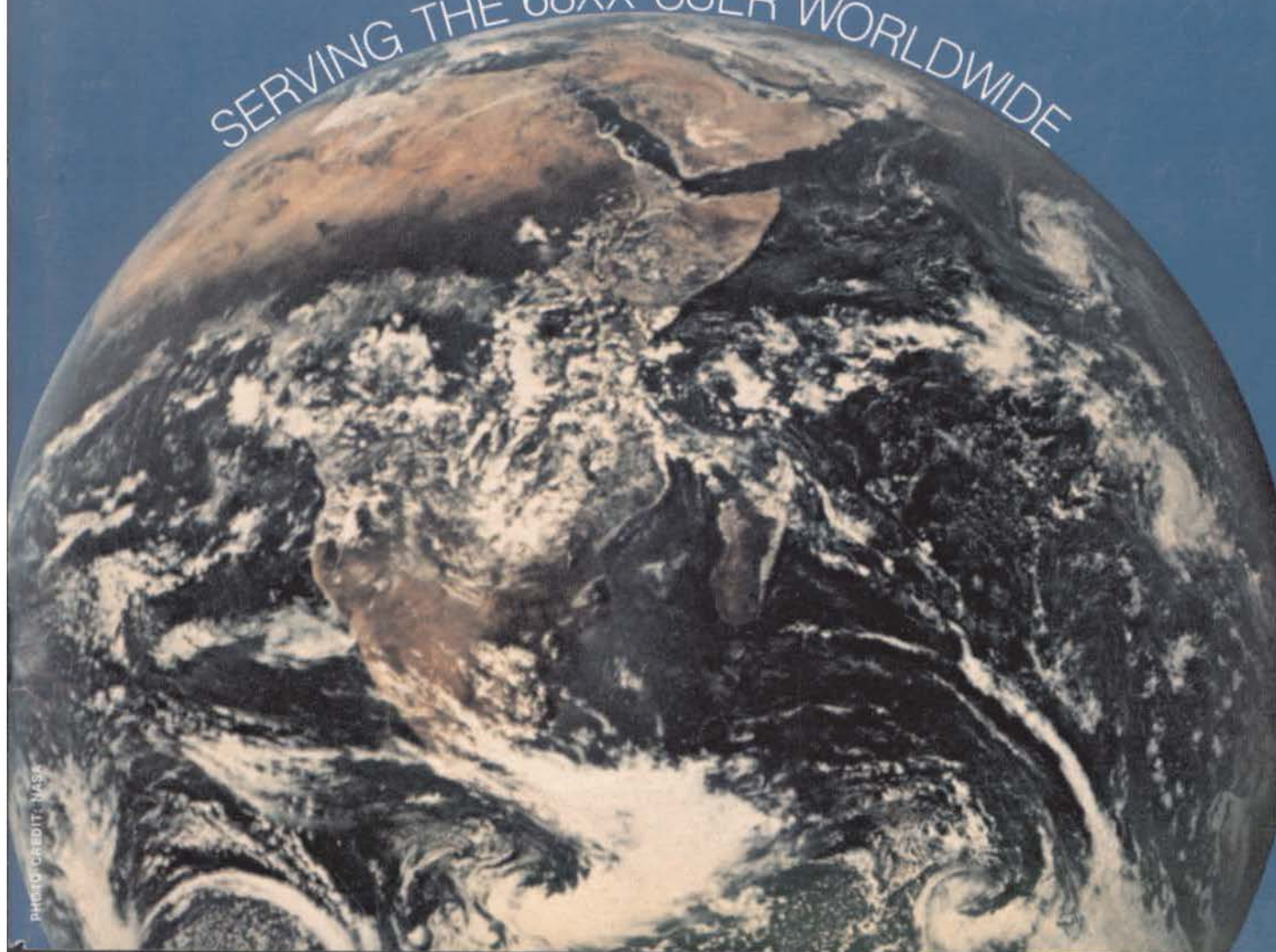
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## MICRO JOURNAL

**VOLUME IV ISSUE IV • Devoted to the 68XX User • April 1982**  
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# Pascal for 6809

Pascal for the 6809 is a true native code compiler. Unlike the usual P-code Pascals which run in an interpretive manner, ours produces efficient assembly language mnemonics which can be assembled and run directly. This compiler is available for both 6809 FLEX™ and UniFLEX™. Many features not found in other Pascal systems were implemented while avoiding those features completely non-standard. Features of the Pascal system include:

- Supports most of Jensen and Wirth specification
- Produces fast and efficient 6809, native code
- FLEX run-time package may be trimmed
- Double precision real numbers (16.8 digits)
- Implements scalar, subrange and structured data types
- Standard I/O using file buffer pointers
- Dynamic storage allocation
- Ability to call other Pascal programs
- FLEX version may call assembly language programs
- Buffered or single character terminal input
- Standard math functions: SIN, COS, ARCTAN, EXP, LN, SQR, SQRT
- Random number generator function
- Many usable, sample programs included
- UniFLEX version supports:
  - Random file positioning
  - Ability to call various UniFLEX system routines
  - Ability to execute UniFLEX utility commands

Pascal on diskette for 5" and 8" 6809 FLEX is available for \$200.00. The 5" version requires two disk drives. The UniFLEX version is \$300.00 and includes one year of maintenance. All orders should include 3 percent for postage and handling (10 percent on foreign orders).

™FLEX and UniFLEX are trademarks of Technical Systems Consultants, Inc.



**technical systems  
consultants, inc.**

111 Providence Rd., Chapel Hill, N.C. 27514  
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# '68'

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San Antonio, Texas 78216

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The following TSC Text Processor commands ONLY should be used (due to our proportional processor): .sp space, .pp paragraph, .ff fill and .nf no fill. Also please do not format within the text with multiple spaces. The rest we will enter at time of editing.

STYLO commands are all acceptable except the .pg page command, we print edited text files in continuous text.

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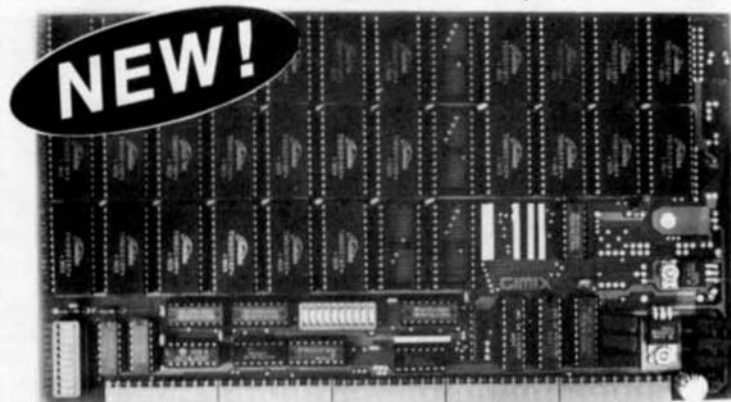
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also available:

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All versions have gold bus connectors and are fully socketed, assembled, burned in, and tested. Versions with less than 64K can be expanded at any time by adding additional RAM chips.

## FEATURES:

- ★ ADDRESSABLE In two 32K sections with separate regular and extended address decoding for each section. Each section can be addressed to any 32K boundary in the address range (1M Byte with extended addressing). Each 32K section is divided into four 8K blocks that can be individually enabled or disabled. Disabled sections do not occupy address space.
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**56K.....\$728.56**

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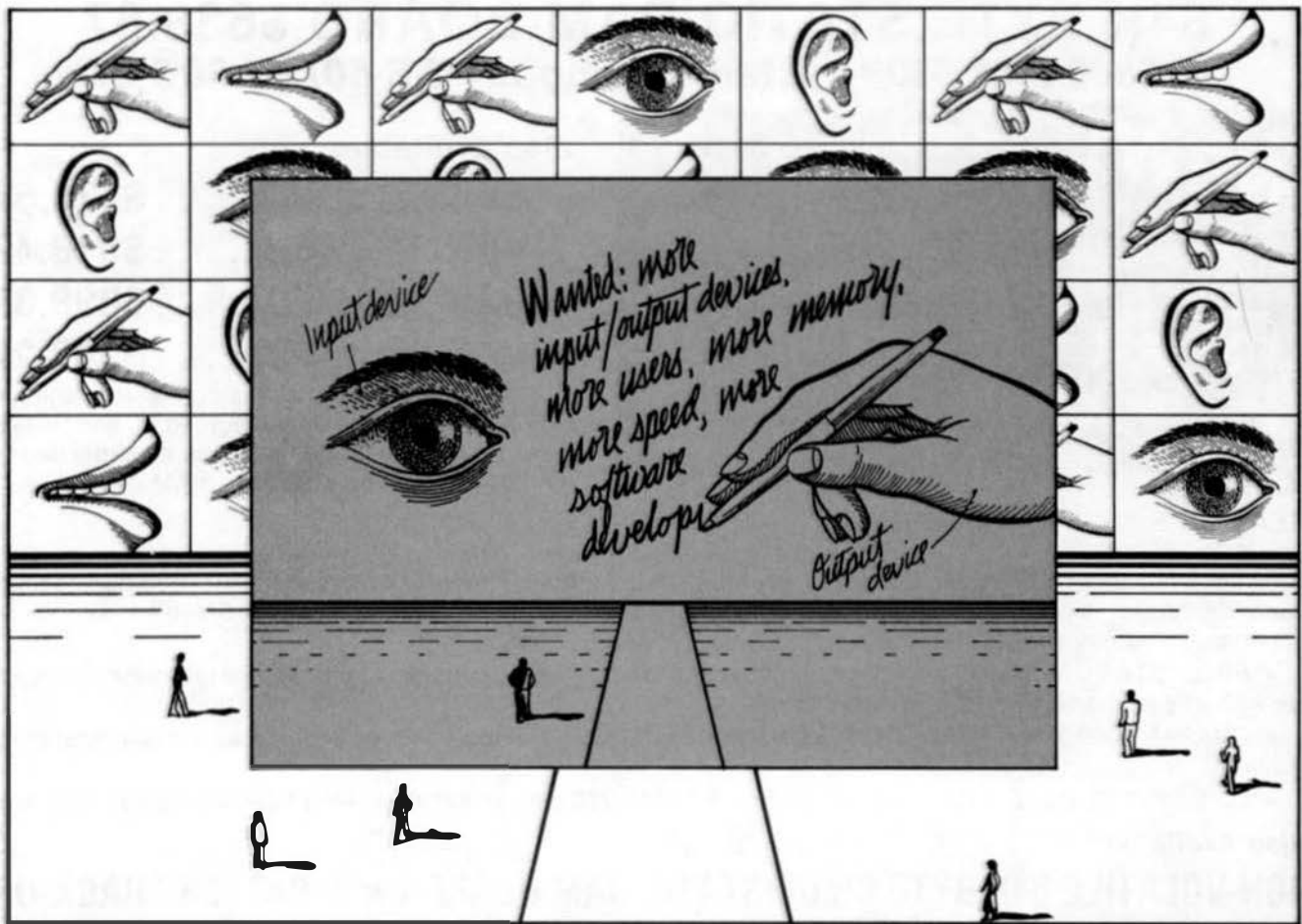


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## Expand your horizons with OS-9™ Level Two

**E**xpand your 6809 computer to a fast, efficient multi-user system utilizing up to one megabyte of memory, almost any I/O device, and comprehensive implementations of the most-wanted programming languages: Basic, C, Pascal, Cobol, and Assembler.

With OS-9 Level Two, your computer is transformed into the ultimate software development system with performance and features found only on large and costly computing systems. It brings to your fingertips the friendliness and power of a Unix\*-based environment. You can even run most Unix\* software tools using OS-9's Unix Version 7 compatible C Compiler.

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**As a real-time system,** your OS-9-based computer can sense, monitor, control, and communicate with the real world thanks to OS-9's highly modular and user expandable structure. Adding customized I/O to OS-9 is

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OS-9 Level Two is available exclusively from manufacturers of most popular 6809 computers equipped with memory management hardware. They offer versions specifically tailored to their computers for use with both new and existing systems.

For more information about OS-9 Level Two contact your computer supplier, or



### MICROWARE.

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# Build performance into your system using the OS-9™ toolbox!

Microware's OS-9 Toolbox holds the finest 6809 programming languages and software tools in the industry. Microware languages are complete language implementations that meet or exceed applicable industry standards, and have unmatched speed, efficiency, and ease of use that Microware software is famous for. *All Microware languages run on OS-9 Levels One or Two.*

## OS-9 PASCAL™ LANGUAGE COMPILER



The OS-9 Pascal language compiler is the most complete and versatile Pascal available for the 6809. OS-9 Pascal has the unusual capability of generating P-code for interpretive execution while debugging OR highly optimized 6809 assembly language source code output for maximum speed. Another feature of OS-9 Pascal is its "virtual memory" P-code interpreter that lets you run incredibly large Pascal programs. OS-9 Pascal meets the ISO 7185.1 Standard and the complete Wirth/Jensen specification.

## CIS COBOL™ COMPILER



6809 CIS Cobol is a compact, interactive and standard Cobol language compiler which is ideal for the most demanding business applications. Standard features are: ISAM, Debug, ACCEPT/DISPLAY, and Interprogram Communications modules. CIS Cobol is the preeminent microcomputer Cobol in the industry, and the OS-9 version retains full compatibility with CP/M applications software. CIS Cobol meets the ANSI 1974 Level One COBOL standard and is CSA certified. Also available is Micro Focus' FORMS 2, an optional automatic program generator that lets you interactively design screen-oriented applications with ease.

## BASIC09™ STRUCTURED BASIC INTERACTIVE COMPILER



Basic09 is the fastest and most comprehensive full Basic language available for the 6809. It combines standard Basic with the best features of Pascal. It is a unique interactive compiler that combines compiler speed, interpreter friendliness, and superlative debugging facilities. RunB, a R()Mable run-time system for compiled Basic09 programs is now available as an option.

## C LANGUAGE COMPILER



C—the systems language of the future—is here today on OS-9. This is a complete implementation of the Unix Version 7 C language including INT, CHAR, SIGNED, UNSIGNED, FLOAT and LONG data types, structures, unions, standard C library, and a full preprocessor with macro definitions. Generates fully reentrant 6809 assembly language source code output.

For information contact your computer supplier, or



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# CIS COBOL™

## The Natural Choice For Business Software



COBOL has been the universal business programming standard for nearly two decades. COBOL is rich in commercially-oriented facilities. It has powerful file handling, formatted printing, and data structure capabilities. It is English-like, so that programs are easy to read and maintain. By far, most organizations use COBOL as their main business programming language. One effect of this is that more professional business programmers know and use COBOL, and the best business application software is written in COBOL.

Microware has developed the 6809 version of Micro Focus's proven CIS COBOL compiler to allow you to run ANSI 1974 standard COBOL on your OS-9 based computer system. It's been certified as such by the U.S. General Services Administration, following stringent testing. This assures that CIS COBOL is compatible with standard minicomputer or mainframe COBOLs. And CIS COBOL has been proven on thousands of micro and mini systems all over the world.

Stability is an important advantage

of COBOL. Unlike some other languages, a firm standard has been established. Because of this, COBOL programs can be transferred from one machine to another with a minimum of modifications. COBOL users can take advantage of the mass of existing programs written in COBOL.

CIS stands for Compact, Interactive, and Standard—the most desirable qualities for microcomputer COBOL. And CIS COBOL offers you much more! It has been specially designed for interactive operation and efficient use on small computers. CIS COBOL has multi-user capability that allows more than one COBOL program to be run simultaneously. CIS COBOL extensions for conversational applications, screen control, interactive debugging, and OS-9's device-independent I/O system.

CIS COBOL's optional FORMS 2 program generator eliminates the need to write simple data entry and inquiry programs. It lets you build a

screen layout on line at the CRT, then automatically generates COBOL source code programs from your screen definitions. Or you can use it to create the interactive screen handling portions of more complex programs.

CIS COBOL and FORMS 2 can be used with any disk-based 6809 computer system having at least 48K of user RAM running Microware's OS-9 Level One or OS-9 Level Two operating systems.

If you need to create business applications, COBOL is your natural choice. And if you want to run COBOL on your 6809 system—or want easy to use interactive business programming facilities—that means 6809 CIS COBOL.

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# EXCITING NEWS FOR COLOR COMPUTER USERS

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## **FLEX, OS-9 and the Radio Shack Disk System ALL on the SAME Color Computer**

Would you believe that you can run FLEX, OS-9 and Radio Shack disk software on the same Color Computer, and all you have to do is change the disk? That's right, just change the disk. If you have a 32K Color Computer with the Radio Shack disk system, all you need to do is make a trivial modification to access the hidden 32K, as described in the Feb. issue of COLOR COMPUTER NEWS and the March issue of '68' Micro. You can get FLEX from us right now. OS-9 will be ready by summer. Please note that this will only work with the Radio Shack disk system and 32K/64K memory chips that RS calls 32K. Maybe they put 64K's in yours, too. If you don't have a copy of the article, send a SASE and we'll send it to you.

Using this system to run FLEX and OS-9 has many advantages. First, it gives you 48K from zero right up to FLEX. This means that *ALL FLEX compatible software will run with NO MODIFICATIONS and NO PATCHES!* There are no memory conflicts because we moved the screen up above FLEX which leaves the lower 48K free for user programs.

What you end up with is 48K for user programs, 8K for FLEX and another 8K above FLEX for the screens and stuff. We are working on a multi screen format so you can page backward to see what scrolled by and a Hi-Res screen that will enable us to have 24 lines by 42 character display. That's better than an Apple!

We also implemented a full function keyboard, with a control key and escape key. All ASCII codes can now be generated from the Color Computer keyboard!

We also added some bells and whistles to Radio Shack's Disk system when you're running FLEX or OS-9. We are supporting single or double sided, single or double density, 35, 40 and 80 track drives. If you use double sided drives, the maximum is three drives because we use the drive 3 select for side select. When you are running the Radio Shack disk, it will work with the double sided drives but it will only use one side and only 35 tracks. Using 80 track drives is okay, but will not be compatible with standard Radio Shack software. You can also set each drive's stepping rate and drive type. (SS or DS - SD or DD)

In case you don't understand how this works, I'll give you a brief explanation. The Color Computer was designed so that the roms in the system could be turned off under software control. In a normal Color Computer this would only make it go away. However, if you put a program in memory to do something first (like boot in FLEX or OS-9), when you turn off the roms, you will have a full 64K RAM System with which to run your program (FLEX or OS-9). When the roms are turned off, it is as if you had removed them from the computer. They are gone!

Now, we need the other half of the 64K ram chips to work, and this seems to be the case most of the time, as the article states. Of course, you could also put 64K chips in.

We decided that this was the best way to run FLEX and OS-9 on the Color Computer because it does remove the roms from the memory map and leaves the full 48K for user programs. If you just put in memory for FLEX and use the Basic hooks for I/O, all you have is a little over 30K for user programs. In addition, very few FLEX programs will run without being modified and some won't run very well, if at all (our DATAMAN + for example). Let me state it again. **ALL FLEX COMPATIBLE PROGRAMS WILL RUN WITHOUT MODIFICATION!!!** and the same goes for OS-9!

It is also the **ONLY** way OS-9 will run because 30K is just not enough.

Some neat utilities are included.

MOVEROM moves Color Basic from ROM to RAM. Because it's moved to RAM you can not only access it from FLEX, you can run it and even change it!! You can load Color Computer cassette software and save it to FLEX disk. Single Drive Copy, Format and Setup commands are also included.

If you don't have a Color Computer, we can sell you one complete with 64K ram, 24K rom, Single RS disk drive and FLEX for only \$1,490, set up and ready to go.

FLEX with Edit, Asmb and installation disk is \$199.

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# WORD PROCESSING SYSTEM FOR OS-9

**DynaStar  
Full Screen Editor  
for OS-9 Systems**

This is the HOT one everyone is talking about. Easy as pie to learn and use. People call and rave about how easy it is to use, and how quickly they got it running. DynaStar is the screen editor we've all been waiting for and now it's here. DynaStar has sold more copies in the first month than any other program we have. Here are some of its features.

Full Screen Editor with a Rich selection of Single Keystroke commands!

MOVE CURSOR Left, Left word, Right, Right word, Right Tab, Up, Down.

SCROLL Up, Down, Up Screen, Down screen.

DELETE Character, Word, Line, Block.

PLUS Find string, Substitute string, Move block, Word wrap, Right Justify, Center. Also read and write side files. Execute Shell commands, Optional help menu.

User configurable to virtually any terminal.  
Available soon for FLEX 9.

## DynaStar Word Processing System

Coming soon!! Full word processing for the DynaStar screen editor. The package will be ready in May. It will include all the capabilities found in other word processors plus many unique to DynaStar. Some of those features are, Mail Merge, Form Letter support, Macro capability, conditional IF, Pagein, and much more....

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**FAST 6809 MACHINE CODE**

SPELLTEST is the most versatile 68XX spelling checker available.

MENUS MAKE OPERATION EASY. From the menu you may: Print a list of suspect words; Print a list of valid words; Check each suspect word one by one; Read your text, stopping to check suspect words; Use additional dictionaries for more thorough checking or special applications; Build an additional dictionary of newly accepted words; Write correct text file to disk. While checking you may: Accept the suspect word; Accept and save in the dictionary; Replace with correct spelling.

Designed to be used by the layman, SPELLTEST is right at home in the office. Ease of use and speed will recover the cost in days.

22,000 word dictionary covers the first 25,000 entries in the American Heritage listing of the most common English words.

500 built in common words (and, or, the, etc.) and 300 specific to your field, filter the text and allows a large file to be processed even in small computers.

**INTRODUCTORY PRICE \$89.95**

**PRICE \$199.00**

**CRASMB**

**MULTI CPU CROSS ASSEMBLER FOR 6809**

**FLEX**

by Frank Hoffman

CRASMB is a conditional macro assembler with the capability to use different CPU overlays in order to cross assemble. These CPU overlays called 'CPU PERSONALITY MODULES' (CPM's) can be called from a source file, thereby making it easy to create object code for a variety of CPUs. It is also possible to create new CPM's yourself for any 8 or 16 bit CPU. The information needed is included in the manual. If you decide to do this, it would be advisable to purchase the source for one of the CPM's and modify it rather than starting from scratch. CPM's are currently available for the following CPUs: 6809, 6800, 6805, 6502, Z80/8080, 1802, and others coming.

**PRICE \$139.95**

Includes one 8 bit CPM of your choice (not source)

Additional CPM's

8 Bit \$25.00 Source \$25.00 extra

**6502 TRANSLATOR**

Translator 6502 code to 6809

**\$75.00**

**INVENTORY  
with MATERIAL  
REQUISITION PLANNING  
\$100.00**

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Disassembler for 6800/6809 or Z80

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**AUTOTASK**

for 6809 FLEX

Just what does a program with the name AUTOTASK do? That is the question, and I will try to answer it. AUTOTASK can be thought of as an addition to FLEX, in that it resides in the background and adds several capabilities to FLEX. For instance, you can run a series of programs or tasks automatically with it, hence the name AUTOTASK. But it's more than that, and its power is not going to come through in this small ad. The tasks are put into a buffer that's similar to a stack. You can add or remove things from the buffer at any time from any program. This means that a running program can change the flow of the procedure that AUTOTASK is running. This may sound familiar to users of the Job Control Program that we also sell, and it is similar in that many of the same things can be done, but different in the way it works. AUTOTASK runs a procedure like JCP but when that is over with, AUTOTASK goes to sleep until it is called to do something else. JCP runs a procedure and then ends, removing itself from memory.

AUTOTASK is small (less than 1 K) and can be configured to be even smaller at assembly time. AUTOTASK can be changed on the fly. It also has conditional features and can call procedures from the disk. It is also simple to use and can be used interactively. To really get the feel of it you have to try it or buy the manual (can be applied to future purchases).

**PRICE \$129.95 with source on disk.  
Manual \$10.00**

**Dynasoft PASCAL 1.4 for OS-9**

Dynasoft Pascal 1.4 includes all the features of the FLEX version 1.3 with the following enhancements: Chain, Read, Write, Seek, Open, Create, Close, Delete, Fork, Send, Wait, Sleep, Settime, Time, Getstatus, Setstatus, Setpriority, Getproctid, and JSR. This is an excellent and fast program, small enough to write utilities but powerful enough for things like DynaStar.

Object only \$69.95  
Add for run-time source on disk \$30.00  
Add for source of Dynasoft Pascal itself \$125.00

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**DATA BASE MANAGEMENT  
ONE PROGRAM THAT DOES IT ALL!**

DATAMAN+ is for BUSINESS systems with 56K user ram. You can use DATAMAN+ for inventory control, work scheduling, mailing lists, sales reports and much more. The powerful report writer lets you create invoices, statements, form letters, and any other type of report your business needs. You can perform calculations with your data and print out the results. Special printer handling allows use of any size column output, not just 80 and 132.

DATAMAN+ is password protected at the menu level so that redundant password prompts are eliminated. We've added the human touch with the use of the operator's name and calculator style input. DATAMAN+ checks for valid data types on input thus eliminating erroneous data in your database.

DATAMAN+ is report writer has added intelligence so that separate select programs need not be run to create different reports from the same database. As a matter of fact, the report writer is so flexible that you can use it to create invoices, statements, even form letters using data from the database. You can even perform calculations with the data and put the results in the report.

Setting up your system to run DATAMAN+ is very easy and automatic. The entire system has been designed with the inexperienced user in mind. The operation of the system is so easy that although a manual is provided none is required to run DATAMAN+.

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# POLICY

Because of a few complaints received concerning our way of doing things I will repeat a policy we published some months ago. I have certain policy requirements that some other magazines do not have. I do not now and do not intend in the future to attempt to emulate other magazines! What we do we do because I feel that all things considered, cost, reader satisfaction, advertiser consideration, distribution and the size of our market place dictates some trade-offs and compromises. There are a lot of ifs, if there were more advertisers, if there were more 68XXs being used, if there were more manufacturers making 68XX devices, if the economy was better, if postage didn't keep continually increasing, on and on. But as we all know the ifs are a continual gotcha. So, I publish 68 Micro Journal in such a manner as to try to keep away from the gotchas and negate the ifs for the least impact. The results are compromises and are never optimal. But at least we do have a magazine, just for us and it keeps coming every month and is full of something that all of us can use and in many cases something that many of us need. To me that is the name of the game, being. Also there is one thing for sure, there is more 68XX related material in 68 Micro Journal than all the other magazines combined! Standard S50 Bus, Color Computer or you name it, 68 Micro Journal has much, much more.

Over the past four years, and we have been around all that time, five (5) other magazines have attempted to publish a 68XX magazine, before and since we started. To the best of my knowledge some failed, a couple are still around, and 68 Micro Journal still covers over 80% of the market. The reasons are fairly evident, first we do our own printing, binding, editorial work and in fact everything except our mailing. This makes us unique in the world of magazine publishers. Most others have to have it done by outside vendors. This increases cost. Also we do most of our typesetting on our own 68XX, Standard S50 Bus computers! We do this for more than one reason. We do it because we are proud of the quality of the hardware and software that allows us this option. Also we do it because the computer knows more about its own language than any human typesetter. That is why you will seldom see a long list each month of errors from previous issues, in 68 Micro Journal. Check some of the others! There is nothing more annoying than to type in a program and then spend two or three months going over, and over and over the program trying to find out why it 'bombs' and then be told (two or three issues later) that a human typesetter had transposed or unknowingly left out a 'couple of lines'! Also another complaint is that some of the letters and other info we publish is hard to read. Well, there is a good reason for this, it came to us that way and in hopes that you can use it we publish it just as we received it. At most other magazines it would have been dumped to 'file 13' and you would have never gotten the information. This way we think is better.

However, the major consideration also figures into all of the above. That is COST! Our 'get ready' and 'make up' cost are the same as if we were going to print 250,000 copies, of course we don't, our group is nowhere near that size, so we have the same initial cost as the biggies. Believe it or not but the paper and press running time is a smaller factor. Our presses can run 150,000 sheets of paper about as fast as we run 15,000 sheets. But again the set up time is the same, and so is that cost. Also we could typeset more but by this time we are scratching our heads trying to figure out how to keep the final cost reasonable. We keep it down by photo-reducing as much as we can, using our

computers to generate as much text as we can and most important of all is you the ones who contribute your experience and skill to all of us by sending us your efforts, in the form of articles, hints and kinks and other valuable and much needed information. For this I and thousands of our readers are grateful to you!!! We can never THANK you enough.

Our loyal advertisers are also to be thanked, for without them we could not continue. And then we would all be back where we were five years ago, a fine group with excellent soft/hardware but nothing in the other magazines for us! It is still that way.

I was lately told by one of our advertisers that 68 Micro Journal 'embarrassed' him in the eyes of one of his friends or a customer, I don't remember which. For this I am truly sorry, BUT I have no intention to change things much. If I did he would soon have no place to advertise his goods (and reach a significant part of the 68XX market, at a reasonable rate), the cost would become a certain gotcha. The 'numbers game' rules still demand our doing the best we can and exist. Exist we do and intend to continue, but there are bills to be paid and readers who want this information, so to stay in the 'black' and continue to bring you the very best I can, some corners will be 'cut'.

I have had less than bubbling reaction from some of our advertisers and manufacturers of Standard S50 Bus concerning the 68XX Convention, in Atlanta in June. Seems that these dates conflict with NCC. A pity as NCC has far less for us than a good assembly of our 68XX folks. It should be noted that some had committed to NCC before we could get this info out. I will still be there as well as some of our advertisers and for those that do make it, I feel sure that the effort will be well worth it. Also to dispell a possible rumor, I and the whole CPI family, 68 Micro Journal, Oata-Comp/S.E. Media will be there. One other point needs to be made, I and CPI have no interest in this show other than to have a place to get together. We have NOTHING to do with the arrangements and will not receive even one penny for our efforts! In fact we will be paying the same price as every other exhibitor and even donating door prizes! We are supporting the 68XX Convention because hundreds of you ASKED for it. That is the only reason. I must admit that even the sponsors of this event have not indicated much enthusiasm either. All I can say is that for all the years we have exhibited in Atlanta this year seems to be the quietest. However, we and others will be there and look forward to seeing all of you!

## Flex User Notes

BY: RONALD W. ANDERSON  
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ANN ARBOR, MI 48105

### MORE ON SPELLING CHECKERS

Peter Stark's Magic Spell has been enhanced by compressing the dictionary by a simple technique. The previous dictionary occupied about 420 sectors. The compressed version occupies 189, and it is processed in 45 seconds. Peter's dictionary is about 13,000 words in length. When you add words, the resulting file is automatically "uncompressed". You may, however, stop the output at a number of sectors specified by you, and change disks to save the remainder of the file. The expanded file with the added words may then be compressed again into a single file, using the COMPRESS utility supplied with the new version. I ran a test on a file of about 2000 words, and found Magic Spell to read the text file in 30 seconds, and process the dictionary in 45 more. Magic Spell now reads as much of the dictionary file as it can, finding suspect words, outputs those words for your consideration, and then



finishes reading the dictionary, outputting more words after it has finished. Peter indicates that this change was done so that if you are adding words to your dictionary, the drives won't select and deselect heads for every sector of old dictionary copied to new dictionary.

This dictionary compression feature makes a very large dictionary feasible even with a 5" disk system. The only juggling that has to be done, is limited to the process of adding new words. Operation is still simple and well prompted.

#### BOOK PROJECT OONEI

Yes, I've finished my book on Pascal Programming. It has been shipped to the publisher, and will hit the bookstores around early Summer. I'm told by TAB, I spent the entire four day Newyears weekend finishing up the text and printing it on a borrowed Centronics 737 printer. I was really impressed by the quality of the output of the 737 when in the high quality mode (proportional spacing). To take full advantage of this printer, you need text processing software that will justify text on a proportional spacing printer. The latest version of Stylograph will do that quite well. That 737 output looked for all the world to me like the output of a good electric typewriter. Characters are all well formed with no "Mickey Mouse" fake descenders on p, q, y, and g. All lower case letters are true lower case and not "miniature" upper case letters. I had only one problem using the TSC text processor. It doesn't support the proportional spacing table required to get true justification. I found, however, that the lines were quite uniform in length, and acceptable in a double spaced manuscript. The only large problem was that the proportional spacing fouled up all my tables. The columns were automatically "detabularized". Had I this job to do over again, I would use Stylograph, which would produce fully justified output, and aligned tables.

I've tried to include numerous examples of various kinds of programs in the book, and I will (pending approval by the publisher) include one or two in this column. I worked out a 5 page pascal program that is a simple text formatter that provides variable line length and margin, paragraphing, paging with page numbers, right justification of titles, centering of titles, and formatting of tables etc. without justification. All paragraphs are fully left and right justified.

#### NEW VERSIONS TO COME

Speaking of Pascal, OmegaSoft has just released version 2 for FLEX09. We received a copy yesterday, so I can't say a great deal about it yet. It has a number of new features including the implementation of RECORDS. I ran a quick compile of a program that I had done in version 1. (A Prime number program, of course.) It compiled in the same time, assembled in about 20% less, generated about 10% less code for the program, (more than offset by an increase in the size of the runtime code), and ran about 30% faster. I promised Bob Reimiller that I wouldn't review it yet, but let the dust settle a bit and see if the first field use uncovers a few bugs. I won't worry if I find a few, because OmegaSoft has been extremely responsive to bug reports, generally having a fix in a day or two! This compiler is very complete, a whole system in itself, well integrated into FLEX, and priced probably out of the range of most of the hobbyist market. I'll fill in more detail in a more complete review after I've had time to digest all the new features.

I suspect that many of the SS-50 users that read this column are more than hobbyists. That is, they thoroughly enjoy this hobby, and use the knowledge in software and hardware gained in the hobby activities directly in their profession as well. If you have a need

for an excellent native code compiler that interfaces well with hardware and the outside world, look into this product.

Lucidata has announced the availability of their Pascal compiler in a UNIFLEX version, most likely before you read this. They have added some enhancements in their "revision 6 of the run-time system and level J of the compiler." These include the extensions necessary so that a program may prompt the user for a filename, and then open that file for use by the program. It will work something like this. If you have a logical filename DATA in your program, and want to associate it with a filename within the program, (a SCRATCH file, for example), you would use ASSIGN (DATA, 'SCRATCH.TXT.1');. If you had input a string into an array of character called FILENAME, I assume you could use ASSIGN (DATA, FILENAME);. Since Lucidata has an extension that allows you to read a string into an array of characters, you could prompt for the filename.

```
WRITELN ('DATA FILE NAME ? ');
READ (FILENAME);
ASSIGN (DATA,FILENAME);
RESET (DATA);
```

The file whose name you input in response to the prompt would at that point be opened for read, and associated with the internal program logical filename DATA. Some of this is conjecture, since I don't at this moment have the new version to try.

#### NEED HELP? BE SPECIFIC.

I received a half page letter from an SS-50 bus user a couple weeks ago. It essentially lists this user's hardware, (indicates non-FLEX system operation), and says that most or all of the software "doesn't work". The BASIC "always seems to give me error codes no matter what I try to do on it". Since I have never seen the operating system in question, and have not used any of the software, there is not much I can do to help. Any readers nearby who could give this reader some help? He is Harry Colborn, 1152 3450 Drive, Hotchkiss, Colorado 81419. Anyone in the area familiar with Percom hardware and software in a SWTPC 6800 system could be a great help in getting someone started in the right direction. With the limited information I've received, there is little I can do from here.

#### MORE ON 6809 ASSEMBLER CODE

In line with continuing my series on effective use of 6809 assembler, I've found some new users of the 6809 a bit bewildered by the concept of indirect addressing. It is really not terribly hard to understand when you use an analogy with indexed addressing.

LDA 0,X loads the accumulator with the contents of "the address contained in the X index register". That is, if X contains the value \$14FE, the A accumulator is loaded with the contents of address \$14FE. In a very similar manner, LOA [\$1000] uses the contents of addresses \$1000 and \$1001 as a 16 bit register to point at an address whose contents are to be loaded into the A accumulator. If address \$1000 contains \$14 and address \$1001 contains \$FE, the result of LOA [\$1000] would be to load A with the contents of memory address \$14FE, the same result as the indexed instruction above.

Thus you can see that indirect addressing provides a virtually unlimited number of pointers in addition to the four hardware pointers (X, Y, U, and S) provided in the 6809. I don't mean to complicate things unduly here, but the instruction LDA [0,X] would load A with the contents of the address pointed at by the contents of the address pointed at by the X index register. No, I didn't accidentally repeat myself, and



there is no typographical error in the preceding sentence. The result of that instruction is to use two levels of pointers. X points at a pointer that points at the data.

Then we have what is known as "register offset indexed addressing". The instruction looks like LDA B,X or LDB A,Y or LDA D,X. The effect is to add the value in the register to the index register contents. For example, if B contains \$10 and X contains \$1000, LDA B,X would load A with the contents of address \$1010. This sort of instruction is very useful in an application such as a look up table. Suppose you want to translate ASCII codes into some other code. You might use a table like this.

```
TABLE EQU * * COMMENTS SHOW VALUE IN B ON ENTRY.
FCB IS VALUE IN A ON EXIT
FCB 'q ASCII 0
FCB 's 1
FCB 'b 2
FCB 'x 3
*
FCB 'A $23
```

Now suppose we write a subroutine that is entered with the ASCII value in A and returns the "looked up" substitute value in B.

```
LOOKUP LDX #TABLE
      LDB A,X
      RTS
```

Now, enter the subroutine with with the value \$23 in A and you will get \$41 returned (the value of ASCII A). You might use this sort of approach, for example, in driving an old IBM typewriter, whose character set is scrambled with respect to the ASCII codes. Your program would output the ASCII codes just as it would to your terminal, but the output routine would include that subroutine to translate the ASCII character to the required foreign code to print that character on the typewriter. The table uses only one byte per output code, the position in the table corresponding to the input code. Writers of compilers make use of the register offset instructions in array accessing. Suppose we have an array of double byte values such as 16 bit integers. We can load X with the "base address" (the address of the array value that corresponds to subscript 0). Now LDD INDEX (the array subscript corresponding to the value to be found). Since the array elements are double bytes, we have to multiply the value in D by two, perhaps by using ASLB followed by ROLA. Now, LDD D,X will get the "INDEX'th" value in the ARRAY in the D accumulator. (Just in case you need a reminder, the D accumulator consists of the A and B accumulators together.) D holds a 16 bit value, high order byte in A and low order in B.

The 6809 has what is called auto increment and decrement mode indexed addressing. LDA O,X+ does the same thing as the two instructions LDA O,X and INX did in 6800 code. You may use two pluses to increment twice. LDA O,-X does the same thing as DEX followed by LDA O,X in 6800 code. Here also, a double decrement is allowed. When the auto modes are used, no offset is allowed. This mode applies to Y, U, and S as well. The 6800 had two instructions TAB and TBA for transfer of contents between the two accumulators. The 6809 has extended that to the more general TFR instruction. TFR X,D copies the contents of X into D. TFR A,B copies the contents of A into B. Any pair of registers are valid as long as they are both the same length. That is, TFR X,A is not valid because X is 16 bits and A is only 8. EXG works the same way, except that it causes the values in the two specified registers to be swapped.

## MORE ON USING LUCIDATA PASCAL

A time or two ago, I included a memory dump program. I thought it might be informative to show how to duplicate the output of that exactly, with a Pascal program. This program is only possible because Lucidata has implemented some extensions. The necessary one is the ability to declare a variable at an absolute address. Unfortunately, since Pascal doesn't allow unsigned arithmetic and doesn't have a TYPE HEX variable, there are still some limitations. Perhaps someone will see a way around this, but I don't think it is possible presently to access memory above \$7FFF for a dump. The problem is that the limit of integer arithmetic is +32767 (\$7FFF). In fact, I found I got an arithmetic overflow until I reduced that limit to 32766 or \$7FFE. The thing that makes this dump program possible is the ability to declare a variable at an absolute memory address.

Lucidata has a means to do this. I found that their Pragmat feature, would work only with comment delimiters (\* and \*), though they will accept the "curly braces" elsewhere. (A pragmat is essentially an instruction passed to the compiler that is not part of the program itself, such as instructions to turn the listing on and off, analogous to LIS and NOL instructions in an assembler). Lucidata signals the compiler that an instruction is to come by placing a "\$" immediately after the left comment delimiter. To start allocating variables at a particular memory location, (suppose \$100 for example) you simply include the following comment before the variable declaration in the VAR section of the program. (\*\$ADDRESS=\$100 followed by optional comment \*). To go back to allocation of variables on the stack (the normal mode) you use (\*\$STACK\*). ADDRESS may be abbreviated A, and STACK may be abbreviated S.

The listing is presented here and you may see that I declared a variable MEMORY : ARRAY (0..32766) of BYTE. Byte is an extension in Lucidata, and is a single byte integer type, that is it has the value range of 0 to 255. Since the array was declared starting at \$0, the index value into the array is identical to the memory address being accessed! Now the only problem is to do the conversions to hexadecimal for input and output. You enter the starting page as a hex value such as 0F to dump memory from \$0F00 to 0FFF, for example. The input value is treated as a character and converted by the procedure GETPAGE to an integer (which is represented as a 16 bit binary value in Pascal) so that it is in the proper form to access that memory area.

The function HEX takes one HEX digit, which can have the binary value 0000 through 1111, representing 0 - F, and converts it to the proper ASCII character to represent the digit. A memory address is four hex digits. The value PAGE represents the two high order digits. LINE represents the next digit, and the index variable L represents the low order digit. The sum of PAGE times 256 + LINE times 16 + L is the address of the byte to be shown. PAGE DIV 16 yields the value of the highest order byte of the address, and PAGE MOD 16 yields the value of the next order byte. Line is the third and L is the last. Performing the HEX function on those four values and outputting them, produces the address at the start of each line. The DIV 16 and MOD 16 trick is again used to split the contents of each memory location into two hex digits for their output.

When a line of 16 bytes has been output as hex digits, that line is repeated with the ASCII representation. There are two problems here. First, the first 31 ASCII values represent control codes, and if they are output, the cursor may be redirected, the screen cleared, etc. Secondly, Pascal will generate an error for any ASCII value larger than \$F (decimal 127). That is, the 8th bit of the ASCII code must be zero. If M > 127 THEN M := M - 128 takes care of the 8th bit, and IF M < 31 THEN WRITE (CHR (M)) ELSE WRITE ('.');



care of control characters. This is, by the way, exactly how the assembler version handles control characters and the 8th bit. OmegaSoft has an extension that allows in line assembler code that could be used to write a procedure to AND the value of M with \$7F to do the same thing. The AND function in Pascal is only implemented for Boolean variables. That is, it is not a bitwise AND.

The program runs in a REPEAT UNTIL loop, and after output of each page of dump it asks for a command. Just as in the assembler version, I've implemented a command table. E will exit the program. N will result in a prompt for a new page. F will dump the next page and B the preceding one. As I indicated above, I don't see a way of accessing all of memory with this program. Since HEX constants are allowed, I tried defining constants MIN = 0; MAX = \$FFFF; and then declaring MEMORY : ARRAY [MIN..MAX] OF CHAR; I got an error message that indicates that MAX is a smaller number than MIN, so apparently it is not recognized as an unsigned number even if declared as a Hex constant. At any rate, the program might illustrate how it is possible to generate a system utility program in Pascal, and show how some Hexadecimal values can be handled.

See Page 36 Pascal Program P Dump Listing

## COLOR User Notes

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Well, another month for the photo-copy routine -- I have my Computer on line now, but the Disk Controller is still ILL. (For those who missed last month's Column; the Primary windings of the Power Transformer in my computer opened, creating havoc with various and sundry parts internally - the new unit I obtained was also open. There was no sign of overheating, etc., it just died; or, more like it, had a stroke.) All systems should be up-and-running by next month - HOPEFULLY. Something like this makes you realize what a SUPER Operating System FLEX is, and how much power and flexibility you have with such a System.

While we are on the Power Supply subject, my problem was not unique. I have talked to SEVERAL other Color Computer owners who have had the same failure; must be a problem with the Transformer itself, although it normally does not cause any damage to other parts when it goes. These things show up sometimes; hopefully, Radio Shack will see the problem in Parts Flow and solve it. Another item; the Crystal Oscillator which generates the System timing appears to be extremely sensitive to +5VDC instability. Since the "TINT" and "COLOR" on the TV are controlled by this frequency, it is easy to determine when your +5VDC Regulator is not up to par. If you are getting different Color "Tints" with RESETS; or the Color is just not stable, then you might consider replacing the Regulator before it goes and really 'cleans house' with Memory Chips, etc.

I have a few FLEX Disks that I haven't been able to read in a while - one from Pete Stark of Starkite on his MAGIC SPELL Dictionary for the Color Computer FLEX Operating System with a "READ/ME" file on it that I'm dying to read. Anybody know how to read magnetic filings; I may try it if my

Controller doesn't show up soon. Also, I think I have a copy of TSC's XBASIC converted to run on the Color Computer FLEX System from DATA-COMP (615 842-4601) - that will sure open the use of this machine to a lot of GOOD Programs. We'll look at these and several other things next month.

### CARTRIDGE TAKE-OVER DISABLE:

Evidently we are gaining some new Color Computer users AND readers: I have a couple letters wondering about the "Cartridge Disable" we mention in some of the 68 MICRO JOURNAL ads in other magazines. As a review for new readers, and a little more info on the Cartridge Slot, the following discussion is presented:

Slide the 'door' on the Cartridge back so you can look at the Printed Circuit Lands on the edge of the Board; notice that one of them on the TOP is shorter than the others, that is, it does not come all the way out to the edge of the Board. That is the +5 VDC Land (more about it later). It is Pin 9 on the Cartridge Connector; the Land right below it goes to Pin 10 on the Connector. To disable the "Cartridge Take Over" feature of the ROM-Packs, COVER PIN 8 (eight) WITH A PIECE OF SCOTCH TAPE. Pin 8 is the Land on the bottom of the Board just to the LEFT of Pin 10; so cover the BOTTOM Circuit Land just to the LEFT of the "shortened" +5 VDC Land on top (looking into the Cartridge with the edge that goes to the BACK of the Computer on your Left). If you cover the wrong one in that area of the Board, it won't hurt anything.

Pin 7, the one just above the "covered" PC Land, is the "G" Clock Signal; normally it triggers the "CART" line and causes this "Take-Over". Since some Expansion Cartridges in the future MAY use this signal, covering Pin 8 will allow it to operate normally.

Once you cover that Land, the Cartridge will not "Take Over" the Computer when you turn it on. To use the Cartridge, ENTER 'EXEC &HC000' and hit '<ENTER>'. This causes the Computer to 'EXEC'ute the program at Hex Address 'C000'. There is no way to get back to 'BASIC' except with some of the non-Radio Shack Monitor Cartridges like The MICRO WORKS CBUG Monitor, which has the capability of "Jumping" to a program (or in their case, 'G'ing to BASIC).

The reason the +5 VDC Land is shortened on the Radio Shack Cartridges is so you will be LESS LIKELY to cause any damage to the Computer if you plug the Cartridge IN, or UNPLUG it, while Power is ON. That shortened Land helps insure that all other connections are made before POWER is applied to the Cartridge. It is RISKY to PLUG and UNPLUG Packs with the Power ON, but it usually doesn't cause any problems with THOSE Packs. NO NON-RADIO SHACK CARTRIDGE that I know of has this protection; they will DEFINITELY blow parts in the Computer if inserted or removed with Power on.

(AS WILL RADIO SHACKS new DISK CONTROLLER, which does NOT have this same protection ?????).



# 'COLOR COMPUTER USERS NOTES' INDEX:

Along the same reasoning, the following is an INDEX of the past COLOR COMPUTER Users Notes Columns, so you can specify which specific issue you need when requesting Beck issues of the 68 MICRO JOURNAL Magazine:

May 81: An article by Bob Mergeson on C.C. fixes.

Sep 81:

REVIEW: CER-COMP COLOR COMPUTER EDITOR; An excellent Tape Based Line Editor.

Oct 81:

GENERAL: Introduction to the CCUN Column.

INFO: More 'fixes'; heat problems, sticky Keys, 32K Mod, inserting Cartridges.

REVIEW: DATASOFT S.E.C.S. Screen Editor (not impressed).

REVIEW: DATASOFT SIGMON - an EXCELLENT Tape Monitor (which I still use A LOT).

Nov 81:

REVIEW: EXATRON's "Expansion Interface" and Disk Controller which are now being used for the 'FLEX' Operating System on the Color Computer - Both Excellent!

REVIEWS: The MICRO WORKS CBUG Monitor (Including wiring it into the RS Diagnostics Pak), 80C DISASSEMBLER Source Generator; Both EXCELLENT, as are ALL of their products.

Also has another review of S.E.C.S. and SIGMON; and the initial letter from Steve Odneal on his "FLEX" Conversion to the Color Computer, which is the one now being sold by DATA-COMP..

Dec 81:

REVIEW: ARMADILLO SOFTWARE's ARMADILLO BUG - A monitor written in BASIC - Good.

QUICK LOOK: TALLGRASS TECHNOLOGIES DISK INTERFACE and FLEA Monitor - Both good but has limitations.

QUICK LOOK: CER-COMP Mdlek-9 - a very good Disk Operating System ROM for the TALLGRASS Controller.

QUICK LOOK: CER-COMP CO-RES9 Editor Assembler - Tape based, EXCELLENT. Full Assembler which provides Pagination on Listings.

QUICK LOOK: CER-COMP TRSMON - An excellent Tape based Monitor.

BOOK REVIEW: MICROCOMPUTER ARCHITECTURE AND PROGRAMMING by John Wexler - An EXCELLENT Book which any one interested in this type of a Magazine should HAVE IN HIS LIBRARY.

Jan 82:

GENERAL: Discussion updating Col. Comp. info. and the first discussions of using FLEX on the Color Computer and its' configuration.

REVIEW: MARK DATA PRODUCTS BERSERK - an excellent Game which makes very good use of the C.C. Sound and Graphics capabilities.

PRODUCT REPORT: The MICRO WORKS MICROTEXT ROM Pack for use with Modems and Communications - provides a Printer Output at the

ROM Pack. As usual; OUTSTANDING.

RUMORS: About R.S. Col. Comp. Products and the FIRST (to my knowledge) PUBLISHED hints about the now "Introduced" 68000-based Model 16.

Feb 82:

INFORMATION: First discussion of the 6803 SAM and "REAL" capabilities of the Color Computer.

QUICK LOOK: ATOMTRONICS 64K Mod and WOLFBUG Monitor - it WORKS Great!!

Mar 82:

INFORMATION: Further discussion of the 6803 w/ use of 96K Memory, adjust Tape Player for 'hard to read' tapes.

REVIEW: The FLEX Conversion Package.

QUICK LOOK: Radio Shack DISK SYSTEM.

QUICK LOOK: NELSON SOFTWARE's SUPER "COLOR" TERMINAL - Looks GOOD for ANY type of Communications; Modem, to other Computers, etc.

REVIEW: COMPUTERWARE's COLOR PAC ATTACK - OUTSTANDING.

## COLOR COMPUTER NOTES:

There seems to be a "BUG" in the BASIC V1.1 ROMs in the RESET/Clearing Pointers area. I haven't been able to nail it down to a specific area yet - thought I'd mention it and see what you have found. If we can get enough information, it will make it a lot easier for Radio Shack to find a "fix". I first ran into the problem when I tried running a Machine Language program I had Taped on a new Color Computer. The program was one of the 'load-and-go' Tapes, which I had stopped with RESET and then loaded with MY Tape deck (I was having trouble reading the original due to Head Alignment). It had played with no problems on my System, but when I went to show it to someone on a new unit, it WOULD NOT RUN. After some checking, we found that it was the Systems with the new Version 1.1 ROM that the Program would not play on. Since then, I have run into a few other things that I think are related.

With a new 32K unit with the Radio Shack Disk System, I have found that two different Machine Language programs will not run when loaded from Disk IF a BASIC Program has been "RUN" previously. Hitting <RESET> will not enable it on one of them; the Computer must be TURNED OFF and Back ON to get the program to run. The problems are probably tied in with the fact that Disk BASIC's Verilable Storage area goes all the way up to \$0988, which means that M. L. Programs will have to start ABOVE this area, and that it will NOT be CLEARED from a RESET. How about letting me know of any problems you have had along these lines; what problems, under what circumstances, etc., and maybe we can accumulate enough information to nail it down.

Next Item: I am hearing RUMORS that there is a GOOD chance the 32K RAMs in the Radio Shack upgrade package are NOT 32K after all; but 64K units with ALL 64K OPERATIONAL. I know the price of the chips has been dropping drastically; it now seems that the FULL 64K chips are as inexpensive,



or maybe even less, than the original 32K units (which were 64K chips with a bad bank) were a few months ago. If this is true, whole new fields are opening up. I have been running the ATOMTRONICS 64K Mod for a while (at least when the Computer was working - they appear to have lived through the problems OK), and it is great. A little more work on WOLFBUG (which just doesn't seem to like the EXATRON Controller right now - and again, I haven't been "up" with the system enough to define the problem yet - I suspect some overlapping memory problems right now, though), and it WILL BE GREAT. I must mention, in complete fairness, that the problem with WOLFBUG is ONLY WITH THE EXATRON EXPANSION UNIT HOOKED UP; WOLFBUG works GREAT, otherwise.

I think you will see a lot of amazing, to some, anyway, things happening with this machine in the near future. We have a Radio Shack DISK CONTROLLER / FLEX marriage working right now; it still needs some smoothing out, but the idea is feasible. Also, I know of some more Disk Controllers on the way - I have already mentioned ATOMTRONICS Double-Density, multiple Side/Track system in previous months to name just one. Frank Hogg is advertising FORTH for the Color Computer; Microware is looking into OS-9 on the Color Computer; The Micro Works is working of a version of Ray Talbot's FORTH for this machine; I think you will see a lot more good programs from Computerware, both Tape and Disk; Nelson Software has their SUPER 'COLOR' WRITER Wordprocessor about ready on both Tape and R.S. Disk; etc., etc., etc.

#### COLOR SCRIPSIT -- Radio Shack

Cartridge --- \$39.95

Well, I have finally obtained a copy of the long-awaited Word Processor for the Color Computer from Radio Shack. It is called COLOR SCRIPSIT; named after it's big brother, the SCRIPSIT Word Processor programs for the Model 11 and 111. Being well acquainted with the excellent Model 11 version of this program, I may have been looking for too much, but I am a little disappointed with COLOR SCRIPSIT. To be honest, it IS one of the BETTER "SERIOUS" (not a Game) Programs they have produced for the Computer (along with "COLOR FILE" and "SPECTACULATOR"); and things seem to be looking up (-- RUMOR: -- They have been surprised that the COLOR COMPUTER is being taken SERIOUSLY by a lot of people - I hear they are considering producing some "REAL" Software in the near future.). Also, I'm sure that the use of the standard ROMPACK concept restricted the capabilities of the Program -- Oh, Oh: I see a hand in the back of the Room. Yeh, I know the Cartridge Slot can handle almost 16K of ROM - No; I don't know why it can't be used. Maybe just "We haven't done that before and just didn't think about it"??

So, just what do we have with COLOR SCRIPSIT? The best description is "a mini Word Processor". It is definitely beyond the normal "Line Editor" concept, although it CAN be used for BASIC Programming and Editing. It does not use the Line Number routine; the "Text" is displayed just exactly like it will be Printed on a Line Printer. You can manipulate Text at the Character, Word, or

"Block" level; by Inserting, Deleting, Copying, Moving, Formatting, etc. You can Left Justify (straight Left Column like normal typing), Center Justify, OR Right Justify (straight Right Column); but you can NOT justify BOTH Left AND Right. You can insert "soft" or "hidden" hyphens while typing the Text, or at a later time. You also have the capability of specifying Headers (the TOP of EACH page) and Footers (at the BOTTOM of EACH page); along with automatic Page Numbers. You can Print various portions of the Text, such as a single line, block, or the whole Text. Finally, you have the Find and/or Replace capabilities.

The Program is slightly "Menu Driven" in that it comes up with a Main Menu and has a few other Menus (for instance, the "STANDARDS" Menu). From the Main Menu the operator has the choice of:

1. CLEAR MEMORY
2. EDIT TEXT
3. SAVE ON TAPE
4. LOAD FROM TAPE
5. PRINT
6. CHANGE STANDARDS

Generally, the Program is "user friendly" and the Commands and Controls consistent. The <BREAK> Key is used exactly like a "CONTROL" Key, with the TOP row of keys defining the various functions (which is a weakness - you have to remember that "BREAK 5" is "TAB SET", for instance; "BREAK 'T'" would have been much better). BUT, <BREAK> is ONLY and ALWAYS a control key when Manipulating Text; it also Stops a Memory Clear, Search, etc., when not in Text Edit modes (which is a natural function of the Key for Color Computer users, so not a large deviation from the norm). The four Arrow Keys provide 'non-destructive' cursor control (Backspace is accomplished by "Left Arrow" back one char. and overtyping the mistake), while <SHIFT> Arrows provide jumps (i.e., <SHIFT> Up-Arrow jumps to the beginning of the Text; <SHIFT> Dn-Arrow to the End of Text; and Rt & Lft to the ends of the Line). "Tab" capability is provided, and Format changes are accomplished by inserting a 'Control Character' in place of a SPACE on the Line of Text preceding the Format change (where Format refers to Line Justification ONLY). Hyphen possibilities can be located by the Computer; it looks for a place where at least 3 characters could fit on the end of the previous line, and you can determine where and when to insert one.

Since I haven't had a chance to use the Program much, I hope some of you readers will send in some reports on it. Generally, it sure beats not having anything, at the minimum, and does appear to be a fairly good program overall. I would prefer the "Control Keys" to be meaningful; i.e., "Control 1" for 'Insert', "Control C" for 'Center', etc., but use brings comfortable operation of a GOOD Program. Let's hope this IS one.

#### PROGRAMMING??

A lot of the people I have talked with the last several months are looking into Writing Programs for the Color Computer as a business venture. Of course, most of these people are "pushing the Technology of the Color Computer", so to speak; that is, they are the ones in the forefront of Color Computer Software and Hardware Development,



or are trying to get there. Since I have been shot down with a "busted" machine lately, I thought I would pass on some comments in the "food for thought" vein.

Lately, I have been working with a lot of different Programs; General Ledger, Accounts Receivable, Word Processors, etc. Some of the problems I have run into should help act as a guide for those investigating writing their own Software for the market place.

Many major Programs, especially those in the G.L., A.R., etc. lines which are intended for use by 'data entry' type personnel that are not technically inclined, are "Menu Driven" programs. This allows a Program Operator to start at the beginning of the program with a Main Menu, and select the operation required, such as "Daily Sales", for instance, and proceed to enter the data from Sales Slips, etc. When this is finished, you are brought back to the Main Menu, from which you select the next procedure; etc. This has become a fairly standard practice with Major Accounting type Programs as it DOES allow the use of the program by unskilled personnel; but it also creates some major problems.

The first one concerns the 'data entry'. If the person entering the data is going to be setting there day after day punching a numeric keypad, they are likely to get pretty good at it, right? Yet, I have seen Major Programs that set there PROCESSING THE ENTRY before giving control back to the operator for the next entry. Here an organization has purchased a Computer to speed things up, and an operator has to set there waiting two minuets for EACH data entry to be processed - that really speeds things up. These programs are normally written in Cobol, which is notorious for loading this module, run it, then load this module, run it, etc., till it has found all the files and updated each one, then finally coming back to you again; IF THE PROGRAM IS NOT PROPERLY STRUCTURED.

The point of this discussion: if there is a lot of Data Entry required of the Program, set the Program up to start with so this data is stored in a file as fast as it can be entered by a SKILLFUL Keyboard Operator, and then can be processed at a later time (say at night, when the System is not needed for other things). Careful planning when the Program is initially developed will prevent this type of problem.

Another problem with the "Menu Driven" Programs shows up when something out of the ordinary comes up; either by accident (wrong entry, etc.), or from the users requirements. With most "Menu Driven" programs, the USER is LOCKED INTO A SPECIFIC PROCEDURE, whether he wants to go that way or not. Agreed, the Program must be 'SAFE' from unskilled operators; BUT PROVIDE A KNOWLEDGEABLE USER A METHOD TO BACK OUT OF A PROCEDURE WHEN THE REQUIREMENT EXISTS. Provide a way SOMEONE can salvage an error.

A couple of examples:

We have been running a small Accounting Package the last several days which FORCES you to make BACKUPS EVERY TIME YOU BRING UP THE SYSTEM. Normally, this would probably be a good idea; it makes sure you have saved

the past 'data entry' session and in normal, routine use, is a good procedure. BUT, this was an "initialization" period, where there is a lot of data to be entered to get things up to the present, and it is next to impossible to do this at one sitting. Well, WE SURE MADE A LOT OF BACKUPS, to the tune of several minutes each time. If there was a procedure provided to circumvent the "MAIN MENU" routine, for the knowledgeable user, it would sure have saved us a lot of time. The procedure does not need to be available to the "normal" user; BUT THERE SHOULD BE A WAY to 'dig your own grave'.

Next example:

We had a user setting up a Major Accounting Program; Accts. Rec., Gen. Ledger, Accts. Payable, Inventory Control, etc. Again, while initializing the system, they hit the wrong key and bad information was 'posted'. BIG TROUBLE!! There was NO WAY to either repel the mistake (yes, I know you have to have 'audit tracking'), or TO ENTER ANY MORE NEW "INITIALIZATION" INFORMATION: the system was set up so that the initial information was entered first, and then the "posting" would finalize things. WELL, IT SURE DID! Three weeks worth of work down the tubes.

By the way; NONE OF THESE PROGRAMS WERE ADVERTISED IN 68 MICRO JOURNAL.

All I'm saying is to put yourself in the OPERATORS position when you are designing a Program; consider how it will NORMALLY be run, and WHAT STUPID, DUMB, IDIOTIC THING could be done to "BREAK" it. If there is ANY POSSIBLE way, it will happen. The solution to the "posting" problem above would have been FULL WARNING that continuing this procedure meant that you were finished with "initialization" and ready to LOOK THE DOOR to any CHANGES, ADDITIONS, etc. This concept should be followed for any type of Program; remember Murphy's Law No. 1. Often, we are so involved with a Program, we forget that WE are not going to be using it, and that the "Operator" or "User" is not as familiar with it as we are.

Next month we will look at some ideas on "STANDARDIZATION" for the Color Computer so that a Program Writer will have SOME HOPE of his program running on the majority of it's Operating Systems.



32K RAM FOR FREE!!!

By Frank Hogg

"How to run Pascal, C, and Cobol, not to mention X-FORTH, and Spelltest, on the TRS-80 COLOR COMPUTER"

Someday, as the Honeywell advertisement would say, integrated circuit processing will become so inexpensive that computer memory will be available for FREE.

That day is today, for owners of the Radio Shack TRS-80 COLOR COMPUTER.

The story begins with my early production model (with a 3-digit serial number) of the 4k COLOR COMPUTER. Its logic board had some extra wires and things on it, indicating that the design was not quite perfected when it was produced. I heard that Radio Shack would



replace the board with a newer version if I purchased their 32k ram upgrade for \$149.00, so I decided to give it a try.

When I took the computer to the local computer center, I was told that the upgrade would only cost \$99.00. They did complete the upgrade, and indeed they did install a new logic board, containing eight memory chips with unrecognizable part numbers on them.

Various rumors have been circulating about how the 32k upgrade is accomplished. It is not done by piggybacking 16k rams! Neither is it done by installing 32k rams, as Radio Shack contends.

The 32k dynamic ram was only available for a short time. These parts were actually attempts at 64k parts that were only half-good, or they had some bad bits in one half or the other. The 32k upgrade was originally designed to take advantage of these parts - a jumper exists on revision E of the COLOR COMPUTER circuit board to select which half of the 64k dynamic ram is accessed.

Since then, memory manufacturers have learned how to produce 64k chips with sufficient yield to drive the cost lower than you or I, or Radio Shack, expected. These chips are available by mail order, in small quantities, for less than \$12.00 each. Radio Shack can certainly buy them in quantity at a lower price.

The astute reader will have guessed the punch line by now. The 32k COLOR COMPUTER actually contains 64k rams! I am not in a position to guarantee this, of course, but so far it seems to be the case. I will now tell you how the "other 32k" might be useful to you.

#### USING THE FULL 64K RAM.

None of the versions of Radio Shack Color Basic know how to use the other 32k. As a matter of fact, this memory is not available to the cpu at all in an unmodified COLOR COMPUTER. This is due to an easily correctable omission in the design of the computer.

The dynamic memory in the COLOR COMPUTER is controlled by a chip known as the SAM, or synchronous address multiplexer. The SAM bears the Motorola part number 6883, or 74LS783. The SAM takes care of refreshing the rams and interlaces the access cycles of the cpu and the video display so that no "specks" occur on the screen. The SAM must be programmed differently for 4k, and 16k, and 64k rams. (this is why Color Basic 1.1 was written - version 1.0 didn't know about 64ks.) The SAM also provides address decoding for the three rams, as well as the I/O hardware.

As the SAM was being designed, Motorola considered the possibility that it might be useful in systems which did not use ram, but might want to use 64k of ram (minus 256 bytes for I/O, etc.) For this reason, the selection of ram in the SAM is programmable. If you whisper the right thing to the SAM (POKE &HFFDF, anything), the rams will go away, at least in theory, leaving behind nearly 32k of clean, untouched ram.

Well, we need a more sophisticated theory, because it doesn't quite work. The SAM will still try to select the rams if the cpu writes to those addresses, regardless of how it is programmed. I guess Motorola must have thought that this decoding might be used for something - clearly it wouldn't hurt, since the system designer would have to provide logic to prevent the rams from being turned on in a write cycle anyway. (the rams are "selected" for write purposes all the time.)

Radio Shack, on the other hand, didn't see things the same way; they figured they would avoid writing to that area, so no problems would result. As a matter of fact, the first thing Color Basic does (after programming the SAM) is to test the memory from zero until it finds a byte that won't write. When this test hits address &H8000, the cpu tries to write the rams with exactly the opposite data they contain, and at the same time the rams are reading - resulting in two different chips trying to put different data onto the same bus at the same time.

The real tragedy is that a few unused NOR gates exist on the COLOR COMPUTER circuit board. You only

need one of these to solve this problem. (Radio Shack designers - take note.)

#### THE MODIFICATION IS REVERSIBLE.

One of the extra NOR gates must be connected into the circuit as shown in figure 1. This modification disables the selection circuitry (G2B high) if a write is attempted (r/w low) and a ram is addressed (r/w low). If you have some experience with fine soldering, you can accomplish this modification in a reversible fashion, allowing you to run to Radio Shack if your COLOR COMPUTER breaks. Warning - you must remove that nasty sticker on the back, thus voiding your warranty (if you're still covered), to get inside.

The procedure is as follows. Remove the case and the top of the rf shield. On the right behind the keyboard, you should be able to find the IC's and TPI as shown in figure 2. They are also marked on the board. U11 is a 74LS138, and U29 is a 74LS02.

You may wish to obtain a new 74LS138 and a 74LS02, so you can save the "originals" for a rainy day. In reality, Radio Shack probably doesn't remember what brand of IC it put in your computer, but precautions are cheap. Anyway, carefully remove those two IC's. (they are not especially sensitive to static.) Bend pins 4, 5 and 6 of the 74LS02 up in the air, as shown in figure 3. They must be almost straight up so they don't touch the shield. Similarly disfigure pin 5 of the 74LS138. (be gentle!)

Next, using a short piece of 30-gauge wire, connect pin 6 of the 74LS02 to pin 8. Pin 8 must plug back in, so try not to get solder down on the pin. You should tack the wire on the very top of the pin, where it enters the package. If it doesn't come out right, buy another 74LS02 - it costs much less than a new computer.

You can do the rest of your soldering either before or after you plug the chips back in - use your own judgement. Pin 4 of the 74LS02 must be connected to pin 5 of the 74LS138, and pin 5 of the 74LS02 must be connected to TPI. I recommend that you do not solder to TPI. Just use a wire wrap tool to wrap the wire around the pin, so it can be pulled off.

After you have reinstalled the IC's, the wiring should appear as in figure 4. Check carefully for shorts!

At this point, you can turn on the computer and do a "PRINT MEM." If it says the usual number, all is probably well, so put it back together.

#### TESTING YOUR NEW FREE MEMORY.

The extended Color Basic program in listing 1 will test the ram which you have just made available. Save it before you try to run it, because if you mistype one of those data statements, anything can happen. The program will take about a minute to get set up, after which it will print "OK" if your memory is good. If you do have a problem, it will tell you the address and the data read from the ram, compared to what was expected. I would like to hear from you if you do find errors. If the errors occur in only one or two bit positions, they can be fixed with one or two 64k rams, for one or two ten-dollar bills. No big deal.

#### WHAT DO YOU DO WITH IT?

You now own a computer with almost 88k of memory, in a box no bigger than a typewriter. This fact alone may be enough for some of you. However, a large collection of software exists which can now be run on your computer.

The most important item in this collection is the popular FLEX operating system. (FLEX is a trademark of Technical Systems Consultants, Inc.) Frank Hogg Lebs has developed a package which will allow FLEX to be run on the 32k COLOR COMPUTER, with the Radio Shack disc



system, and the modification described above. FLEX will reside in memory at addresses &HC000-&HDfff, as always. addresses 0-&HBFFF will be available for user programs. All FLEX compatible software will run as is without patches or modifications. Addresses &HE000-&HFFF will be available for utility programs. (We are working on an enhanced display package, using hi-res graphics to simulate a 41-by-24 screen. That's better than an apple!)

With FLEX you have a whole cosmos of software available to you. Besides the items mentioned in the subtitle, there are Basic compilers, business programs, adventure games, assemblers and text editors, word processing software, machine-language debug programs, disc system diagnostic packages, and too much more to mention. FLEX is an excellent system which is widely supported.

If this isn't enough, we are planning to have the powerful OS-9 system on the Color Computer in the same way by early summer. Besides supporting several high level languages like Basic09, Pascal, C, and Cobol, OS-9 is Multi-User, Multi-Tasking, and Multi programming as well. The CC and OS-9 will be more powerful than all of the other TRS-80's put together!

#### SUMMARY

The 32k upgrade of the Radio Shack TRS-80 COLOR COMPUTER is accomplished by installing 64k dynamic ram chips. With a simple, reversible modification, nearly all 64k of this ram can be utilized. A package has been developed which will allow the FLEX operating system to be run on the modified 32k TRS-80CC with disc. You can do a lot of stuff with that.

In addition, the OS-9 operating system is being installed now and when finished (early summer '82) will allow the use of Radio Shack disk software, FLEX software, and OS-9 software on the same computer by merely changing Disks. No other computer made has those capabilities!

This article was prepared, using a preliminary version of the FLEX package, on a COLOR COMPUTER.

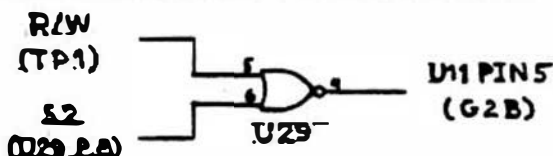


Figure 1: Modification to TRS80CC for 64K RAM.

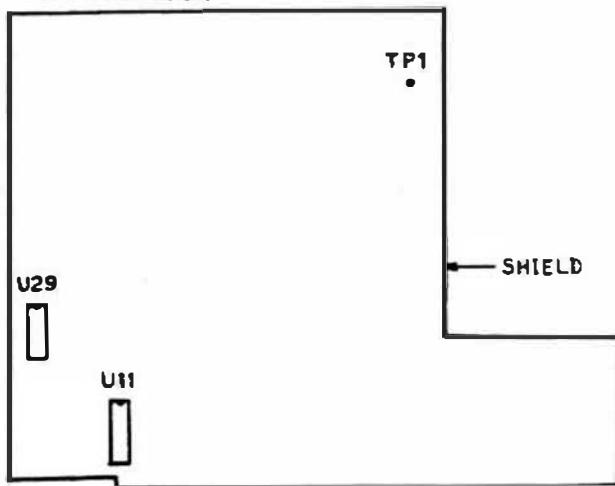


FIGURE 2. LOCATION OF COMPONENTS

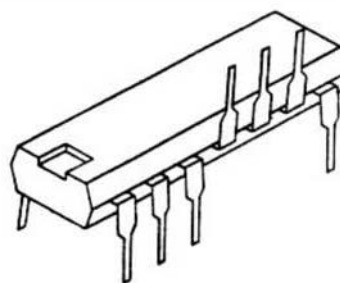


FIGURE 3. MODIFIED 74LS02 PACKAGE

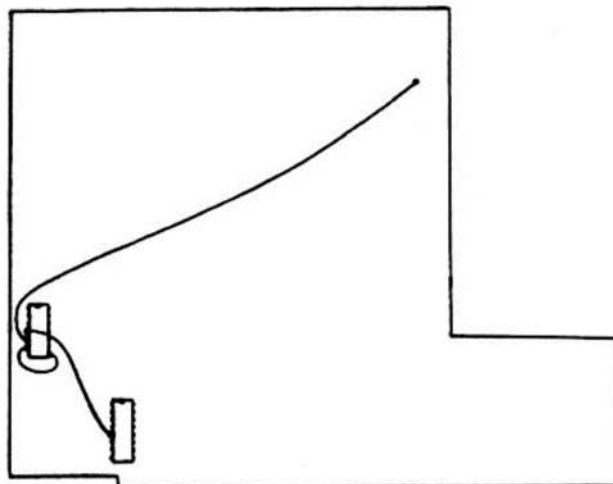


FIGURE 4. INSTALLED MODIFICATION

PACKETMAN  
by Greg Zumwalt

Packetman resembles another arcade game of a fairly similar name. It consists of (yes, you guessed it) a maze with three munchkins and a man (creature?) which the player controls with the right joystick. The player makes little noises as he eats the dots throughout the maze. The three munchkins give you a few seconds, and then chase after you. They are fairly slow at first, and not too smart, but after half a minute or so they speed up and get smarter. It is very wise to get as many dots out of the way before they speed up!

This game is fantastic. My family have almost entirely taken over my computer in playing it! The game is more addictive than any I've played on the CC, and unlike the others I have tried, the excitement doesn't seem to wear out. The graphics are very clear and rapid, and all in all this is the best game I have seen yet for the CC.

First time Packetman users may be disappointed in one difference from the arcade - Packetman does not have little blue dots that enable one to reverse and eat the munchkins.

The other unfortunate part of the game is that the subrouting for your man being slowly eaten and the pieces falling through the screen is missing. If you can live with these shortcomings, you will love Packetman. I recommend it very highly.

Packetman by Greg Zumwalt. \$24.95.

Reviewed by Bill Bruck

Is there anyone out there who could answer a few questions about the Color computer?

1. How about a home ad for 64k? It's advertised from Spectral Associates for \$300, and others have ads for this, but can it be done at home for cheaper?

2. What is the radio shack ad for grounding the CC to prevent screen glitches when using the RS disk system?

3. Anyone in the Washington, D.C. area interested in a CC users group?

Bill Bruck (703) 734-9291



```

100 ' THIS PROGRAM TESTS THE
110 ' MEMORY CHIPS IN A MODIFIED
120 ' 32K TRS80 COLOR COMPUTER
130 ' FOR FUNCTIONALITY. THE
140 ' TEST DETERMINES WHETHER
150 ' FULL 64K RAM CHIPS WERE
160 ' USED IN THE RAM UPGRADE.
170 '
180 ' REQUIRES EXTENDED COLOR
190 ' BASIC.
200 '
210 ' REQUIRES ABOUT 65 SECONDS
220 ' TO SET UP THE MACHINE
230 ' LANGUAGE PROGRAM BEFORE
240 ' RUNNING.
250 '
260 ' RESERVE RAM
270 '
280 CLEAR 256,103670
290 '
300 ' SET UP MACHINE LANGUAGE
310 ' PROGRAM
320 '
330 GOSUB 1660 : SA=H
340 GOSUB 1660 : EA=H
350 GOSUB 1660 : E0=H
360 GOSUB 1660 : E1=H
370 '
380 ' SA=START ADDRESS
390 ' EA=END ADDRESS
400 ' E0=ENTRY POINT 0
410 ' E1=ENTRY POINT 1
420 '
430 FOR A=SA TO EA
440 GOSUB 1660
450 POKE A,H
460 NEXT A
470 '
480 DEFUSR0=E0
490 DEFUSR1=E1
500 '
510 ' TEST THE MEMORY
520 '
530 X=USR0(0)
540 '
550 ' CHECK FAILURE ADDRESS
560 '
570 FA=PEEK(103670)*256
    +PEEK(103670)
580 '
590 IF FA=0 THEN 760 : ' END TEST
600 '
610 ' REPORT FAILURE
620 '
630 DW=PEEK(103670) : ' DATA WRIT
640 DR=PEEK(103670) : ' DATA READ
650 '
660 PRINT "AT ADDRESS ";HEX$(FA)
670 PRINT "WROTE ";HEX$(DW);
    " "; READ ";HEX$(DR)

```

```

680 '
690 ' RESUME TEST
700 '
710 X=USR1(0)
720 '
730 GOTO 550
740 '
750 '
760 ' END OF TEST
770 '
780 END
790 '
800 ' MACHINE LANGUAGE PROGRAM
810 '
820 DATA 4000,4054
830 DATA 4000,4046
840 '
850 ' THE MACHINE LANGUAGE
860 ' MEMORY TEST PROGRAM IS AS
870 ' FOLLOWS:
880 '
890 ' E0 ORCC 0150 DIS. INTS.
900 ' STA 0FFDF MAP TYPE 1
910 ' LOX 004100
920 ' P1 CLR ,X+ CLEAR TO ZERO
930 ' CMPX 00FF00
940 ' BNE P1
950 ' CLR 03FFE DW
960 ' LDX 004100
970 ' P2 LDA ,X
980 ' BNE RE REPORT ERROR
990 ' P2A LDA 00FF
1000 ' STA ,X+
1010 ' CMPX 00FF00
1020 ' BNE P2
1030 ' COM 03FFE DW
1040 ' LDX 004100
1050 ' P3 LDA ,X
1060 ' CMPA 00FF
1070 ' BNE RE REPORT ERROR
1080 ' P3A LDA 0000
1090 ' STA ,X+
1100 ' CMPX 00FF00
1110 ' BNE P3
1120 ' LDX 000000 END TEST
1130 ' RE STX 03FFC FA
1140 ' STA 03FFF DR
1150 ' STA 0FFDE MAP TYPE 0
1160 ' ANDCC 00AF ENBL INTS.
1170 ' RTS
1180 '
1190 ' RESUME TESTING
1200 '
1210 ' E1 ORCC 0150 DIS. INTS.
1220 ' STA 0FFDF MAP TYPE 1
1230 ' LDX 03FFC FA
1240 ' TST 03FFE DW
1250 ' BEQ P2A
1260 ' BRA P3A

```

```

1270 '
1280 ' ACTUAL CODE
1290 '
1300 DATA 1A,50
1310 DATA B7,FF,DF
1320 DATA BE,41,00
1330 DATA 6F,80
1340 DATA 8C,FF,00
1350 DATA 26,F9
1360 DATA 7F,3F,FE
1370 DATA BE,41,00
1380 DATA A6,84
1390 DATA 26,21
1400 DATA 86,FF
1410 DATA A7,80
1420 DATA 8C,FF,00
1430 DATA 26,F3
1440 DATA 73,3F,FE
1450 DATA BE,41,00
1460 DATA A6,84
1470 DATA 81,FF
1480 DATA 26,0C
1490 DATA 86,00
1500 DATA A7,80
1510 DATA 8C,FF,00
1520 DATA 26,F1
1530 DATA BE,00,00
1540 DATA BF,3F,FC
1550 DATA 87,3F,FF
1560 DATA 87,FF,DE
1570 DATA 1C,AF
1580 DATA 39
1590 '
1600 DATA 1A,50
1610 DATA B7,FF,DF
1620 DATA BE,3F,FC
1630 DATA 7D,3F,FE
1640 DATA 27,C6
1650 DATA 20,D9
1660 '
1670 ' READ A HEX NUMBER TO H
1680 '
1690 READ A$
1700 LZ=LEN(A$)
1710 H=0
1720 IF LZ<=0 THEN RETURN
1730 C$=LEFT$(A$,1)
1740 FOR I=0 TO 15
1750 IF (I=0)AND(C$="0")
    THEN 1790
1760 IF C$=HEX$(I) THEN 1790
1770 NEXT I
1780 RETURN
1790 H=H*16+I:LZ=LZ-1
1800 A$=RIGHT$(A$,LZ)
1810 GOTO 1730

```



## Editor's Note

It has always been the policy of 68 Micro Journal and its sister organizations to follow a strict rule concerning advertising and articles. In light of the advertising carried in 68 Micro Journal by Data-Comp Division and S.E. Media Supply, who are associated with Computer Publishing Inc. (CPI), who also owns 68 Micro Journal, the following is offered.

It is a policy of CPI's 68 Micro Journal that the interest of the reader is the primary consideration. This means simply that we want our readers to know as much as there is to know about the Standard S50 Bus, the 6809, 6800 and any computer that use these devices, period!

As the above article indicates and also as indicated in the FRANK HOGG LABORATORY, INC advertising in this issue, there are now two ways to run FLEX™ and other 6809 software on the color computer. There is a difference in the approach addressed by these two, also there will be others later that will use even other approaches. It is our desire that our readers know about everything available and have enough knowledge to make a rational and wise choice for his or her particular use.

Originally I allowed Data-Comp and S.E. Media to proceed with a FLEX™ program for the Color Computer as devised by Steve Odneal. I made this known to all major advertisers who had indicated, to me, an interest in the Color Computer. My desire was to expose to as many as I could the advantages of all the excellent software and disk systems we have for the 6809. This would bring in more 6809 users and hopefully more business for our advertisers, as well as helping 68 Micro Journal grow. So far this has been accomplishing what we set out to do. Now that there is more than one way to do it, I feel that an attempt to explain the differences between them is in order, as best we know so far, and then you can decide which is best for YOU.

First, because of my close association with Data-Comp and S.E. Media (I own stock) I know that it may seem that I might want to 'hype' my product over the others, well for the record I can assure you that my most pressing concern is for the well being of the 6809 community, our readers and advertisers as well as 68 Micro Journal. As has been the policy in the past I will not allow Data-Comp or S.E. Media to go head up against another product already being advertised. But once we get into it, because someone else just was not prepared or did not let me know, there is not much I can do, after all if no one else wants to do it for our readers, by golly we will try! To me our readers are the most important aspect, above all, including advertisers. And you advertisers should consider that without readers, who will read your ads? Just remember back a few years!

The FRANK HOGG approach is a good one for the reader who has the skill to modify and maintain his own color computer. It has the advantage of running software that will not have to be patched. However, except for FLEX™ and a few other software products the Data-Comp/S.E. Media system does also. Both promise additional utilities and other software goodies.

There are several considerations beyond the above that need to be addressed. As Frank outlines above, one is the skill to accomplish the HOGG modification without damage to the computer or finding out at 'failure time' that you must repair your own. Radio Shack informs me that they will not repair a modified Color Computer. Of course you may be able to perform the modifications so neatly that they can be removed and not detected by the repair agency, if so you have removed one very important consideration. Also if your computer actually has the 64K RAM chips, and they are all o.k., chalk up another plus. However should Radio Shack refuse to repair your

computer because of unauthorized modifications you could be faced with a costly disaster. I do not doubt for one minute that many of our readers can utilize this method without any problems, I just feel that you should be aware.

The Data-Comp/S.E. Media scheme takes a somewhat different approach, although the end results are about the same, both from a cost and operational view point. When we first looked into this we decided against a system similar to the HOGG approach, mainly due to the fact that many Color Computer users do not want to be patching their computer, both from a standpoint of warranty and 'maybe someday need a repair'. A call from Frank Hogg confirms that they are working on a board to be plugged into an IC socket and will require no cutting or soldering. For those who do not mind removing a few screws, this will be fine. We opted, as Steve Odneal has done to relocate some of the software to allow the color computer to operate in its normal manner. Use the Radio Shack tape, disk and game-pak as originally set up by RS and also the Exatron way, or a mix of them all. Once the software patches are done everything works normal, and the computer has not been tampered with.

We have not had a chance to evaluate the Hogg system but if it is like their other software it will be another fine product. As for the Data-Comp/S.E. Media System we and others have been running it for some four months now and all seems well. So there you have it, a choice between what should be two good systems. Frankly, I really don't care which one you choose; just get going and have FUN

DMW - - -

## "C" User Notes

Norm Commo  
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Natick, MA 01760

Maybe someday, in the great beyond, I'll manage to elude Mr. Murphy. But until then let me apologize for a minor snafu with the first column. Seems that I inadvertently put my Pascal word counting program on the same disk as the column. At that time I didn't have my C library up to snuff and was using the Pascal version to keep tabs on the size of the article. Wouldn't you know that it would get printed along with the column.

I am going to change the game plan a bit. Initially I was planning to present a fairly complete tutorial on the C language. However, responses to the first column indicate that I should stop after this month's session and devote some time to giving hints and tricks for the compilers that are available today. So this month we will go over C's operators and control statements. With the conclusion of this installment we will have covered most of the C language that is available with today's compilers. Incidentally, today means February. You will probably be reading this in April.

One of the nicer features of C is its set of operators. Few languages have as rich a set as C does. These include all sorts of unary, binary, bitwise, relational, and assignment operators. Let's take them one group at a time.

### UNARY OPERATORS

The unary operators require only one operand, which could be an expression, variable or constant. Included in the group is



- two's complement negation
- ~ one's complement negation
- ! logical negation
- ++ incrementation (pre or post)
- decrementation (pre or post)
- \* indirection
- & address of

Most of us have used "~" in other languages. It merely negates the operand in an arithmetic sense. So the statements

```
n = -5;
```

would leave the value of minus 5 in n. The "~" performs a one's, or bit ise, compliment of the number. The statements

```
n = ~0x55;
```

would leave 0xAA in n, assuming n to be a char. If n was an int then 0xFFAA would be the result since 0x55 expands 0x0055.

The "!" is the logical negator. If the operand is zero then it becomes one; if it is nonzero, then it becomes zero. This operator is typically used in control or conditional statements like

```
while (!quit)
if (!cond)
```

which are completely equivalent to

```
while (quit == 0)
if (cond == 0)
```

Neither the logical nor bitwise negators may be used with floats or doubles.

The "++" and "--" operators increment or decrement a variable respectively. They may be applied before or after the variable so that

```
n = ++x;
```

increments x and then assigns n the value of x, whereas

```
n = x++;
```

assigns n the value of x and then increments x. These two operators are often combined with other operators. Some examples would be

```
n = *pointer++;
n = element[++i];
if (i++ == 5)
```

The last example needs some comment. Here, i is compared against 5 and the result is saved for use after i has been incremented. From experience I can tell you that you won't realize how handy these two operators are until you've used them for a while and then find yourself programming in another language that doesn't have them!

The "\*" is the indirection operator. It causes an operand to be used as a pointer. We saw this last month in the discussion of pointer types. Some examples would be

```
n = *arg;
n = *(arg + 5);
n = **arg;
```

The last example is interesting. It says to take what arg points to and use it as a pointer. There is no limit to how many indirections can be applied, the statement

```
n = ****arg;
```

is totally valid as far as the compiler is concerned. It would crank out the appropriate code to execute a fifth order indirection.

In preparing for this installment I saw an example similar to

```
n = ++*arg;
```

If you start from right to left, then it means that what arg points to is incremented and then used as a pointer. I dare say the beginner will be hard put to get much beyond \*\*arg. But when you're ready the power is there.

The "&" is the address operator and means "take the address of the variable". It usually crops up in pointer assignments, or passing an address to a function. Suppose that you wanted to scan the right side of a string. The call to the scanning function could look like

```
scan(&array[14]);
```

which would pass scan() the address of the fifteenth element of array[]. The address operator is not limited to arrays. Many times we have to write some type of sorting routine. The call to the swapping function would pass the addresses of the variables

```
if (a > b)
swap(&a,&b);
```

and the parameters to swap() would be declared as pointers.

```
swap(n1, n2)
int *n1, *n2;
{
int temp;

temp = *n1;
*n1 = *n2;
*n2 = temp;
}
```

## BINARY OPERATORS

The binary operators take two operands and can be broken down into subgroups as shown

* / % + -	arithmetic
>> << & ^	bitwise
< > <= >= == !=	relation
&&	logical
= += -= /= %= *	assignment
*= /= &= >>= <<=	

The arithmetic operators are the same as they are for most other language with the possible exception of % which is the MODULO or remainder operator. 9 modulo 4 implies the remainder when 9 is divided by 4. In this case 1.

The bitwise operators consist of

```
& and
| or
^ exclusive or
>> shift right
<< shift left
```

and may be applied to most of the primary data types except floats and doubles.

The "&", "!", and "\*" are typically used with some sort of



mask for manipulating bits.

```
data = *acia & 0x7F; /* strip off parity */
data = data | 0x01; /* turn on bit 0 */
data = data ^ 0x02; /* flip bit 1 */
```

The shift operators shift the operand on the left by the amount of the operand on the right.

```
n = n >> 4; /* shift right 4 bits */
if ((j << i) <= 0) /* shift left and compare */
```

A left shift zero fills the operand. A right shift may zero fill or one fill depending on the host machine and the compiler. For 68xx users it will probably always be zero fill.

The relational operators work the same as they would in Basic or Pascal except that the symbol for not equal is "!=". The relational operators are used in conditional or control statements like

```
while (i <= 5)
if (foo == 'a')
```

A warning to the neophyte C programmer. Many of your initial programs will run amuck because you used the equate symbol, "=", instead of the equality symbol, "==". When you do that, C executes the equate and uses the result for the compare so

```
if (ascii = NULL)
```

will always be FALSE since ascii is set to NULL.

The operators "&&" and "||" are the logical AND and the logical OR respectively. They are used with conditional and control statements such as

```
if (ch >= 'a' && ch <= 'z')
```

which would evaluate to TRUE if ch was an ascii lower case alpha character. Here is where compilers modeled after "Small C" really fall down. They use the bit wise operators & and | as logical operators also. While, most of us will never have to worry about portability, I still feel this is a very serious shortcoming or oversight. At least Intersoft had the foresight to point this out. They recommend that the operands be parenthesized when using the bit wise operators logically. So the previous example would code as

```
if ((ch >= 'a') & (ch <= 'z'))
```

which should execute properly on most other compilers.

The assignment operators take two operands, with the left operand being the one that is modified. We are all acquainted with the statement

```
x = x + 5;
```

where x is assigned the value of x with 5 added to it. Of course, any valid expression could comprise the right operand. C departs from most other languages in that the previous statement could also have been written as

```
x += 5;
```

which says to add 5 to x. The other assignment operators allow the same sort of thing. Some examples would be

```
x -= y; /* x = x - y */
x >>= 4; /* x = x >> 4 */
```

So far I have avoided discussion of operator precedence. Like all languages there is a ranking of the operators, given in the following table, which is shown in decreasing

order

expression operators group from left to right  
( ) [ ]

unary operators group from right to left  
\* & - ! ~ ++ --

binary operators have decreasing precedence as shown and group from right to left  
\* / %

+ -  
<< >>  
< > <= >=  
== !=  
&

!  
&&  
||  
assignment operators have equal precedence and group from left to right  
= += -= \*= /= %= >>= <<= &= ^= |=

## CONTROL STATEMENTS

The C language has two basic forms of control structures, the conditional and the loop statements. Oh yes, and there is the ubiquitous GOTO.

## CONDITIONAL STATEMENTS

The primary conditional is the "IF-THEN" and the "IF-THEN-ELSE" structure. These have been used in a few of the examples already. The syntax is

```
if (condition)
    statement /* the "then" action */
else
    statement /* the optional "else" action */
```

When the condition evaluates to TRUE, which is any nonzero value, the "then" statement is executed. When the condition evaluates to FALSE, which is zero, either the "else" statement is executed, or control merely passes on to the next statement following the IF-THEN. Remember that if multiple statements are to be executed for either result of the test they must be surrounded by the {} braces.

Note that the condition could be an expression, a variable, a function call, or a comparison between operands using the relational operators. Or it could be some combination of any or all of these linked with the logical operators.

IF's may be nested but some care must be exercised in handling the ELSE. Consider the statement

```
if (condition 1)
    if (condition 2)
        statement;
else
    statement;
```

By the format it appears that the ELSE binds to the first IF. But that's not the case. The standard calls for the ELSE to be associated with the closest "nested" IF. To bind the else to the first IF, we must enclose the second IF and its statement with brackets

```
if (condition 1)
{
    if (condition 2)
        statement;
}
else
    statement;
```



We have now told the compiler in unambiguous terms what we really wanted.

To construct an n-way choice, we may string together IF's as follows

```
if (condition 1)
    statement;
else if (cond 2)
    statement;
else if (cond 3)
    statement;
else
    statement;
```

However, C has a much cleaner construct for multiple choice branching. It is called the SWITCH statement and has the following syntax

```
switch(expression)
{
    case const_1 : statement;
                    break;
    case const_2 : statement;
                    break;
    case const_3 : statement;
                    break;
    default : statement;
}
```

The expression is evaluated, just once, and then compared to the constants (const 1, const 2, etc) associated with each CASE. If the expression matches a CASE, the corresponding statement is executed. If there are no matches, then the optional DEFAULT statement will be executed. If there is no DEFAULT, control passes on to the next statement following the SWITCH. The constants may be any of the legal literal formats except a string.

The limitation of the SWITCH, as compared to the "IF-THEN-ELSE IF" construct is that the expression may be compared only against constants. With the IF's it may be compared against other expressions or variables. But the SWITCH is certainly adequate for a host of everyday problems and has the advantage of readability.

Notice the use of the BREAK following some of the statements. These are required if you do not want control to fall through to the rest of the switch. For certain CASE's you may execute a unique statement by using the BREAK. Or you could execute the statement for a CASE and execute the DEFAULT by leaving out the BREAK. You may also cause more than one CASE to execute the same statement. Consider

```
switch (expression)
{
    case 1 :
    case 2 :
    case 3 : statement;
              break;
    case 4 : statement;
    default : statement;
}
```

Here, CASE's 1, 2 and 3 will execute only one statement. CASE 4 will execute both its own statement and the DEFAULT. The BREAK will be discussed more fully later.

## LOOPING STATEMENTS

C has three looping statements, the WHILE, the DO-WHILE and the FOR.

The WHILE statement is a loop construct where the condition is tested at the "top" of the loop.

```
while (condition)
```

```
statement;
```

As long as condition evaluates TRUE, the statement gets executed. If it evaluates FALSE, then the loop is exited, or perhaps never even entered if the condition is FALSE the first time through. A WHILE might be used to print a string as in

```
puts(stg)
char *stg;
{
    while (*stg)
        putchar(*stg++);
}
```

Each time through the loop, the character pointed to by \*stg is tested. If it is not NULL then it is printed. Remember, FALSE is zero and TRUE is any nonzero value. We might also want to initialize an array to some value

```
init array(nums,size)
{
    int nums[], size;
    int i;

    i = 0;
    while (i < size)
        nums[i++] = MAGIC_NUMBER;
}
```

The DO-WHILE is a loop that tests for a condition at the "bottom". It is usually used where the loop must be executed at least once.

```
do
{
    printf("\nEnter a number from 1 to 10");
    getch(char(num);
}
while (num < 1 && num > 10);
```

The prompt will get issued at least once since the test for a good entry is at the bottom of the loop. If the entry is valid, the loop is exited.

The FOR loop in C is really unique. It has the syntax

```
for (exp 1; exp 2; exp 3)
    statement;
```

which is equivalent to

```
exp 1;
while (exp 2);
{
    statement;
    exp 3;
}
```

What makes it special is that exp 1 is an initializer, exp 2 is a condition, exp 3 is some action that occurs at the bottom of the loop and they are totally independent of each other. Also, any given part or parts may be left out. In fact, in the "The C Programming Language", the statement

```
for (;;)
    statement;
```

is given as an example for doing something forever, since the lack exp 2 causes a TRUE to be assumed. Let's rewrite init\_array() with the FOR loop.

```
init array(nums,size)
{
    int nums[], size;
    int i;
```



As another example, lets write a function to copy one string onto the tail of another

Here, `i` is initialized to the length of `s1` by the call to `slen()`. This example shows that the `exp 1` and `exp 3` may each be a list of actions. The conditional may be any legal combination of relational and logical operators just like a `while` or an `if` statement.

The GOTO. I've never had to use one but there a few rare cases where it is useful, usually to recover from some catastrophic chain of events.

GOTO labels have the same syntax as a variable name and are followed by the colon. They must appear in the same function as the call to them. The GOTO was needed here because a BREAK would have only exited from the innermost loop. At present, neither Dugger nor Intersort implement the GOTO.

These Small C derivatives have their problems. By and large they make an effort to stick with the standard. But they fail here and there. The problem is that the "here and there" can really trip up the neophyte C

Speaking of "Small C" compilers, there is another one on the block. It is offered by Word's Worth of Dallas, in the mid \$50 price class. The ad will probably have come out in the February issue. They have done something very interesting. The compiler was designed to work in conjunction with RLOAD. It gives you the ability to assembler source files into binary object modules and then link them with RLOAD. They have also included what is called a "peep-hole" optimizer with the compiler. This is a program that looks for, and eliminates, a lot of superfluous code from the output file created by the compiler.

Next month we will go over some of the "standard" functions that most compiler packages come with and how to make life easier when using compilers designed for absolute assemblers.

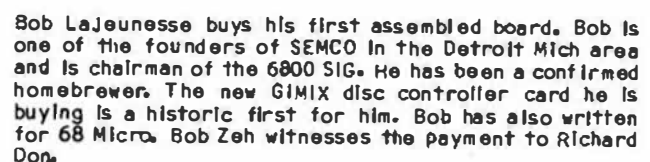
```

translate(s)
    char *atq; // pointer to the Spanish s/
    {
        int i;
        static char number[2][5] =
            {"Uno", "Dos", "Tres", "Cuatro", "Cinco"},
            {"One", "Two", "Three", "Four", "Five"};

        for (i = 0; i < 5; i++)
            if (match(atq, number[i][0]))
                return(number[i][1]);

        return(0); // failed -- no match 0/
    }

```





# Flex Disk Format

by Leo Taylor

Leo Taylor is Editor of the Connecticut Computer Club newsletter. He has written material which has recently appeared in the 11/81 680X User Notes column. Leo has submitted the following article to '68 Micro Journal'. It was originally written in October 1980 and was updated in December 1981.

I would like to clear the air as to how Technical Systems Consultants formats a FLEX disk. I suspect few readers of '68 JOURNAL' know what order the sectors are in and how this affects the speed of the operating system. FLEX users have questioned why FLEX-1 with DMA isn't a lot faster than FLEX-2 with accumulator I/O. In the Sept 80 issue of '68 JOURNAL' Ray Talbot observes that speeding up the step rate did not speed up the loading of a program. Imagine my shock when I first initialized an 8 INCH drive with my F&D controller and found it was slower than the 5 inch drive I was using. This shock proved very enlightening since it prompted me to write my own initializing program, which in turn forced me to learn the answer to this speed paradox.

The File Management System in FLEX is very elegant, very versatile, and very SLOW. First I'll explain the advantages of the FMS, then I'll relate this to speed. TSC has provided FLEX with an FMS that is easy to use which has encouraged the many assembly language programmers that we find in '68 JOURNAL'. Other companies have written operating systems that are faster but harder for the programmer to access. For example, the RT-11 system used on the DEC LSI-11 requires much more user program to access a disk. To read through a file the user commands RT-11 to read a block into a buffer. The user is then responsible for maintaining a buffer pointer, checking for end of file, and testing for end of block. At the end of each block the user must request RT-11 to read another sector. FLEX reads through a file with only a JSR FMS which returns a character in the accumulator. The FMS maintains the pointer, checks for end of file, and automatically reads the next sector when the current sector is depleted.

All this elegance takes its price in speed. For each character in RT-11 the user loads from the buffer, stores where desired and checks a few conditions. This may take about 50 microseconds per character. FLEX requires less user code to call FMS but the subroutine being called takes a lot longer. FMS must check the type of command, legality of command, juggle some pointers, expand TABS, etc. A rough count of a typical call is 140 machine cycles. Add a dozen or so cycles for the user's program and multiply by 256 bytes per sector and add 10 msec for head load and you can see that it takes about 50 msec to read a sector using FLEX at 1MHz. This is several times longer than it takes for a sector to pass under the head. Sectors go by in 18 msec for 5 inch and 9 msec for 8 inch single density. Thus FLEX skips over a number of sectors while processing the current sector. Since the processing time is constant, FLEX must skip twice as many sectors for 8 vs 5 inch. Thus the speed of reading a FLEX-1 disk on my system is only slightly greater than a FLEX-2 disk.

Now how does FLEX account for this skipping of sectors? Going back to RT-11 the processing time between sectors is less than one sector so RT-11 can read every other sector. The operating system handles this interleaving of sectors in software. In FLEX you may know that we usually read sectors in order. This is much simpler for the user to handle. Fortunately the physical order of the sectors on the disk is intentionally scrambled. Otherwise as we process a sector one sector two would pass by and we would have to wait for the disk to rotate all the way around before sector two is again available. We now wonder how many sectors

will pass by before we read sector two. The FLEX manual did not elaborate on the interleaving other than acknowledging that it exists, so I set up a scope and test program to find out. I found a FLEX-2 disk to be sectored 135792468A. This was later confirmed by disassembling NEWDISK.CMD. You should notice that there are four physical sectors between any numerically adjacent sectors. This allows plenty of slack over the 50 msec we calculated. Testing an 8 inch disk initialized by the F&D INITI.CMD revealed a surprise! It was formatted sequentially which is why it was slower than the 5 inch disk. Surely a new format was in order. I thought of creating my own sector order but decided to copy FLEX-1. I borrowed a disk that was formatted by a DMA controller and analyzed it with a scope. It was sectored 16B38D5AF27C49E. Notice it skips 8-9 physical sectors between numerically adjacent sectors. The fact that the controller used DMA made no difference which kills the claim that DMA is much faster than accumulator I/O. In some systems it would be but not with FLEX.

If FLEX seems slow in reading compared to RT-11 the write speed fares even worse. The LSI-11 writes about as fast as it reads. FLEX requires three passes over a sector when writing! this limits the write speed to 400-500 bytes per second. My digital cassette goes faster than that! Much to my amazement turning off verify (not recommended) makes only a small improvement. For FLEX to write a sector it first has to read it to find the link to the next sector. This link is merged in with the new data and the sector is written on the second pass. Usually there is a third pass for the verification of the data. This can not be improved with reformatting, faster processing, or DMA.

So what does all this mean? FLEX is not I/O bound when reading a disk so changes in hardware make only a minor change in speed. When Mr. Talbot changed the step speed it was a drop in the bucket compared to the processing time. Not that his change was not worthwhile, when a lot of seeks are involved (such as his EXEC) the seek speed is significant. Can you speed up FLEX? Only by increasing CPU speed. Since I'm using a 68B00 I can reorder the sectors to take advantage of my reduced processing time. But the disk would be very slow (but fully compatible) on a 6800 system. One can save 10 msec per sector by not loading the head when it is already down but this requires new disk drivers. I found a 'trick' to eliminate the head load delay that enabled me to speed up FLEX noticeably.

Anyone interested in the source of my disk formatting program can have a copy for free by sending me two initialized FLEX-2 disks or single sided 8 inch disk in a self address stamped container. The program is very fast (25 sec for 5 inch and 45 sec for 8 inch). The program demonstrates that the hardware can read and verify a disk far faster than TSC allows for. Currently the program supports SWTP MF-68, F&D MDI-1, and DMAF-1. The program is table driven so the sectors can be arranged in many ways. Included are tables for IBM disks and TRS-80 disks to mention a few.

My INIT.CMD is in use by several friends and I haven't had a bug report in a long time. A few users have asked "How do you do it so FAST"? All I can say is I do it with mirrors! I write the disk using a sector table at 2 revolutions per track (same as anyone else). I use this sector order 'image' to verify the tracks in one revolution. Also I use track-to-track skew and verify from the inside out to reduce the overhead from seek time resulting in a total format time of 3+ revolutions per track.

I hope I have cleared up some misconceptions about the speed vs. flexibility of the FLEX operating systems. Though FLEX may be a little slow reading from a disk I wouldn't trade it for any other DOS around!



```

NAR UNIVERSAL DISK FORMATTER
OPT PAG
PAG
UNIVERSAL DISK FORMATTER BY LED TAYLOR
UTILITY FOR INITIALIZING 5 AND 8 INCH SOFT
SECTOR DISKS WITH 701771 TYPE CONTROLLERS.
PROGRAM CAN FORMAT 4800 OR 4809 DISKS FOR
DMF-1 OR MF48 TYPE CONTROLLERS USING ANY
OF THOSE FOUR SYSTEM COMBINATIONS.
FORMATS DISKS USING TABLES FOR SECTOR SIZE,
SECTORS PER TRACK, AND TRACKS PER SIDE. CAN
BE USED WITH FLEX-1, FLEX-2, OR FLEX-9. ALSO
CAN FORMAT AN IBM OR RADIO SHACK DISK. IF NO
PARAMETERS ARE GIVEN ON THE COMMAND LINE THE
PROGRAM WILL PROMPT FOR ALL DATA NEEDED. THE
PROMPTING CAN BE REDUCED TO THE DISK NAME AND
NUMBER IF THE DRIVE IS INCLUDED ON THE COMMAND
LINE. THE PROGRAM WILL DEFAULT THE OPTION BY
USING A DRIVE DEFAULT TABLE FOR LACK OF USE
WITH A MIXED DRIVE SYSTEM. YOU CAN OVERRIDE
THE DEFAULT BY ADDING AN OPTION ALONG WITH
THE DRIVE ON THE COMMAND LINE. THIS PROGRAM
CONFORMS TO SEVERAL FLEX SECTOR ORDERING
PATTERNS OR ANY USER DESIRED SECTOR ORDER
AND INITIALIZES VERY QUICKLY. REPORTS SOFT
ERRORS WHICH MAY BE MORE FREQUENT THAN YOU
EXPECT. MULTIPLE RETRIES AFTER SOFT ERRORS.
REPORTS PROGRESS AFTER EACH PROGRAM MODULE.
ALL NUMBERS ARE IN HEX EXCEPT VOLUME NUMBER
(4535 MAX) AND TOTAL AVAILABLE SECTORS. IF
THE USER DESIRES THERE ARE PROVISIONS FOR AN
EXTENDED LENGTH FLEX CATALOG USING TRACK ONE.
REQUIRES ABOUT 16K OF RAM STARTING AT 420 BUT
MAY BE ASSEMBLED ELSEWHERE. SELECTING A DISK
OPTION THAT DOESN'T MAKE SENSE (SUCH AS IBM
FORMAT ON A 5 INCH DISK) IS ILL ADVISED. YOU
SHOULD SET UP THE DRIVE OPTION DEFAULT TABLE
(FOUR CHARACTERS AT ADDRESS 00105) TO MATCH
A YOUR SYSTEM BEFORE ASSEMBLING. THE NEXT FOUR
BYTES AFTER THE DEFAULT TABLE ARE:
DMF1LG USED FOR DMF-1 CONTROLLER.
USEMFLC USED IF DRIVES 0 AND 1 ARE DMF-1 AND
DRIVES 2 AND 3 ARE MF48 AND USEMFLC.CMD
HAS BEEN LOADED.
EXTFLG USED ONLY IF AN EXTENDED DIRECTORY IS
DESIRED MOST OF THE TIME.
FORTYF USED FOR DRIVES SUCH AS MPI THAT ALLOW
FORTY TRACK OPERATION.
A FEW EXAMPLES OF DEFAULT TABLE SETUP:
FOR A FLEX-2 SYSTEM WITH MF48 CONTROLLER AND
1 MHZ CLOCK USE 'N','M','M','M,00,00,00,00.
FOR A FLEX-9 SYSTEM WITH DOUBLE SIDED DMF-1
AND MF48 AND 1 MHZ USE 'B','B','M','M,01,01,00,00.
FOR A FLEX-2 SYSTEM WITH F40 5 AND 8 INCH AND
2 MHZ CLOCK USE 'G','G','O','O,00,00,00,00.
A BOOTSTRAP IS SELECTED FROM FOUR BOOTSTRAP
PROGRAMS FOR EITHER PROCESSOR AND CONTROLLER.
THE DEFAULT BOOT IS THE ONE FOR THE SYSTEM THE
DISK IS BEING INITIALIZED ON.
<EXAMPLE OF USE WITH USER INPUT BRACKETED>
COMMAND LINE FORMAT:
[INIT] PROMPTS FOR DRIVE AND OPTIONS
[INIT B] DRIVE 0 DEFAULT OPTION
[INIT 2 A] DRIVE 2 A=8 IN SINGLE SIDE DMF
<OPTIONAL PROMPTS FOR PARAMETERS>
LED'S UNIVERSAL DISK FORMATTER
DRIVE NUMBER TO INITIALIZE (0-3) [1]
0=4800 8=RADIO SHACK
0=4809 8=STANDARD IBM
DIS TO BE USED WITH 10,9,8,01 [9]
      8 INCH      5 INCH
      DMF MF48 DMF MF48
SINGLE SIDED NORMAL A C I M
DOUBLE SIDED NORMAL B F J N
SINGLE SIDED FAST C G K O
DOUBLE SIDED FAST D H L P
ENTER DESIRED OPTION CHAR. (A-P) [M]
THIRTY FIVE OR FORTY TRACK (T,F) [T]
NORMAL OR OVERSIZE CATALOG (N,O) [M]
<REQUIRED FROM TS FOR NAME>
NAME FOR DISK IN DRIVE 1
OR PRESS RETURN TO SORT [MAX5CHAR]
VOLUME NUMBER (12345)
TRACK ZERO VERIFIED
ALL TRACKS WRITTEN
FORMAT FINISHED
AVAILABLE SECTORS: 340
ERROR MESSAGES:
DISK IS PROTECTED (INSERT UNPROTECTED DISK)
SOFT ERROR AT TT 88 (RETRIED IF FLEX FORMAT)
HARD ERROR AT TT 88 (FLEX SECTORS DELETED)
FORMAT ABORTED (FATAL ERROR OCCURRED)

```

```

PAC
STANDARD FLEX ADDRESS ASSIGNMENTS
ONLY THREE EQUATES NEED TO BE CHANGED FOR
4809) THE START OF FLEX AND TH ADDRESSSES
OF THE DISK CONTROLLERS.
FLEX EQU 04800 USE 0C000 FOR 4809
MF48 EQU 08010 USE 0E010 FOR 4809
DMF1 EQU 09400 USE 0F000 FOR 4809
FCB EQU FLEX+0040 FLEX FILE CONTROL BLOCK
PAUSE EQU FLEX+0C09 TTYSET PAUSE
WASH EQU FLEX+0C0C WORK DRIVE ASSIGNED
FLXDAT EQU FLEX+0C0E FLEX DATE REGISTERS
LIPNTR EQU FLEX+0G14 LINE BUFFER POINTER
WASH EQU FLEX+0D03
GETCHR EQU FLEX+0D15
PUTCHR EQU FLEX+0D18
INBUFF EQU FLEX+0D1B
PSTRNG EQU FLEX+0D1E
NXTCH EQU FLEX+0D27
GETFL EQU FLEX+0D2D
ADDBX EQU FLEX+0D36
OUTDEC EQU FLEX+0D39
OUTHEX EQU FLEX+0D3C
RPTERR EQU FLEX+0D3F
GETHEX EQU FLEX+0D42
INDEC EQU FLEX+0D48
FMS EQU FLEX+01406
DFSET EQU 0C000-FLEX
OFFSET EQU 0E010-FLEX
FIX FLEX INCONSISTANCY
READ EQU FLEX+01000-OFFSET 0BC00 OR 0DE00
RESTOR EQU READ+9
PAG
ORG 020 BASE PAGE
DRIVE RMB 1 DRIVE NUMBER
VOLUME RMB 2 VOLUME NUMBER
FATFL RMB 1 MULTI-PURPOSE ERROR FLAG
DELFLC RMB 1 SECTOR DELETED
SIDEHW RMB 1 SIDE OF DISK
LASTSEC RMB 1 LAST GOOD DISK SEC
TYPNTR RMB 2 TYPE OF DISK POINTER
TOLPNT RMB 2 FORMAT TABLE IN USE
SPECFT RMB 1 SPECIAL FORMAT
MAXTRK RMB 1 TRKS PER SIDE
MAXSEC RMB 1 SECS PER TRK
TOTALS RMB 2 MAXTRK = MAXSEC
BYTSEC RMB 1 BYTES PER SECTOR-1
GAPCHR RMB 1 GAP FILL CHAR
GAPSIZE RMB 1 SIZE OF LARGER GAPS
DBLFLC RMB 1 1 = DOUBLE SIDED
TR NOW RMB 1 CURRENT TRACK
SECHOW RMB 1 CURRENT SECTOR
WORKEN RMB 2 COMPUTED END OF WORKSPACE
ENOMOV RMB 2 END OF BLOCK MOVE
NAME RMB 8 VOLUME NAME
TEMP RMB 2 GENERAL PURPOSE
LETTER RMB 1 SAVE FORMAT LETTER
PAUSTR RMB 1 SAVE FLEX PAUSE
NXTLNK RMB 2 NEXT GOOD LINK
SOURCE RMB 2 POINTER FOR BLOCK MOVE
SECCNT RMB 1 SECTOR COUNTER
CHADRV RMB 1 DMA DRIVE (1,2,4,8)
BOOTNU RMB 1 BOOT NUMBER (0-3)
FLX2FL RMB 1 1 = FLEX-2 (4809)
FLX9FL RMB 1 1 = FLEX-9 (4809)
EXTEND RMB 1 1 = OVERSIZE FLEX CATALOG
T48FLC RMB 1 1 = FORTY TRACK
BT8FLC RMB 1 1 = DMA SYSTEM CONTROLLER

```

```

WORKBP EQU 02000 TRACK OKK SPACE
PAG
ORG 00100
START BRA INIT
VN FCB 3 VERSION NUMBER
BRA INIT ALLOW WASHSTART AT 0103
SPC 3
0 DEFAULT DRIVE FORMAT TABLE USES SAME
LETTER AS CALLING FORMAT. THE DMF1LG
MUST BE SET TO 1 FOR DMF1 CONTROLLER.
IF THE USEMFLC IS ALSO SET THEN DMA
WILL BE USED FOR DRIVES 0 OR 1 AND
MF48 USED FOR DRIVES 2 OR 3. EXTFLC
WOULD ONLY BE SET IF YOU ALWAYS WANT
AN EXTENDED DIRECTORY. FORTYF SHO LD
BE SET TO 1 BY USERS OF DRIVES SUCH
AS MPI THAT ALLOW FIVE EXTRA TRACKS
PER SIDE.
DEFTM FCB 'D DRIVE 0 SINGLE SIDE 5 FAST
FCB 'O DRIVE 1 SINGLE SIDE 5 FAST
FCB 'C DRIVE 2 SINGLE SIDE 8 FAST
FCB 'G DRIVE 3 SINGLE SIDE 8 FAST
DMF1LG FCB 0 0=USE MF48 1=USE DMF1
USEMFLC FCB 0 0=ALL DMF1 1=USERF 2/3
EXTFLC FCB 0 0=NORMAL 1=EXTEND DIR
FORTYF FCB 0 0=35 TRACK 1=40 TRA K
SPC 3
MAIN PROGRAM CALLS ALL SUBPROGRAMS
INIT LOAA EXTFLC GET DEFAULT
STAA EXTEND
LOAA FORTYF GET DEFAULT
STAA T48FLC
LOAA PAUSTR
CLR PAUSE NO PAUSE DURING PROMPTS
JSR PARSE PARSE PARAMETER LIST
JSR PROMPT GET DISK NAME AND NUMBER
TST SPECFT RADIO SHACK OR IBM
ONE ALLSAME NO SPECIAL TRACK

```

```

TRYACA JBR WRITED WRITE TRK 0
JBR VERIFY TEST TRACK ZERO
NOFATAL CLR TRKNOW
STAB BECHOW
JBR RPTSEC COUNT BAD SECTOR
DEC FATFLC COUNT RETRIES
BNE TRYAGAIN
FATAL LOX 0FATALMSG
JBR PSTRNG
BRA EXIT2 FIVE FATAL ERRORS
NOFATAL LOX 0OVERMSG
JBR PSTRNG
ALBAM JBR RTALL WRITE REMAINING TRACKS
LOX 0ALWRTMSG
JBR PSTRNG
JBR VERIFALL
TST SPECFT
BNE NOUPDATE
JBR UPDATE FIX TRK 0 SEC 3
BNE FATAL
NOUPDA JBR REPORT DISPLAY RESULTS
EXIT2 JMP EXIT
SPC 3
0 PARSE INPUT LINE FOR DRIVE AND FORMAT
IF HOME REQUEST PARAMETERS FROM USER
PARSE JBR GETHEX GET DRIVE NUMBER
BNE BADRIVE
TST 0
BNE NOHIVE
JBR DRIVEFOUND
BADRV LOAA 013 ILLEGAL DRIVE N MBER
JMP ERROR2
NOHIVE LOX 0DIAVMSG TITLE AND PROMPT
LOAA 0'8 MIN
LOAD 0'3 MAX
JBR REPLY
SUBA 0'0 REDUCE '0 TO 00
STAA DRIVE
JBR SETUP CONFIGURE HARDWARE
MENU LOX 0YSTMSG 0,9,R,S
LOAA 0'0 MIN
LOAD 0'5 MAX
BSR REPLY
LOAD 01
CMPA 0'0 4800
BNE NOT0
STAB FLX2FLC SET 4800
CLR FLX9FLC CLK 4809
BRA GETLETR
NOT0 CMPA 0'9 4809
BNE NOT9
CLR FLX2FLC CLK 4808
STAB FLX9FLC SET 4809
BRA GETLETR
NOT9 CH A 0'8 RADIO SHACK
BED COTCH2
CMPA 0'8 STANDARD IBM
BNE MENU
COTCH2 JMP COTCH2 SKIP REMAINING QUESTIONS
GETLETR LOX 0LETMSG
LOAA 0'A MAX
LOAD 0'P MIN
BSR REPLY
TAS
STAA LETTER GAVE FOR TABLE
SUBB 0'A REDUCE A TO 00
BITB 04 DMF OR MF48?
BED SETDMA
LOAD FLX9FLC 0 OR 1
ASLB
STAB BOOTNUM 0 OR 2
BRA TRY40
SETDMA LOAB FLX9FLC 0 OR 1
ASLB
ORAB 01
STAB BOOTNUM 1 OR 3
CLR FLX2FLC
TRY40 CLR T48FLC
CAPA 0'1 FIVE OR EIGHT?
OLD GETCAT NO PROMPT N EDED?
LOX 0FORTYMSG
LOAA 0'F FORTY TRACK
LOAD 0'T THIRTY FIVE
BSR REPLY
CMPA 0'F
BNE GETCAT
INC T48FLC
GETCAT CLR EXTEND
LOX 0CATMSG
LOAA 0'N NORMAL CATALOG
LOAD 0'O OVERSIZE
BSR REPLY
CMPA 0'N
BED NOEXT
INC EXTEND GO EXTENDED CATALOG
NOEXT LOAA LETTER
BRA COTCH2
SPC 2
PRINT MESSAGE AND WAIT FOR USER REPLY.
CHECKS RANGE OF REPLY AND RECYCLES IF
NEEDED.
ENTER WITH X=MESSAGE, A=MINIMUM,
S=MAXIMUM
RRCERR LOX 0OYNMSG OUT OF RANGE
JBR PSTRNG
LOX TEMP ORIGINAL MESSAGE
BRA REPLY2
REPLY STA TEMP
STAA SOURCE
STAB SOURCE+1

```



```

REPLYZ JSR PSTRNG
JSR GETCHR
CMPA 0040 LETTER?
BLD NOT4TH
ANDA 005F UPPER CASE NO PARITY
NOTLTR CMPA 00D RETURN=ABORT
BNE NASDRT
JMP FATAL

NASDRT CMPA SOURCE MIN
BLD RMCERR
CMPA SOURCE+1 MAX
BNE RMCERR
RTS
SPC 3
= SAVE DRIVE AND FIND FORMAT TABLE
=
DRIVEF STX TEMP
LDAA TEMP+1 GET DRIVE BYTE
CMPA 03 AX DRIVE
BLS NOTBAD
JMP BADRIVE

NOTBAD STAA DRIVE SAVE FOR LATER
JSR SETUP CONFIGURE HARDWARE
JSR WATCH GET DISK TYPE
SEC GOTCHR
LOAD DRIVE
LDX 0DEFTBL
JSR ADDBN COMPUTE DEFAULT FORMAT
LDAA 0,X GET DEFAULT
GOTCHR ANDA 005F UPPER CASE NO PARITY
CMPA 0'A MINIMUM LETTER
BLT SYNTAX
CMPA 0'D MAXIMUM LETTER
BHI SYNTAX
LDX 0TABLE TABLE OF FORMATS
GUSA 0'A REDUCE LETTER A TO ZERO
STAA LETTER SAVE FOR FAST FLAG
TAB
ASL0 FOR AT LETTER=2
JSR ADDBX INDEX INTO TABLE
LDX 0,X GET TABLE ADDRESS
STX TYPNTR START OF TABLE
LDAB 0Y SIZE OF PARAMETER TABLE
JSR ADDBX
STX TBLPNTN SECTOR TABLE POINTER
STX ENDMOVE SETUP BLOCK MOVE
LDX 0SPCFMT FIRST PARAMETER
STX TEMP
LDX TYPNTR TABLE START
JSR MOVE TABLE TO TEMP AREA
TST T40FLG FORTY TRACKS?
BEO NOT40
LDAA 039 USER TRACKS
STAA MAXTRK
LDAA 05 ADDITIONAL TRKS
LDX TOTALSEC
ADDTAK LDAB MAXSEC
JSR AD 0X TOTAL+MAXSEC
DECA
BNE ADDTRK
STX TOTALSEC
NOT40 LDAA 05
STAA FATFLG MAXIMUM FATAL ERRORS
LDAA MAXSEC
STAA LSTSEC LAST GOOD SEC
RTS

SYNTAX LDAA 026
ERROR2 LDX 0FCB
STAA 1,X
ERROR JSR PTERR
EXIT NOP
CLI ALLOW 8 OOLR
LDAA PAUBTMP
STAA PAUSE RESTORE ORIG PAUSE
JMP WARMS

NOORIV LDAA 016 DRIVE NOT READY
BRA ERROR2
SPC 3
= PROMPT FOR NAME OF DISK
=
PROMPT JSR CLRFCB PUT DRIVE IN FCB
JSR RESTORE SEEK TRACK ZERO
BES NOORIVE NO SUCH DRIVE
BEO PROMP2 PROTECTED DISK?
LDX 0PROTHSG
JSR PSTRNG CONTINUE ON ANYWAY

PROMP2 LDX 0NAMEMSG
TST SPCFMT
BEO ASKNAME
LDX 0SPCMMSG
ASKNAM JSR PSTRNG
LDAA DRIVE
ORAA 0'0 DRIVE IN ASCII
JSR PUTCHR
LDX 0NAME2MSG
JSR PSTRNG
TST SPCFMT
BEO GETNAME
JSR GETCHR
CMPA 0'Y YES OK?
BEO JOG
FATAL2 JMP FATAL
SPC 2
GETNAM JSR INBUFF GET DISK NAME
LDX LIPNTR POINT AT FLEX BUFFER
LDAA 0,X GET FIRST CHAR
CMPA 00D RETURN?
BEO FATAL2
LDX 0FCB
JSR GETFILE FLEX PARSEB NAME
BEO ERROR
LDX 0NAME SET UP MOVE
STX TEMP
LDX 0FCB+12
STX ENDMOVE

LDX 0FCB+4
JSR MOVE FROM FCB TO NAME
GETVOL LDX 0VOLUME
JSR PSTRNG
JSR INBUFF GET VOLUME NUMBER
JSR INDEC
BES GETVOLUME
TST NUMBER FOUND?
BEO FATAL2
STX VOLUME

JOG LDAA 02 TRK 2
JSR SEEK JOG HEAD OUT
JSR CLRFCB
JSA RESTORE BRING HEAD BACK
BNE FATAL2 STILL PROTECTED?
SPC 3
= WRITE TRACK ZERO
=
WRITED CLR SIDNOM
CLR TRKNOM TRK ZERO FIRST SIDE
JSR SETRAM
CLRA
JSR WRITRK WRITE EMPTY TRACK ZERO
TST LFLG
BEO NOT0B1
INC SIDNOM
CLR TRKNOM TRK ZERO SECOND SIDE
JSR SETRAM
CLRA
JSR WRITRK
NOT0B1 JSR CLRFCB
LDAA 03 TRK 0 SEC 3
STAA 01F,X
LDAA 01 PREPARE 6Y5 INFO RECORD
STAA 050,X FIRST TRK
STAA 05E,X FIRST SEC
LDAA MAXTRK
STAA 05F,X LAST TRK
STAA 066,X MAX TRK
LDAA MAXSEC
STAA 060,X LAST SEC
STAA 067,X MAX SEC
CLR 061,X SET TOTAL SECTORS TO
CLR 062,X ZERO FOR NOW
LDAA FLXDATE MONTH DAY YEAR
STAA 063,X
LDAA FLXDATE+1
STAA 064,X
LDAA FLXDATE+2
STAA 065,X
LDAA VOLUME NUMBER
STAA 05B,X
LDAA VOLUME+1
STAA 05C,X FAST FORMAT FLAG
LDAA LETTER
BITA 02 FAST FORMAT?
BEO NOTFST
LDAA 06FF
STAA FCB+319
NOTFST LDX 0FCB+050 SET UP MOVE
STX TEMP
LDX 0NAME+0
STX ENDMOVE
LDX 0NAME
BGR MOVE FROM NAME TO FCB
BGR WRITSECT WRITE SIX
BGR CLRFCB TRK 0 SEC 0
TST FLX2FLG
BNE ISFLX2
INC 01F,X TRK 0 SEC 1
ISFLX2 LDX 0FCB+040
STX TEMP FCB DATA POINTER
LDAA 000THUR SELECT BOOTSTRAP
BEO USEB0
CMPA 02
BLO USEB1
BEO USEB2
BHI USEB3

USEB0 LDX 0B0END
STX ENDMOVE
LDX 0B00T0
BRA MOVEB0

USEB1 LDX 0B1END
STX ENDMOVE
LDX 0B00T1
BRA MOVEB1

USEB2 LDX 0B2END
STX ENDMOVE
LDX 0B00T2
BRA MOVEB2

USEB3 LDX 0B3END
STX ENDMOVE
LDX 0B00T3
MOVEB3 BGR MOVE FROM BOOT TO FCB
BRA WRITSECTOR
SPC 3
= BLOCK MOVE SUBROUTINE
=
MOVE STX SOURCE SOURCE POINTER
LDAA 0,X
LDX TEMP DESTINATION POINTER
STAA 0,X
INX
STX TEMP
LDX SOURCE GET SOURCE
INX
CPX ENDMOVE END OF SOURCE+1
BNE MOVE
RTS
SPC 3
= CLEAR FILE CONTRL BLOCK
=
CLRFCB LDX 0FCB
CLRFCB CLR 0,X
INX

CPX 0FCB+040+256
BNE CLRFCB2
LDX 0FCB POINT X AT START
LDAA DRIVE
STAA 3,X RESTORE DRIVE
RTS
SPC 3
= READ OR WRITE SECTOR USING FCB
= FLEX AKES SEVERAL RETRIES
=
WRITEB LDAA 00A WRITE MODE
READB LDX 0FCB
STAA 0,X
JMP FMS WRITE SEC
SPC 3
= SETUP TRACK IN RAM
=
BETRAM CLR BECNOM
CLR BECNT
TST DBLFLG
BEO BETRA2
TST SIDNOM SECOND SIDE?
BEO BETRA2
LDAA MAXSEC
LSRA 1/2 MAXSEC
STAA BECNT
LDX TBLPNTN
LOOKU2 INX
DECA
BNE LOOKU2
LDAA 0,X FIRST SEC SECOND SIDE
STAA BECNOM
LDX 0WORKSPACE
BRA BECZER DON'T BUMP 6E NOW

BETRA2 LDX 0WORKSPACE
TST SPCFMT NO SEC 0 FOR IBM
BHI BECZERO ALWAYS SEC 0 FOR RS
BNE BUMPSEC
TST FLX2FLG
BEO BUMPSEC
TST TRKNOM FLEX-2 HAS TRK 0 SEC 0
BEO BECZERO
BUMPB INC BECNOM FIRST SEC NORMALLY ONE
BECZER LDAA CAPCHR
LDAB 040 LEADER SIZE
CAP1 STAA 0,X 40 CAPCHR BYTES
INX
DECB
BNE CAP1
LDAB 06
CAP2 CLR 0,X 6 ZERO BYTES
INX
DECB
BNE CAP2
LDAB 04FC
STAB 0,X INDEX MARK
INX
LDAB CAPSIZE
CAP3 STAA 0,X CAPSIZE CAPCHR BYTES
INX
DECB
BNE CAP3
SPC 3
= SETUP SECTORS
=
BECLOOP LDAB 06
CAP4 CLR 0,X 6 ZERO BYTES
INX
DECB
BNE CAP4
LDAB 04FE
STAB 0,X ID ADDRESS MARK
INX
LDAB TRKNOM
STAB 0,X TRACK
INX
CLR 0,X SEPARATOR
INX
LDAB BECNOM
NOP SPACE FOR TEST TRAP
NOP
NOP
STAB 0,X SECTOR
INX
CLRB CODE 00 FOR 120 BYTES
TST 0YTSEC
BPL SEC120
INCB CODE 01 FOR 256 BYTES
SEC126 STAB 0,X SECTOR SIZE CODE
INX
LDAB 00F7
STAB 0,X CRC FOR ADDRESS MARK
INX
LDAB 011
CAP5 STAA 0,X 11 CAPCHR BYTES
INX
DECB
BNE CAP5
LDAB 04
CAP6 CLR 0,X 4 ZERO BYTES
INX
DECB
BNE CAP6
LDAB 047B
STAB 0,X DATA ADDRESS MARK
INX
CLRA RS FILL CHAR
TST SPCFMT
BEO FLXFMT
BHI RADBNK
LDAA 00E5 IBM FILL CHAR
RADBNK LDAB 0YTSEC SECTOR SIZE
SPCFIL STAA 0,X SECTOR FILLED WITH 0E5
INX
DECB
BNE SPCFILL
STAA 0,X ONE MORE
INX
BRA BECFULL

```



```

SPC 2
FLXFM1 LDAA BECNOM
LDAB TRKNOM
CMPA MAXSEC
BLT BECNOM NOT LAST SEC
CLRA
TSTB
BEQ DIRECTORY
INCB
SECON INCA LINK NEXT SECTOR
DIRECT STAB 0,X LINK TRK
INX
STAA 0,X LINK SECTOR
INX
LDAB BYTSEC SIZE OF SECTOR
DECB ADJUST FOR LINK
FLXFLZ CLR 0,X FILL WITH ZEROS
INX
DECB
BNE FLXFILL

SECFUL LOAB 00F7
STAB 0,X CRC
INX
LDAB CAPSIZE
LDAA CAPCHR
CAP7 STAA 0,X CAPSIZE CAPCHR BYTES
INX
DECB
BNE CAP7
SPC 3
# SECTOR COMPLETE. COMPUTE NEXT SECTOR
#
TST BPCFMT
BEQ FLEXFMT
INC BECNOM NUMERICAL ORDER
LDAB BECNOM
CMPB MAXSEC DONE?
BLC BECLO2 LOOP FOR ANOTHER SEC
BRA TRKDONE

FLEXFM INC SECCNT BUMP COUNTER
LDAB MAXSEC
TST DBAFLC
BEQ NOTDB3
TST SIDONH B COMD SIDE?
BNE NOTDB3
LSRB 1/2 MAXSEC
NOTDB3 CMPB SECCNT DONE?
BLE TRKDONE
STX TEMP SAVE WORKSPACE POSITION
LDX TBLPNTX
LDAB SECCNT
LOOKUP INX NEXT SEC FROM TABLE
DECB
BNE LOOKUP
LDAB 0,X
STAB BECNOM NEXT PHYSICAL SECTOR
LOK TEMP
SECCLO2 JMP SECCLOP
SPC 2
YANDONE STX WORKEND SAVE END OF WORKSPACE
RTB
SPC 3
# VERIFY TRACK ZERO USIN FLEX READ
#
VERIF0 CLRB
TST FLXZFLG FLEX-2 HAS SEC 0
BNE VLOOP
INCB
VLOOP LDX 0FCB+00D
CLRA TRACK
PSHB SAVE SECTOR
JSR READ
PULB
BNE 0 DTR0
TSTB
BNE INCBEC
INCB FLEX-2 SKIP SEC 1
INCBEC INCB
CMPB MAXSEC
BLE VLOOP
CLRB FLA TRK 0 OK
BADTR0 RTB
SPC 3
# WRITE ALL REMAINING TRACKS
#
WRTALL CLRA
TST BPCFMT
BNE WRTLOOP IDH STARTS WITH TRK 0
INCA FLX STARTS WITH TRK 1
WRTLOOP STAA TRKNOM
CLR SIDONH
JSR SETRAM
LDAA TRKNOM
JSR WRTTRK FIRST SIDE
TST DBAFLC
BEQ NOTDB4
INCB SIDONH SECOND SIDE
JSR SETRAM SETUP TRACK
LDAA TRKNOM
JSR WRTTRK
NOTDB4 LDAA TRKNOM
INCA
CMPA MAXTRK DONE?
BLE WRTLOOP
JSR CLRFCD
LDX MAXTRK REWRITE LAST SECTOR
STX FCB+01E
JMP WRTSECTOR
SPC 3
# VERIFY ALL TRACKS QUICKLY BY USING
# FAST READ AND TRACK SKEWING
#
VERIFA LDAA MAXTRK
STAA TRKNOM
LDAB 02 DUMMY SEC
LDX 0FCB+040
JSR READ DROP HEAD ON INSIDE TRK
LDAA MAXSEC

```

```

BTAA SECCNT
LDX TBLPNT
STX TEMP
CLA FATLFLG
CLR DELFLC
VESEK LDAA TRKNOM
JSR SEEK
VELOOP LDX TEMP POINTS TO TABLE
LDAB 0,X SECTOR
BNE NOTEND
LDX TBLPNT RESET POINTER
LDAB 0,X
NOTEND INX
STX TEMP
STAB SECNOM SAVE FOR ERROR
JSR FSTRED HIGH SPEED READ
BEQ DOOS2
JSR RPTSECT 'SOFT' ERROR
TST BPCFMT
BEQ VESLOW
LDAB 02 DUMMY SEC
LDX 0FCB+40
JSR READ INSURE HEAD STILL DOWN
DOOS2? JMP DOOSSEC ONLY FLEX CAN DELETE
SPC 3
# IF SOFT ERROR IS FOUND ENTIRE TRACK MUST BE
# VERIFIED WITH FLEX AND BAD SECTORS DELETED.
#
VESLOW JSR CLRFCD
LDAA TRKNOM
STAA 01E,X
LDAA MAXSEC LOGICAL END OF TRK
STAA SECNOM
VLOOP2 STAA 01F,X
LDAA 09
JSR READSEC
BEQ VEROK THIS SECTOR OK?
LDX 0HAROMSG
JSR RPTSECT+3
TST DELFLC FIRST CONSECUTIVE DELETE
BNE NOTLAST
INC DELFLC
LDAA TRKNOM SAME TRACK
STAA NXTLNK
LDAA SECNOM
INCA NEXT HIGHER SECTOR
STAA NXTLNK+1
CMPA MAXSEC LAST IN TRACK?
BLE NOTLAST
LDAA 01 FIRST SEC NEXT TRK
STAA NXTLNK+1
LDAA TRKNOM
INCA NEXT TRK
STAA NXTLNK
CMPA MAXTRK LAST SEC ON DISK?
BLT NOTLAST
COM DELFLC FLAG LAST SECTOR
CLR NXTLNK END DISK LINKS ZERO
CLR NXTLNK+1
NOTLAST LDAB 01
STAB FATLFLG
LDX TOTALSECTORS
DEX
STX TOTALSECTORS
LDX 0FCB
BRA MOVEON

VEROK TST DELFLC ANY DELETIONS PENDING?
BEQ MOVEON
BPL FLGNORMA
LDAA SECNOM LAST GOOD SEC
STAA LSTSEC
FLGNORM LDAA NXTLNK NEXT GOOD LINK
STAA 000,X LINK IN SECTOR
LDAA NXTLNK+1
STAA 001,X
JSR WRTSEC
CLR DELFLC NO ELETIONS PENDING
MOVEON LDAA SECNOM
DECA
STAA SECNOM BACK UP ONE
BNE VLOOP2
TST DELFLC
BEQ NITTRK HIGH SPEED NEXT TRACK
DEC TRKNOM
BNE VESLOW CONTINUE SLOW VERIFY
JMP FATAL RAM INTO TRK 0
SPC 3
# SKEW OUT SIX SECTORS PER TRACK
#
DOOS2 DEC BE NT
BEQ NITTRK
JMP VELOOP

NITTRK L AB MAXSEC RESET COUNTER
STAB SECCNT
LDAB 00 TRK TO TRK SKEW
LDX TEMP
SKEWLO TST 0,X
BNE MOTEN2
LDX TBLPNT RESET POINTER:
DEX
MOTEN2 INX ADD SKEW TO TEMP
DECB
BNE SKEWLOOP
STX TEMP
DEC TRKNOM
BEQ ENDOVERIFY
BLT RETR03
ENDOR3 JMP VESEK STEP OUT TOWARD ZERO
SPC 3
ENDOR3 TST BPCFMT IF SPECIAL THEN
BNE ENDOHORE VERIFY TRK 0
RETR03 RTB
SPC 3
# UPDATE SIR FOR TOTAL SECTORS
#
UPDATE JSR CLRFCD
LDAA 03 SIR
STAA 01F,X

```

```

LDAA 09 READ MODE
JSR READSEC
BNE RETR03
TST EXTEND
BEQ OIR0M NORMAL DIRECTORY
LDAA MAXSEC
LDX TOTALSECTORS
OOEXT0 DEX REDUCE BY ONE TRACK
DECA
BNE ODEXTEND
STX TOTALSECTORS
INCB FCB+030 FIRST USER TRACK
BSR OIR0M UPDATE SIR
BNE RETR03
JSR CLRFCD
LDAA 01
STAA 01E,X TRK 1 SEC MAX
LDAA MAXSEC
STAA 01F,X SEC
BSR WRT02 END OF DIRECTORY
BNE RETR03
CLR 01E,X TRK 0 SEC MAX
LDAA 01
STAA 000,X LINK EXTENDED DIRECTORY
STAA 001,X
BSR WRT02
SPC 2
DIX0M LDX TOTALSECTORS
STX FCB+001
LDAA LSTSEC NORMALLY=MAXSEC
STAA FCB+000 NEW LAST SECTOR
WRT02 JMP WRTSECTOR
SPC 3
# REPORT PROGRAM FINISHED
#
REPORT LDX 0FINMSG
JSR PSTRNG
LDX 0TOTALSECTORS
CLRB
JMP OUTDEC
SPC 3
# REPORT BAD SECTOR
#
RPTSEC LDX 0SOFTMSG
LDAB TRKNOM
BSR PNTNUM BAD TRK
LDAA 0020 SPACE
JSR PUTCHR
LDAB SECNOM
BSR PNTNUZ BAD SEC

PNTNUM JSR PSTRNG PRINT MESSAGE
PNTNUZ STAB ENDOHORE USE AS TEMP
LDX ENDOHORE
JMP OUTDEX
SPC 3
# INDEX TABLE FOR FORMAT TABLES
#
FTABLE FOR TABLE
FDB TABLE
FDB TABLEC
FDB TABLED
FDB TABLEA
FDB TABLEB
FDB TABLEC
FDB TABLED
FDB TABLEI
FDB TABLEJ
FDB TABLEK
FDB TABLEL
FDB TABLEI
FDB TABLEJ
FDB TABLEK
FDB TABLEL
FDB TABLE
FDB TABLES
SPC 3
# FORMAT TABLES FOR SEVERAL DISKS
#
# BYTE 1 SPECIAL FORMAT FLA
# (1)=IDH 0FF=06)
#
# BYTES 2-3 MAXIMUM TRACKS AND SECTORS
# BYTES 4-5 TOTAL USER SECTORS PER DISK
# BYTE 6 BYTES PER SECTOR-1
# BYTES 7-8 CAP CHARACTER AND SIZE
# BYTE 9 DOUBLE SIDED=1
# BYTES A-N PHYSICAL ORDER OF SECTORS
# BYTE M+1 NULL MARKS END OF TABLE
#
TABLEA FCB 0 NORMAL 0 INCH SINGLE
FCB 76,15 TRACKS,SECTORS
FDB 76X15 TOTAL USABLE SECTORS
FCB 255 SEC SIZE-1
FCB 0FF,27 CAP CHARACTER AND LENGTH
FCB 0 SINGLE SIDED
FCB 1,0,00,3,0,00,3,0A,0F,2
FCB 7,0C,1,9,0E,0
SPC 2
TABLEB FCB 0 NORMAL 0 INCH DOUBLE
FCB 76,30
FDB 76X30
FCB 255
FCB 0FF,27
FCB 1
FCB 1,0,00,3,0,00,3,0A,0F,2
FCB 7,0C,1,9,0E
FCB 010,015,01A,012,017,01C
FCB 014,019,01E,011,016,01B
FCB 013,010,01D,0
SPC 3
TABLEC FCB 0 FAST 0 INCH SINGLE
FCB 76,15
FDB 76X15
FCB 255
FCB 0FF,27
FCB 0
FCB 1,0,00,3,0,0E,2,7,0C
FCB 3,0A,0F,3,0,0D
SPC 2

```



```

TABLED FCB 0 FAST 8 INCH DOUBLE
FCB 76,30
FCB 76,30
FCB 255
FCB 0FF,27
FCB 1
FCB 1,6,90,4,9,8E,2,7,8C
FCB 5,8A,8F,3,8,8D
FCB 017,01C,018,015,01A,013
FCB 010,01D,011,016,01B,014
FCB 019,01E,012,0
SPC 3
TABLEI FCB 0 NORMAL 5 INCH SINGLE
FCB 34,10
FCB 34,10
FCB 255
FCB 0,13
FCB 0
FCB 1,3,5,7,9,2,4,6,8,0A,0
SPC 2
TABLEJ FCB 0 NORMAL 5 INCH DOUBLE
FCB 34,20
FCB 34,20
FCB 255
FCB 0,13
FCB 1
FCB 1,3,5,7,9,2,4,6,8,0A
FCB 00,0D,0F,011,012,0C
FCB 0E,018,012,014,0
SPC 2
TABLEK FCB 0 FAST 5 INCH SINGLE
FCB 34,10
FCB 34,10
FCB 255
FCB 0,13
FCB 0
FCB 1,6,4,9,2,7,5,0A,3,0,0
SPC 2
TABLEL FCB 0 FAST 5 INCH DOUBLE
FCB 34,20
FCB 34,20
FCB 255
FCB 0,13
FCB 1
FCB 1,6,4,9,2,7,5,0A,3,0,0
FCB 012,0B,018,0E,013,0C
FCB 011,0F,014,0D,0
SPC 2
TABLEM FCB 0 QUICK 8 INCH SINGLE
FCB 76,15
FCB 76,15
FCB 255
FCB 0FF,27
FCB 0
FCB 1,4,7,0A,0D,0F,2,5,0,0B
FCB 0E,3,0,9,0C,0
SPC 2
TABLEN FCB 0FF RADIO SHACK
FCB 34,9
FCB 34,10
FCB 255
FCB 0,13
FCB 0
FCB 0,5,1,6,2,7,3,0,4,9,0
SPC 2
TABLEO FCB 1 STANDARD IBM
FCB 76,26
FCB 76,26
FCB 127
FCB 0FF,27
FCB 0
FCB 1,2,3,4,5,6,7,8,9,10
FCB 11,12,13,14,15,16,17,18,19,20
FCB 21,22,23,24,25,26,0
SPC 2
= MESSAGES
FATALM FCC /FORMAT ABORTED//
FCB 7,4
VER0M FCC /TRACK ZERO VERIFIED/
FCB 4
ALWRM FCC /ALL TRACKS WRITTEN/
FCB 4
PROTM FCC /DISK IS PROTECTED//
FCB 7,4
BPCMG FCC /ENTER Y FOR SPECIAL FORMAT /
FCC /ON DRIVE /
FCB 4
NAMRM FCC /NAME FOR DISK IN DRIVE /
FCB 4
NAMZM FCC /OR PRESS RETURN TO ABORT//
FCB 4
VOLRM FCC /VOLUME NUMBER/ /
FCB 4
FINRM FCC /FORMAT FINISHED/
FCB 0D,0A,0
FCC /AVAILABLE SECTORS/ /
FCB 7,4
SOFTM FCC /SOFT ERROR AT /
FCB 4
HARDM FCC /HARD ERROR AT /
FCB 4
DRVM FCC 0A,0A
FCC /LEO'S UNIVERSAL DISK FORMATTER/
FCB 0D,0A,0A
FCC /DRIVE NUMBER TO INITIALIZE (0-3)//
FCB 4
SYSTM FCB 0,0A
FCC /0=8008 R=RAIDQ B=ACA/
FCB 0D,0A
FCC /9=6007 S=STANDARD IBM/
FCB 0D,0A,0A
FCC /DISK TO BE USED WITH (0,9,R,B)//
FCB 4
LETRM FCB 0D,0A
FCC /
FCC / 8 INCH 5 INCH/
FCB 0D,0A
FCC /
FCC / DNAS MFAB DNAS MFAB/
FCB 0D,0A

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FCC /SINGLE SIDED NORMAL /
FCC / A E J M/
FCB 0D,0A
FCC /DOUBLE SIDED NORMAL /
FCC / B F J N/
FCB 0D,0A
FCC /SINGLE SIDED FAST /
FCC / C G K O/
FCB 0D,0A
FCC /DOUBLE SIDED FAST /
FCC / D H L P/
FCB 0D,0A,0A
FCC /ENTER DESIRED OPTION (HAK. (A-F))//
FCB 4
FORTYM FCB 0D,0A
FCC /THIRTY FIVE OR FORTY TRACK (T,F)//
FCB 4
CATM FCC 0D,0A
FCC /NORMAL OR OVERSIZE CATALOG (N,O)//
FCB 4
DUIM FCC 0D,0A
FCC /OUT OF RANGE/
FCB 0D,0A,7,4
FAG
= DISK DRIVERS FOR BOTH MFAB AND DNAS
= DRIVER IS SELECTED BY VALUE OF SYSFLG
=
SETUP TST SYSFLG WHICH SYSTEM?
BEQ MSETUP
JMP SETUP
WRITM TST SYSFLG DMA OR MFAB?
BEQ MWRITM
JMP MWRITM
SEEM TST SYSFLG DMA OR MFAB?
BEQ MSEEM
JMP MSEEM
FS RED TST SYSFLG DMA OR MFAB?
BEQ MFSRED
JMP MFSRED
SPC 3
= DRIVER FOR SHIP MF-08 OR F&D MDI-1
=
= LOCAL ADDRESS EQUATES
=
MCMRG EQU MFAB 1771 COMMAND REGISTER
MTRKRG EQU MCMRG+1 TRACK
MSECRG EQU MCMRG+2 SECTOR
MDATRG EQU MCMRG+3 DATA
SPC 3
= SET CONTROLLER TYPE
=
MSETUP LDAB 02 BOOT 0009 MFAB
CLR FLXZFLG
LDAA TESTBYT
CMPA 00CC 0000 IN USE?
BNE IS0009
CLR0
INC FLXZFLG
IS0009 STAB 001NUM 0 OR 2
CLR SYS LG NO DMA
RTS
PC 3
= WRITE TRACK FROM RAM TO DISK
=
MWRITM BSR MSETID
BSR MSEEK FIMO TRACK
TESTBY LDX 0WORKSPACE
NOP
SET STOP SPOOLER
LDAB 00F4 WRITE TRACK COMMAND
STAB MCMRG
BSR DELAY
LDAB 02 DATA REQUEST BIT
WRITR1 LDAA 0,X GET BYTE IN ADVANCE
WRITR2 BTR MCMRG
BEQ WRITR2
STAA MDATRG WRITE BYTE
INX
CPK WORKEND OUT OF DATA?
BNE WRITR1
DECH BUST BIT
LDAA GAPCMR TRAILER BYTE
TRAIL CMPS MCMRG
BCT RETRNM END OF TRACK
BEQ TRAIL NO DATA REQUEST
STAA MDATRG TRAILER OF GAPCMR
BRA TRAIL
MSEEK CMPS MTRKRG ALREADY THERE?
BEQ RETRNM
BEEN2 STAA MDATRG
BSR DELAY
LDAA 0010 SEEK
STAA MCMRG
BSR DELAY
MREADY LDAA MCMRG
BITA 01 BUST BIT
BNE MREADY
RETRNM RTS
ABETST TST 00FLG NEEDED?
BEQ MOTDB4
NOP ADD SIDE HERE
MOTDB4 RTS
MFSTRD STAB MSECRC
CLR SLOWM ASSUME FIRST SIDE
TST 00FLG
BEQ MOTDB7
LDAB MAXSEC
LSRB MAXSEC/2
CMPS SECONM
BGE MOTDB7
INC SLOWM SECOND SIDE
MOTDB7 BSR ABETSID
NOP
SET STOP SPOOLER

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LDAA 0000 READ WITHOUT DELAY
STAA MCMRG
MMDATA LDAB MCMRG
CMPS 01
BNE MMDATA
BSA MREADY
BITA 0010 ENRDM MASK
RTS
DELAY BSA 002 MASTIE TIME
BSK 002
BSR 002
BSR 002
RTS
SPC 3
= DRIVER FOR DNAS
=
= LOCAL ADDRESS EQUATES
=
ADDRESS EQU DNAS+000 FIRST ADDRESS FOR DMA
CNTREC EQU DNAS+002 BYTES TO BE TRANSFERED
CCREG EQU DNAS+010
PRIREG EQU DNAS+014
CCMCRG EQU DNAS+020 1771 COMMAND AND STATUS
DTRKRG EQU DNAS+021 1771 TRACK
DSECRG EQU DNAS+022 1771 SECTOR
DDATRG EQU DNAS+023 1771 DATA
DMVREG EQU DNAS+024
SPC 3
= SET CONTROLLER TYPE AND DRIVE
=
DSETUP LDAB 01 BOOT 0000 DMA
STAB SYSFLG ASSUME DMA
CLR FLXZFLG
LDAA TESTBYT
CMPA 00CC 0000 IN USE?
BEQ IS0000
LDAB 03 BOOT 0009 DMA
IS0000 STAB 001NUM 1 OR 3
LDAA DRIVE 0-3
TST USENFG USENFG.CMD IN USE?
BEQ KEEPDM
CMPA 02
BLO KEEPDM
CLR SYSFLG USE MFAB
ANDB 02 STRIP DMA BIT
STAB 001NUM 0 OR 2
BNE KEEPDM
INC FLXZFLG 0000 MFAB=FLX-2
KEEPDM LDAB 01
LDAA DRIVE
BEQ DMADOK
DLOOP ASLB SHIFT DRIVE BIT
DECA
BNE DLOOP
DMADOK STAB DMADRV 1 2 4 OR 8
RTS
SPC 3
= WRITE TRACK FROM RAM TO DISK
=
DMWRITM BSR SETDRV
BSR DSEEK
NOP
SET STOP SPOOLER
TESTBY LDX 0FFFF-WORKSPACE
STB ADDRESS
LDX 0MDRSPACE
STX TEMP MOVE TO MEMORY
LDAA WORKEND+1
BUBA TEMP+1
LDAB WORKEND
SBCB TEMP
COMB
COMB
STAB CNTREC -(WORKEND-WORKSPACE)
STAA CNTREC+1
LOR CNTREC GET COMPLIMENT
LDAB 00FC
STAB CCREG
LDAB 00FE
STAB PRIREG
LDAA 00F9 TRACK WRITE
STAA DCMCRG
DLOOP1 TST CCREG STARTED COUNTING?
BNI DLOOP1
LDX 00FFFF
STB PRIREG
BRA DREADY
= SEEK TRACK
=
DSEEK CMPS DTRKRG ALREADY THERE
BEQ DRETRM
STAA DDATRG
LDAA 001V SEEK SPEED
STAA DCMCRG
BSR DELAY USE MFAB DELAY
DREADY LDAA DCMCRG
BITA 01
BNE DREADY
DRETRM RTS
SPC 3
= SET DRIVE NUMBER
=
SETDRV LDAB DMADRV
DRAB 0000
TST SLOWM SECOND SIDE?
BEQ SETDR2
DRAB 0000
SETDR2 COMB
STAB DMVREG
DELAY1 JMP DELAY
SPC 3
= FAST READ CHECKS WITHOUT MEAN LOAD DELAY
=
OFSTRD STAB DSECRG
CLR SLOWM ASSUME FIRST SIDE
TST 00FLG
BEQ MOTDB5
LDAB MAXSEC

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```

LSR0 MAXSEC/2
CAPP SECHOM
BCE NOT005
INC SIDMOM SECOND SIDE
NOT 05 BSR SETDRV
LDX 0FFFF-FCB-040 LOAD DATA HERE
BTX ADDRESS
LDX 0FFFF-256 BYTES TO TRANSFER
TST BYTSEC SECTOR SIZE
BNI NOT120
LDX 0FFFF-120
NOT120 STX CNTREG
LOAD 00FD
B1A CCREG
INCO
STAS PRIREG
NOP
BEI STOP SPODLER
LDAA 0000 READ WITHOUT DELAY
STAA DCOMR0
LDX 00F00 TIME OUT LIMIT
FSTREZ TST CCREG
BPL FSTREZ
DEX
ONE FSTREZ
FSTPEJ LDX 0FFFF
BTX PRIREG
BSR DREADY
BITA 0010
RT0
BPC 3
* BOOTSTRAP LOADER FOR TRK 0 SEC 0/1
*
* THIS SECTOR IS READ BY ROM BOOTSTRAP
* AND IN TURN READS ALL OF THE FLEX.BY5
* FILE STARTING AT THE SECTOR STORED IN
* THE FIFTH AND SIXTH BYTES BY 'LINK.CMD'.
*
* THERE ARE FOUR VERSIONS OF THE BOOTSTRAP.
* THE VALUE OF BOOTNUM DETERMINES WHICH ONE
* WILL BE USED. THESE BOOT0S ARE RELOCATABLE
* AND WILL ASSEMBLE CORRECTLY REGARDLESS OF
* WHICH PROCESSOR ASSEMBLES THIS PROGRAM.
*
* BOOT0 = 6800 MF60
* BOOT1 = 6800 DNAP
* BOOT2 = 6809 MF60
* BOOT3 = 6809 DNAP
*
* LOCAL ADDRESS EQUATES FOR 6809 AND 6809
*
BPNT0 EQU 0A07C POINTER AND BUFFERS
BPNT1 EQU 0C1FC
BTMP0 EQU BPNT0+2
BTMP1 EQU BPNT1+2
BFCB0 EQU 0A200
BFCB1 EQU 0C200

MCOMR0 EQU 00010 6800 MF60
MTRKR0 EQU MCOMR0+1
MSECR0 EQU MCOMR0+2
MDATR0 EQU MCOMR0+3

ADDRESS EQU 09000 6800 DNAP
CNTRE0 EQU ADDRESS+002
CCREG0 EQU ADDRESS+010
PRIRE0 EQU ADDRESS+014
DCOMR0 EQU ADDRESS+020
DTRKR0 EQU ADDRESS+021
DSECR0 EQU ADDRESS+022
DDATR0 EQU ADDRESS+023
DRVRE0 EQU ADDRESS+024

MCOMR0 EQU 00010 6809 MF60
MTRKR0 EQU MCOMR0+1
MSECR0 EQU MCOMR0+2
MDATR0 EQU MCOMR0+3

ADDRESS EQU 09000 6809 DNAP
CNTRE0 EQU ADDRESS+002
CCREG0 EQU ADDRESS+010
PRIRE0 EQU ADDRESS+014
DCOMR0 EQU ADDRESS+020
DTRKR0 EQU ADDRESS+021
DSECR0 EQU ADDRESS+022
DDATR0 EQU ADDRESS+023
DRVRE0 EQU ADDRESS+024

BPC 3
* BOOT0
*
BOOT0 FCB 00E LDX 0
FDB BPNT0-2 LEAVE ROOM FOR TEMPS
FCB 1 NOP TO ADJUST POSITION
FCB 0 0,0,0 LDX 0LINK ADDR.
BNE GOTLN0
FCB 0FE,0FF,0FE GET VECTOR
FCB 0AE,0 JMP 0,X RESET

GOTLN0 FCB 0FF BTX
F B BPNT0 SAVE LINK SECTOR
BRA BSTR0 RESTORE HEAD

FRT01 L0AA BPNT0 FIRST TRK AND SEC
LOAD BPNT0+1
BREAD1 BSR BSECK1
LOAD 00FD
FCB 00E LDX 0
FDB 0FFFF-0FCB0 LOAD DATA HERE
FCB 0FF BTX
FDB ADDRESS
FCB 00E LDX 0
FDB 0FFFF-256 BYTES TO TRANSFER
FCB 0FF BTX
FDB CNTRE0
STAB CCREG0
INCB
LDAA 000C READ COMMAND
STAB PRIRE0
FCB 00E LDX 0
FDB 0F00 TIME OUT

```

```

FCB 0 INX
DECB COUNT BYTES
BNE MORE00

BSR BRDY0 WAIT FOR CRC CHECK
BIT0 001C READ ERROR?
BNE B0010 YES, TRY AGAIN
FCB 00E LDX 0
F B 0FCB0+1
SAVPT0 FCB 0FF BTX
FCB 0BPNT0 BOOTFCB POINTER
RT0

BSECK0 CMPA MTRKR0 ALREADY THERE?
BED GOTR0
STAA MDATR0
BSR BDEL0
LDAA 001B SEEK COMMAND
STAA MCOMR0
BSR BDEL0
FCB 037 PSH 0
BSR BRDY0
FCB 033 PUL 0
GOTR0 STAB MSECR0
BDEL0 BSR 0+2 WASTE TIME
BSR 0+2
BSR 0+2
RT0

BRDY0 LOAD MCOMR0
BIT0 01 RE 0Y?
BNE BRDY0 NO
RT0

ROBYT0 FCB 0FE LDX
FDB BPNT0
CPX 0BFCB0+256
BED GETLN0 END OF CURRENT SECTOR
FCB 0AA,0 DAA BYTE FROM SECTOR
FCB 0 INX
BRA SAVPT0

GETLN0 LDAA 0FCB0 LINK TO NEXT SECTOR
LOAD FCB0+1
BSR BREAD0
BRA R BT0

BSTR0 L0AA 000 RESTORE COMMAND
STAA MCOMR0
BSR BDEL0
BSR BRDY0
BSR FRT01 GET FLEX SEC
GETYP0 BSR ROBYT0
CMPA 02 BINARY BLOCK?
BED GETAD0
CMPA 0016 START ADR BLOCK?
BNE GETYP0
BSR ROBYT0
STAA BTMP0 STARTING ADDRESS
BSR ROBYT0
STAA BTMP0+1
FCB 0FE LDX
FDB BTMP0
FCB 0AE,00 JUMP TO FLEX

GETAD0 BSR ROBYT0
STAA BTMP0
BSR ROBYT0
STAA BTMP0+1
BSR ROBYT0
FCB 016 TAB BYTE COUNT
BED GETYP0 EMPTY BLOCK
ADVSE0 FCB 037 PSH 0
BSR ROBYT0 GET BYTE FROM BOOTFCB
FCB 033 PUL 0
FCB 0FE LDX
FDB BTMP0
FCB 0A7,0 STAA IN MEMORY
FCB 0 INX
FCB 0FF BTX
FDB BTMP0
DECB
BNE MOVE00
BRA GETYP0 LOOK FOR NEXT BLOCK

BIENO EQU = END OF BOOT0
BPC 3
* BOOT1
*
BOOT1 FCB 00E LDX 0
FDB BPNT0-2 LEAVE ROOM FOR TEMPS
FCB 1 NOP TO ADJUST POSITION
FCB 0 0,0,0 LDX 0LINK ADDR.
BNE GOTLN1
FCB 0FE,0FF,0FE GET VECTOR
FCB 0AE,0 JMP 0,X RESET

GOTLN1 FCB 0FF BTX
FDB BPNT0 SAVE START SE TOR
BRA BSTR1

FRT1A L0AA BPNT0 FIRST TRK AND SEC
LOAD BPNT0+1
BREAD1 BSR BSECK1
LOAD 00FD
FCB 00E LDX 0
FDB 0FFFF-0FCB0 LOAD DATA HERE
FCB 0FF BTX
FDB ADDRESS
FCB 00E LDX 0
FDB 0FFFF-256 BYTES TO TRANSFER
FCB 0FF BTX
FDB CNTRE0
STAB CCREG0
INCB
LDAA 000C READ COMMAND
STAB PRIRE0
FCB 00E LDX 0
FDB 0F00 TIME OUT

```

```

TIME1 T0T CCREG0
DPL ON1
FCB 0 DEX
BNE TIME1

OK1 FCB 00E LDX 0
FDB 0FFFF
FCB 0FF BTX
FDB PRIRE0
BSR BRDY1 WAIT FOR CRC CHECK
BIT0 001C READ ERROR?
BNE B0010 YES, TRY AGAIN
FCB 00E LDX 0
FDB 0FCB0+1
SAVPT1 FCB 0FF BTX
FDB 0BPNT0 BOOTFCB POINTER
RT0

BSTR1 BRA BSTR1A ISLANDS
FRT01 BRA FRT1A
BREAD1 BRA BREAD0

BSECK1 STAB DSECR0
EMP0 00F 0 INCH FIRST SIDE
BNI FLIP1
LOAD 007E DRV 0 FIRST SIDE
BRA B0010

FLIP1 LOAD 006E DRV 0 SECOND SIDE
DOSID1 STAB DRVRE0
CMPA 0TRKR0 ALREADY THERE?
BED BDEL1
STAA MDATR0
BSR BDEL1
LDAA 0019 SEEK SPEED
STAA DCOMR0
BSR BDEL1
BRA BRDY1

BDEL1 BSR 0+2 WASTE TIME
BSR 0+2
BSR 0+2
BSR 0+2
RT0

BRDY1 LOAD DCOMR0
BIT0 01 RE 0Y?
BNE BRDY1 NO
RT0

ROBYT1 FCB 0FE LDX
F B BPNT0
CPX 0BFCB0+256
BED GETLN1 END OF CURRENT SECTOR
FCB 0AA,0 L0AA BYTE FROM SECTOR
FCB 0 INX
BRA SAVPT1

GETLN1 L0AA 0FCB0 LINK TO NEXT SECTOR
LOAD 0FCB0+1
BSR BREAD1
BRA ROBYT1

BSTR1A L0AA 000 RESTORE SPEED
STAA DCOMR0
BSR BDEL1
BSR BRDY1
BSR FRT01 GET FLEX SEC
GETYP1 BSR ROBYT1
CMPA 02 BINARY BLOCK?
BED GETAD1
CMPA 0016 START ADR BLOCK?
BNE GETYP1
BSR ROBYT1
STAA BTMP0 STARTING ADDRESS
BSR ROBYT1
STAA BTMP0+1
FCB 0FE LDX
FDB BTMP0
FCB 0AE,00 JUMP TO FLEX

GETAD1 BSR ROBYT1
STAA BTMP0
BSR ROBYT1
STAA BTMP0+1
BSR ROBYT1
FCB 016 TAB BYTE COUNT
BED GETYP1 EMPTY BLOCK
MOVE1 FCB 037 PSH 0
BSR ROBYT1 GET BYTE FROM BOOTFCB
FCB 033 PUL 0
FCB 0FE LDX
FDB BTMP0
FCB 0A7,0 STAA IN MEMORY
FCB 0 INX
FCB 0FF BTX
FDB BTMP0
DECB
BNE MOVE1
BRA GETYP1 LOOK FOR NEXT BLOCK

BIENO EQU = END OF BOOT1
BPC 3
* BOOT 2
*
BOOT2 FCB 00E LDX 0
FDB BPNT0-2 LEAVE ROOM FOR TEMPS
FCB 00E,0,0 LDX 0LINK ADDR.
BNE GOTLN2
FCB 00E,0FF,0FE GET VECTOR
FCB 0AE,0 JMP 0,X RESET

GOTLN2 FCB 0FF BTX
FDB BPNT0 SAVE START SECTOR
BRA BSTR2

FRT2A L0AA BPNT0 FIRST TRK AND SEC
LOAD BPNT0+1
BREAD2 BSR BSECK2
LDAA 000C READ COMMAND

```



```

STAA MCONR9
BSR BDEL2
CLR6 COUNT 256 BYTES
FCB 98E LDX 0
FDB BFCB9 READ DATA INTO HERE
MORDED2 LDAA MCONR9
BITA 02 DATA AVAILABLE BIT
BNE GBYTE2 DATA IS READY
BITA 01 READY BIT
BNE MORDED2
BRA B00T2 ENDED TOO SOON

GBYTE2 LDAA MCONR9 GET A BYTE
FCB 0A7.00 STORE IN MEMORY
FDB 03001 INX
DECB COUNT BYTES
BNE MORDED2

BSR BROY2 WAIT FOR CRC CHECK
BITB 001C READ ERROR?
BNE B00T2 YES, TRY AGAIN
FCB 98E LDX 0
FDB BFCB9+4
SAVPT2 FCB 00F STX
FDB 00NTR9 BOOTFCB POINTER
RTS

FRSTB2 BRA FRST2A ISLANDS

BSECK2 CMPA MTRK9 ALREADY THERE?
BEO GOTRK2
STAA MCONR9
BSR BDEL2
LDAA 0010 SEEK COMMAND
STAA MCONR9
BSR BDEL2
FDB 03404 PSH 0
BSR BROY2
FDB 03504 PUL 0
GOTRK2 STAB MBECK9
BDEL2 BSR 0+2 WASTE TIME
BSR 0+2
BSR 0+2
RTS

BROY2 LDAB MCONR9
BITB 01 REA Y?
BNE BROY2 NO
RTS

RDBYT2 FCB 98E LDX
FDB 00NTR9
CPX 00FCB9+256
BEO CETLN2 END OF CURRENT SECTOR
FCB 0A6.0 LDAA BYTE FROM SECTOR
FDB 03001 INX
BRA BSAVPT2

CETLN2 LDAA 00FCB LINK TO NEXT SECTOR
LDAB BFCB9+1

BSR BREAD2
BRA RDBYT2

BRSTR2 LDAA 000 RESTORE TRK 0
STAA MCONR9
BSR BDEL2
BSR BROY2
BSR FRSTB2 GET FLEX SEC
GETYP2 BSR RDBYT2
CMPA 02 BINARY BLOCK?
BEO GETAD2
CMPA 0010 START ADR BLOCK?
BNE GETYP2
BSR RDBYT2
STAA BTEMP9 STARTING ADDRESS
BSR RDBYT2
STAA BTEMP9+1
FCB 98E LDX
FDB BTEMP9
FCB 0A6.00 JUMP TO FLEX

GETAD2 BSR RDBYT2
STAA BTEMP9
BSR RDBYT2
STAA BTEMP9+1
BSR RDBYT2
FDB 01F09 TAB BYTE COUNT
BEO GETYP2 EMPTY BLOCK
MOVSE2 FDB 03404 PSH 0
BSR RDBYT2 ET BYTE FROM BOOTFCB
FDB 03504 PUL 0
FCB 98E LDX
FDB BTEMP9
FCB 0A7.0 B TAA IN MEMORY
FDB 03001 INX
FCB 00F STX
FDB BTEMP9
DECB
BNE MOVSE2
BRA GETYP2 LOOK FOR NEXT BLOCK

BZEND EQU = END OF B00T2
SPC 3
= B00T3
=
B00T3 FDB 010CE LDB 0
FDB 00NTR9-2 LEAVE ROOM FOR TEMPS
FCB 98E.0 LDX 0 LINK ADDR.
BNE GOTLN3
FCB 00FF.0FE GET VECTOR
FCB 0A6.0 JUMP 0.0K RESET

GOTLN3 FCB 00F STX
FDB 00NTR9 HAVE START SECTOR
BRA BRSTR3

FRST3A LDAA 00NTR9 FIRST TRK AND SEC
LDAB 00NTR9+1
BREAD3 BSR BSECK3

LOAD 00FD
FCB 00E LDX 0
FDB 00FFF-00CB9 LOAD DATA HERE
FCB 00F STX
FDB 00NTR9
FCB 00E LDX 0
FDB 00FFF-256 BYTES TO TRANSFER
FCB 00F STX
FDB 00NTR9
STAB CCREG9
INCH
LDAA 000C READ COMMAND
STAB PRIER9
STAA MCONR9
FCB 98E LDX 0
FDB 00F00 TIME OUT
TIMES TST CCREG9
BPL OK3
FDB 0301F DEX
BNE TIME2

OK3 FCB 98E LDX 0
FDB 00FFF
FCB 00F STX
FDB PRIER9
BSR BROY2 WAIT FOR CRC CHECK
BITB 001C READ ERROR?
BNE B00T3 YES, TRY AGAIN
FCB 98E LDX 0
FDB BFCB9+4
SAVPT3 FCB 00F STX
FDB 00NTR9 BOOTFCB POINTER
RTS

BRSTR3 BRA BRST3A ISLANDS
FRST3B BRA FRST3A
BREAD3A BRA BREAD3

BSECK3 STAB DBECK9
CMPB 00F 0 INCH END FIRST SIDE
BNT FLIP3
LDAB 007E DRV 0 FIRST SIDE
BRA DB00103

FLIP3 LDAB 00A6 DRV 0 SECOND SIDE
DBST03 STA DRVRE9
CMPA 00NTR9 ALREADY THERE?
BEO BDEL3
STAA MCONR9
BSR BDEL3
LDAA 0010 SEEK SPEED
STAA MCONR9
BSR BDEL3
RA BROY3

BDEL3 BSR 0+2 WASTE TIME
BSR 0+2
BSR 0+2
BSR 0+2
RTS

BROY3 LDAB MCONR9
BITB 01 R ADY?
BNE BR Y3 NO
RTS

RDBYT3 FCB 98E LDX
FDB 00NTR9
CPX 00FCB9+256
BEO CETLN3 END OF CURRENT SECTOR
FCB 0A6.0 LDAA BYTE FROM SECTOR
FDB 03001 INX
BRA BSAVPT3

CETLN3 LDAB 00F 00 LINK TO NEXT
SECTOR
LDAB 00FCB9+1
BSR BREAD3A
BSR RDBYT3

BRST3A LDAA 000 RESTORE SPEED
STAA MCONR9
BSR BDEL3
BSR BROY3
BSR FRST3B GET FLEX SEC
GETYP3 BSR RDBYT3
CMPA 02 BINARY BLOCK?
BEO CETAD3
CMPA 0010 START ADR BLOCK?
BNE GETYP3
BSR RDBYT3
STAA BTEMP9 STARTING ADDRESS
BSR RDBYT3
STAA BTEMP9+1
FCB 98E LDX
FDB BTEMP9
FCB 0A6.00 JUMP TO FLEX

GETAD3 BSR RDBYT3
STAA BTEMP9
BSR RDBYT3
STAA BTEMP9+1
BSR RDBYT3
FDB 01F09 TAB BYTE COUNT
BEO GETYP3 EMPTY BLOCK
MOVSE3 FDB 03404 PSH 0
BSR RDBYT3 CE1 BYTE FROM BOOTFCB
FDB 03504 PUL 0
FCB 98E LDX
FDB BTEMP9
FCB 0A7.0 STAA IN MEMORY
FDB 03001 INX
FCB 00F STX
FDB BTEMP9
DECB
BNE MOVSE3
BRA GETYP3 LOOK FOR NEXT BLOCK

BZEND EQU = END OF B00T 3

```

## BIT Bucket

Mr. Don Williams  
68 Micro Journal  
Hixson, Tennessee 37543

Vail Electronics, Inc.  
P.O. Box 1136  
Palm Bay, Fl. 32905  
February 9, 1982

Dear Mr. Williams,

We thought your readers would be interested in our experiences with two relatively new products recently appearing on the market relating to the 6809 microprocessor: the Hazelwood Computer Systems' HELIX computer and Micromare's FASCAL for the OS-9 operating system.

### The HELIX computer:

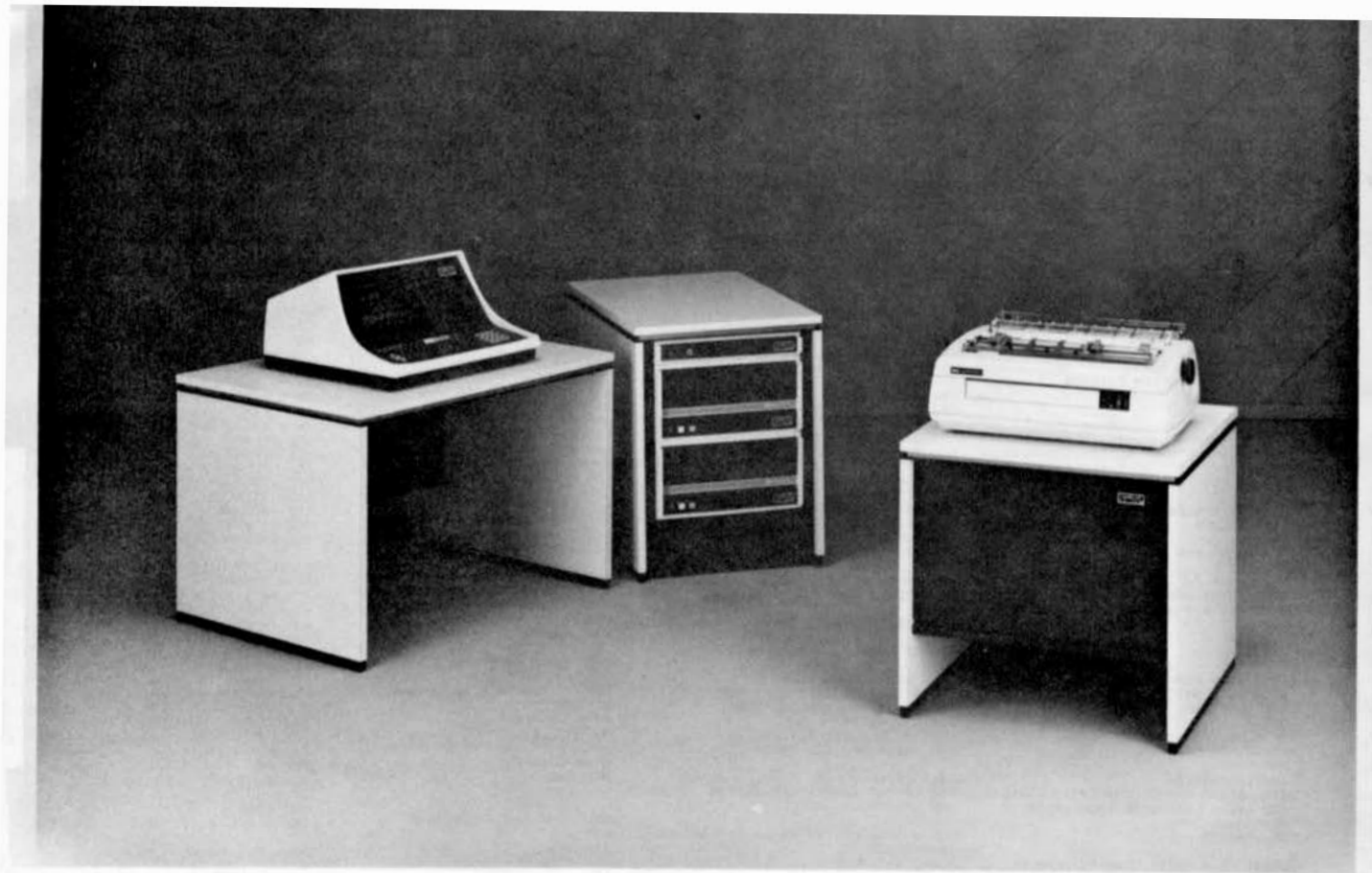
We purchased one of the first HELIX computers to be built, in November 1981, after seeing it advertised in 68 Micro Journal. Even though our unit is really a "preproduction" model, it has performed flawlessly from the start. It seems to be mechanically well constructed, housed in an Optima commercial grade cabinet. Our unit uses a Hazelwood 2 MHz 6809 CPU board, a 64K dynamic memory board, and has an upwards-compatible SS-64 main bus to allow future expansion to a 68000 system. The main features that attracted us to the HELIX were:

- Low price (>1995 list, including 2 parallel and 2 serial I/O ports, chassis, CPU card and 64K RAM)
- SS-50, OS-9 and FLEX compatibility
- 68000 compatibility with a full 16 bit data bus and 24 bit address bus
- Sturdy professional chassis, oversize power supply, "better" real time clock circuit (It knows the year as well as date and time, even after power has been off.)
- Use of only 2 MHz rated 8 series 68XX chips
- Hi-resolution, low cost color graphics card coming during the second quarter of 1982
- SS-64 bus allows physically large cards, such as special breadboard circuits

Of course, a new system, especially an early model like ours, will have some imperfections and design tradeoffs evident. Our list of "minuses" for the HELIX include:

- Relatively high cooling fan noise (At least in our early model.)
- Detailed hardware documentation just now becoming available
- Production schedule slips of about 6 months, to the second quarter of 1982, for the 68000 CPU card, disk controller card, 512K memory card, and hi-resolution color graphics card.





# THE COMPLETE BUSINESS SYSTEM

## + Multiuser + Highly Expandable + Cost Effective

### S+ THE CONCEPT

The S+ system is a modular computer system in which all portions of the hardware and software are designed to work together in the most efficient way possible. An S+ single user system with floppy disk storage is a competitive and cost effective entry level system. Unlike most other small computers being sold as "personal", or "small business" machines, the S+ system may be expanded to maximum capabilities using this same hardware and software. You cannot end up with a DEAD END system that cannot be expanded and whose software is not compatible with larger machines. A basic S+ system may be expanded to thirty-two users, a megabyte of main memory and hundreds of megabytes of hard disk storage by simply plugging in, or connecting the desired upgrade equipment.

### TOTAL DESIGN—Hardware and Software

The S+ system is an integrated hardware and software design. The two complement and enhance each other in this system. The UniFLEX® operating

system used in the S+ systems is patterned after the Bell Laboratories UNIX® operating system, one of the most admired and widely used operating systems in the world. Instead of being an afterthought, the software is part of the design of the S+ system. You can be sure that with this approach that all parts of the computer operate with maximum efficiency and cost effectiveness.

### THE CENTRAL PROCESSOR

The basic S+ system is configured with 256K bytes of memory and can be expanded to more than 1 million bytes. An efficient and fast hardware memory management system is used to allocate the available memory among the users on a dynamic basis. As little as 8K bytes, or the entire memory—if needed—can be used by any individual user. This makes it possible to run very large programs on the system, but it also uses no more memory than necessary for a particular job. The increase in cost effectiveness of this system over crude and outdated bank switching arrangements is dramatic.



The central processor runs in both user and supervisor states. It can detect and reject a defective user program. It is impossible for a user program to go bad and stop the entire system, as can happen quite easily in less sophisticated systems.

Task switching is accomplished by use of a multiple map RAM memory, with sixty-four individual task maps. Each task can access from 4 to 64 K-bytes of memory. Multiple tasks may be used in programs that require more than 64K bytes of memory for execution. When a task is completed the memory is automatically released for other use.

## SOFTWARE

The S+ operating system, UniFLEX® is a multiuser, multitasking operating system based on the UNIX® operating system that has been used for many years on Digital Equipment Corp. PDP-11 series minicomputers. It is considered one of the most sophisticated and "user friendly" operating systems available. Variations of UNIX® are rapidly becoming standard on mini and larger microcomputers.

A large variety of languages are available for use with the system. These include FORTRAN, COBOL, BASIC, and Pascal. Word processing packages are also available to give you full text processing capability on the system.

Applications programs are available in large quantities in many fields. This includes general business, medical, dental, veterinary, library and real estate management; plus others. Since the system is multiuser it can also be connected to cash registers to produce a point-of-sale terminal system combined with the computer. The possibilities for application of this system are endless.

## THE I/O SYSTEM

The S+ system is totally interrupt driven. All terminal and printer I/O devices connect to an I/O bus separate from the main bus. Up to thirty-two separate devices may be connected to the I/O bus at any one time. If I/O activity is great enough to cause an unacceptable slowdown in system operation, a separate I/O processor can be installed in the system. This plug-in option removes all I/O handling

overhead from the main processor and allows operation of up to thirty-two external devices at 9,600 baud. Without an integrated total design, as in the S+ system, it would become impractical to use a UNIX® type operating system in a situation with heavy terminal I/O activity.

## DISK STORAGE

A wide range of disk storage capacity is available for the S+ system, from 2.5 M-byte floppy disks to an 80 M-byte Winchester and many sizes between. All disk controllers use direct memory access (DMA) type operations to maximize data transfer and to minimize overhead on the main processor. The Winchester disks also use intelligent controllers along with DMA transfers to preserve the performance that these type devices are capable of giving. Without this distributed intelligence the system performance would be greatly degraded. The UniFLEX® operating system is designed to work at maximum efficiency with this type disk system. The data transfer rates achieved by this combination rival those of large minicomputers.

## COMMUNICATIONS

A high speed local network communications system is available to interconnect S+ systems. The VIA-BUS® network will allow communication between systems at data rates of over 400K baud. Such a system makes it possible to share data between local systems in an efficient and low-cost manner.

## AVAILABLE SOON

Tape backup—20M-Byte in less than 15 minutes on a standard ¼ inch cartridge.

Mini-Wini—5 and 10 M-Byte Winchesters—5¼ inch package. Winchester performance, for smaller systems in a small package. UniFLEX® compatible design.

Large Capacity—190 and 340 M-Byte Winchesters, plus SMD cartridge drives.

*UniFLEX is a registered trademark of Technical Systems Consultants, Inc.*

*UNIX is a registered trademark of Bell Labs.*

*VIABUS is a registered trademark of Southwest Technical Products Corporation.*



SOUTHWEST TECHNICAL PRODUCTS CORPORATION  
219 W. RHAPSODY  
SAN ANTONIO, TEXAS 78216 (512) 344-0241



- Neither the 64K nor the 512K memory card uses parity.

In summary, we found the HELIX to be a well built, reliable machine, answering our need for an SS-50 6809 computer that would be easily upgradable to the 68000. We have used it a total of approximately 200 hours with no problems. In fact, we became such enthusiastic supporters of the HELIX that we are now authorized HELIX dealers!

### OS-9 PASCAL:

Our selection of an operating system and high level language for the HELIX was based on our long term goal of developing software for an ultra-low cost computer-aided printed circuit design system. Execution speed is a primary concern, followed by compatibility with a multitasking operating system, good memory efficiency and future portability to compilers for the 68000.

We selected Microware's OS-9 operating system and their new OS-9 Pascal product. For the Pascal list price of \$400 the user receives six major pieces of software:

- Compiler - for compiling Pascal source code to pcode
- Interpreter - for executing pcode programs small enough to reside in memory
- Virtual code swapping interpreter - for executing very large pcode programs
- Optimizing translator - for converting pcode programs or individual procedures into 6809 assembly source code. (An assembler, not included, but available from Microware is required to convert the translator output to native code.)
- Linkage editor utility - for integrating native code "external" routines such as separately translated or assembled routines into Pascal programs
- Support packages - for support of the runtime execution of pcode or Pascal produced native code programs (Three separate support packages of varying size and complexity are provided.)

The OS-9 Pascal seems to meet all of our requirements. Under the most important criterion, speed, the OS-9 Pascal appears to be more than twice the speed of its nearest competitor when running the prime number benchmark program listed in the July 1981, issue of 68 Micro Journal.

The November 1981, 68 Micro Journal compared the results of running that program using several Pascal compilers. That data is repeated below with the addition of the OS-9 Pascal results:

PASCAL	TIME (SEC)	USER BYTES	TOTAL BYTES
OS-9 native code	27	919	6113
OS-9 pcode	56	427	10241
Lucidata	158	598	2929
Dynasoft	143	301	1490
Omegasoft	66	940	2465
TSC	59	721	14334

The OS-9 Pascal version 1.1.0 is a nearly full implementation of the ISO standard. Exceptions are that conformant arrays are not supported; procedures and functions can be passed as formal parameters only by value, not by name; and goto statements cannot branch out of their enclosing procedure or function. Many enhancements are provided including several recommended by the ANSI-IEEE Joint Pascal Committee (See Electronic Design, January 7, 1982, page 42-46). These include:

- Slicing as a method of substring manipulation, for example:  
S[2..4] := 'ABC';
- Subranges as case constants, and an otherwise option for the case statement

Additional enhancements include:

- Additional standard functions and procedures, many of which allow convenient I/O file handling for both sequential and random access files
- 5 byte format for floating point numbers, allowing over 9 digits of precision

The OS-9 Pascal documentation is excellent, with a 120 page user's manual that is concise and readable. The manual assumes that the user is familiar with the Pascal language and therefore concentrates on how to install and use the OS-9 Pascal products. The manual is very tutorial in nature, with many specific examples demonstrating the use of the language enhancements provided and the manipulation of OS-9 I/O files from Pascal. The section on error messages also provides clues as to the most likely cause of the error and/or how to correct it. The lack of an index makes the manual's use as a reference document more difficult, but the table of contents and list of figures are usually sufficient to locate needed information.

We hope the this information will be of help to any readers who may have an interest in the above products.

David and Barbara Vail  
Vail Electronics, Inc.  
(305) 729-6363



F.S. As mentioned above, we have recently become authorized dealers for both HELIX computers and Microware Systems Corporation software. As a "Going Into Business" sale we are currently offering both product lines at 10% below the current list prices.

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A CONVERSION TO GIVE 16 ADDRESSES PER PORT WITH SOUTHWEST TECHNICAL 6800's.

#### BACKGROUND.

Some time ago the Psychology Labs of Middlesex Polytechnic were faced with the problem of re-equipping. To fit the budget we decided to opt for a distributed cluster system based on the S.W.T. 6800 microcomputer, running FLEX.

A central machine runs a dual 8 inch floppy disc unit. The outlying computers are directly connected, via parallel ports, to the central machine. This enables us to run real time experiments on each outlying computer and give these computers access to the disc unit, while only having to buy one such item. The time taken to load, save and run programs is practically the same as if each outlying computer had its own disc drives.

Having started using S.W.T. 6800's we then replaced the central machine with a GIMIX 68 chassis, fitted with a 6809 CPU, to give greater capacity for plug in boards. We then purchased four S.W.T. S/09 computers as the old 6800's were no longer available.

Finding such a mixture to much of a headache we converted everything to 6809's. This left us with 4 addresses per port on the 6800's and 16 addresses per port on the S.W.T. S/09's. Two sets of software were needed to run this setup so we went for 16 addresses per port all round.

#### CONVERSION

These notes apply to S.W.T. MPB and MPB2 motherboards only.

Convert these motherboards to 6809

standard using the instructions supplied by S.W.T. with the replacement 6809 CPU card.

Remove the wire link between pins 1 and 30 on port 5 (board select and UD4).

Cut the track from address line A5 to pin 4 of IC 6 and join pin 4 (IC 6) to address line A7.

Cut the tracks from A2, A3 and A4 to pins 1, 2 and 3 of IC 3, close to the molex pins.

Reconnect these three tracks as follows:-

IC 3 pin 1 to A4

IC 3 pin 2 to A5

IC 3 pin 3 to A6

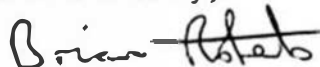
Finally connect A2 to UD3 and A3 to UD4.

UD3 and UD4 have now become register select lines 2 and 3. This gives 16 addresses per port, in conjunction with RS0 and RS1. These new register select lines do not affect the working of single port boards, but are needed if double port boards are installed.

As the original port numbers have divided by 4 the VDU serial port card should now be installed in port 0 and the minifloppy disc controller in port 1. The S.W.T. minifloppy disc controller board needs modifying to work with 16 addresses per port. S.W.T. issues a modification sheet for this.

The disc controller board for 8" drives should work in in a converted chassis but we have not tried this as our controller card is installed in the Gimix chassis which converts to 16 addresses per port by altering a switch bank and moving a socketed IC.

Yours Sincerely,



BRIAN ROBERTS

#### STAR-KITS

P. O. Box 200  
MT. KISCO, NEW YORK 10549  
February 16, 1982

Don Williams, Sr.  
'68' Micro Journal  
5920 Cassandra Smith  
Missoula TN 37363

Dear Don:

I recently received a phone call from John Tucker, a long time 68xx user, reporting a problem he encountered while running my Spell 'n Fix spelling correction program (formerly called Magic Spell). Under certain conditions, the program would write in the wrong place on a disk, clobber



files, or give the wrong number of free sectors.

On closer examination, it turns out to be a problem in TSC's Flex. Given just the right combination of events, Flex will clobber a disk in a way which makes it difficult to assign the blame.

Each time Flex writes on a disk, it needs to know where to find the next free sector to use. This information is kept in sector 3 of track 0, which is the System Information Record (also called the SIR), and Flex usually reads this sector before opening a new file for write. Once the file is open, however, Flex maintains the data in memory, and does not update the SIR itself until the file is closed. Keeping the data in memory saves much time by avoiding frequent disk accesses.

A special situation occurs when we write to two files on the same disk at the same time. Flex uses the free sector data in memory to assign sectors to the two files, alternating between the two files and assigning space to them as needed. If you close one of the two files, Flex updates the SIR on the disk, but continues to use the data in memory to assign space to the remaining file. In other words, it assumes that the data in memory is more up-to-date than the data in the SIR on the disk. If you then open and write into a new file on the same drive, space will be assigned according to the data in memory, since that is assumed to be more current than the free sector data written in the SIR.

Note the difference: when opening a new file for write, Flex usually reads the SIR to determine where to put it. But when another file is already open, Flex uses the free sector data in memory to assign space for the new file, and does not read the SIR. This is essential when writing two or more files on the same disk, since the actual free sector data in memory may be different from that last written on the disk's SIR.

But a problem arises when two different drives are involved. Flex does not check drive numbers. Whenever you open a file for write while another file is open on any drive, Flex always assumes that the data in memory (if available) is correct. That is indeed the case ... but only if you haven't switched disks!

Now, switching disks is obviously a bad idea while you're writing on them. But it is perfectly reasonable to take the disk out of a drive which is not being used, and substitute a different disk. The problem is that if a file is open on another drive, Flex will continue using the old disk data to write on the new disk, thereby putting the new file in the wrong place (perhaps clobbering existing files, and getting the number of free sectors wrong in the process).

Here is how to demonstrate the existence of the bug:

```
1. NEWDISK a diskette.
2. Load TSC Basic and enter the following program.

10 REM OPEN A FILE ON DRIVE 0
20 OPEN NEW "0.FILE" AS 1
30 FOR I=1 TO 10 : PRINT #1, "NUMBUG" : NEXT I
40 REM NOW LEAVE FILE OPEN AND START FILE1 ON DRIVE 1
50 OPEN NEW "1.FILE" AS 2
60 FOR I=1 TO 1000 : PRINT #2, "NUMBUG" : NEXT I
70 CLOSE 2
80 REM FILE ON DRIVE 0 IS STILL OPEN, BUT FILE1 ON
90 REM DRIVE 1 IS NOW CLOSED AND IT SHOULD BE SAFE
100 REM TO SWITCH DISKS ON DRIVE 1.
110 PRINT "NOW PUT A FRESH DISK INTO DRIVE 1."
120 PRINT "TYPE 'OK' WHEN READY TO CONTINUE."
130 INPUT AS : REM WAIT FOR ANSWER
140 OPEN NEW "1.FILE2" AS 3
150 FOR I=1 TO 1000 : PRINT #3, "NUMBUG" : NEXT I
160 CLOSE 1 : CLOSE 3
```

3. Put diskettes into drive 0 and drive 1. They will both be written on, so make sure they have some room and are not write protected. Now run the program.

4. When the message in lines 110-120 is printed, a file is left open on drive 0, but the file on drive 1 is properly closed so it should be safe to take the disk out. So take the disk out of drive 1, and put in your NEWDISKed disk. (Don't put in a valuable disk, because it will get clobbered!)

5. Now type 'OK' to continue the program.

6. When the program is done, get a CATALOG of the disk in drive 1. You will note that the number of free sectors is incorrect. If you have a utility (such as DIR) which allows you to examine where FILE2 is on the disk, you will also see that it is in the wrong place. Specifically, you will see that the file location and number of sectors are what they would have been if you hadn't switched to a new disk.

The particular combination of events which leads to the problem is clearly somewhat unusual (fortunately), but it is nevertheless one that can and does occur in many situations, especially when programs update or copy several disk files at the same time. It's quite possible that you have clobbered a disk in the past in just this way without knowing just why.

The solution to the problem is obviously to have TSC fix Flex so that it checks drive numbers, and always reads the SIR whenever you open a file on a drive which does not currently have an open file on it. Until such time as they do so, however, here is an alternate solution which works. Its only disadvantage is that it must be incorporated into any program which requires you to switch disks.

Flex maintains an internal drive data table which consists of 24 bytes. In MiniFlex, this table starts at \$7815; in 6800 Flex it starts at \$8a10; in 6809 Flex it begins at \$0410. This table contains six bytes for each of four drives - two bytes contain the track and sector numbers for the first free sector on the disk, two bytes contain similar data for the last free sector, and two bytes contain the number of free sectors. If the very first of these six bytes is 0, Flex will assume that the drive data is missing, and will therefore be forced to read the SIR for that drive. Hence the solution is to clear that first byte prior to opening the new file. In the above Basic program, this can be done by inserting the following line:

```
135 DR=1 : POKE HEX("D41D"),DR*6, 0
```

This line multiplies the drive number DR by 6, adds the result to the address of the table to point to the data for that drive, and pokes a 0 into that

location. Multiplication by 6 is required since there are six bytes per drive - address \$D41D is used for drive 0, \$D423 is used for drive 1, and so on (assuming Flex 9 addresses are used.)

You must obviously be careful to use the right drive number, and to avoid doing this if there is an open file on that drive (when you oughtn't switch disks anyway!)

A similar procedure can be used in machine language programs. The patch we are using in Spell 'n' Fix is as follows:

```
LOX #FCB POINT TO FILE CONTROL BLOCK
LDA B 3,X GET DRIVE NUMBER
ASL B
ADD B 3,X MULTIPLY BY 6
ASL B
LDX #D41D POINT TO DRIVE DATA TABLE
TST B CHECK FOR 0
BEQ ABX1
ABX JMX
DEC B ADD B TO X
PHE
ABX1 CLR ABX 0,X ERASE TABLE ENTRY FOR THIS DRIVE
```

In 6809 systems, the loop at ABX can be replaced by the ABX (add B to X) instruction or by a JSR to Flex's ADDBX routine.

Hope that the foregoing explains the problem and a temporary solution. Obviously the only real solution is to have TSC fix Flex, but due to the large number of copies affected (dating all the way from the first MiniFlex versions to the latest 6809 Flex), it is quite possible that their solution will be to add a sentence to the manual instructing us never to switch disks while executing a program.

Sincerely yours,

Peter A. Stark



technical systems  
consultants, inc.

Mr. Don Williams  
68 Micro Journal  
P.O. Box 849  
Hixson, TN 37343

February 17, 1982

Dear Don,

We recently received a few letters regarding problems in the FLEX Disk Operating System when changing diskettes. I would like to clarify this situation for all your readers.

In general a user should not change diskettes in a drive while FLEX is executing a program. The only safe time to change diskettes is when all files on the system are closed (not just the drive on which the diskette is being changed). This is the case when FLEX's three plus sign prompt is issued. If a user wishes to develop a program which allows the changing of diskettes during execution, he should close all files on the system before asking the operator to change diskettes. One simple way to ensure all files are closed is to execute an "FMS Close" function (at \$0403 in 6809 FLEX).

Thanks, Don, for the opportunity to spread this word. 68 Micro Journal is quite an asset to the 68XX industry.

Sincerely yours,

Daniel E. Vanada

DEV/pr

PROGRAM DUMP (INPUT,OUTPUT);

( HEX ASCII DUMP OF MEMORY )

VAR

```
( $A=$0000 ) ( ASSIGN WHAT FOLLOWS STARTING AT 40 ABSOLUTE ADDRESS )
MEMORY : ARRAY ( 0..32766 ) OF BYTE;
( $S ) ( RESUME ASSIGNING VARIABLES ON STACK )
```

```
K,L,M,N,
LINE,PAGE : INTEGER;
CH : CHAR;
```



```

FUNCTION GETPAGE : INTEGER;

( CONVERTS TWO ASCII HEX DIGITS TO THE EQUIVALENT INTEGER VALUE.
  COULD BE EXTENDED TO FOUR DIGITS BY CHANGING ARRAY DIMENSION
  AND LOOP COUNT. )

VAR
  CH : ARRAY [1..2] OF CHAR;
  K,M : INTEGER;

BEGIN
  WRITE 'A' STARTING PAGE (TWO HEX DIGITS)? ' ';

  M := 0;
  FOR K := 1 TO 2 DO
    BEGIN
      M := M * 16;
      READ (CH [K]);
      IF CH [K] IN ['0'..'9'] THEN M := M + (ORD (CH [K]) - 48);
        [ ALTERNATELY ORD (CH [K]) - ORD ('0') ]

      IF CH [K] IN ['A'..'F'] THEN M := M + (ORD (CH [K]) - 55);
        [ ALTERNATELY ORD (CH [K]) - ORD ('A') + 10 ]

    END;
  GETPAGE := M;
END;

FUNCTION HEX (NUMBER : INTEGER) : CHAR;

( CONVERTS 4 BITS BINARY TO ONE HEX DIGIT ASCII REPRESENTATION )

BEGIN
  IF NUMBER IN [0..9] THEN HEX := CHR (NUMBER + 48);
    [ ALTERNATELY, CHR (NUMBER + ORD ('0')) ]

  IF NUMBER IN [10..15] THEN HEX := CHR (NUMBER + 55);
    [ ALTERNATELY, CHR (NUMBER + ORD ('A')) ]

END;

( START OF MAIN PROGRAM )

BEGIN
  PAGE := GETPAGE;
  REPEAT
    WRITELN;
    M := PAGE * 256;

    FOR LINE := 0 TO 15 DO
      BEGIN
        K := 16 * LINE;
        WRITE (HEX (PAGE DIV 16);1,HEX (PAGE MOD 16);1,HEX (LINE);1,' ');

        FOR L := 0 TO 15 DO
          WRITE (HEX (MEMORY [M+K+L] DIV 16);1,
            HEX (MEMORY [M+K+L] MOD 16);1,' ');

        WRITE (' ');

        FOR L := 0 TO 15 DO
          BEGIN
            M := MEMORY [M+K+L];
            IF M > 127 THEN M := M - 128;
            IF M > 31 THEN WRITE (CHR (M))
              ELSE WRITE (' ');

          END;

        WRITELN;
      END;

      WRITELN;
      WRITE ('COMMAND? ');
      READ (CH);

      CASE CH OF
        'E' : BEGIN ( DON'T DO ANYTHING ) END;
        'F' : PAGE := PAGE + 1;
      END;
    UNTIL CH = 'E'; [ CASE OF 'E' TAKEN CARE OF HERE ]
  END.

```

```

'B' : PAGE := PAGE - 1;
'N' : PAGE := GETPAGE ( NO SEMICOLON HERE )
END;
UNTIL CH = 'E'; [ CASE OF 'E' TAKEN CARE OF HERE ]
END.

```

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#### NEW SS50 PRODUCTS

META LAB announces two new products for the SS50 Bus Computer ... the DAC 1220 and the ADA 800.

The DAC 1220 is a High Speed Dual Channel Digital to Analog Converter Board compatible with the SS50C Bus. This card utilizes state of the art dual buffered monolithic semiconductors. The dual channel allows many '2 Axis' control problems to be handled with this single 30 pin board. Blanking outputs are available so that external circuits can update or generate blanking during settling time. Output update of both channels can be done synchronously. This feature is very useful in such applications as XY displays.

The ADA 800 is an Eight Channel 8 Bit Analog to Digital Converter Board with a Single Channel 8 Bit Digital to Analog Converter. The A/D converter uses a standard 0 to 5V input range with the 5V reference available for easy and accurate ratio-metric measurements. The 6821 Peripheral Interface Adapter (PIA) provides simple and familiar programming of inputs, outputs, and interrupts. The typical A/D conversion time is 128 uSec (7800 samples per second). The ADA 800 can be jumpered to convert at twice this speed with reduced accuracy. A buffered continuous conversion mode is available where a single channel can be monitored with minimum software and runtime overhead. The D/A channel can be set up for standard 0 to 5V or +5V operation with zero and full scale adjustments. The settling time of 2 uSec for the D/A converter is fast enough for almost all applications.

These products are part of a series of high quality SS50 based products manufactured by Meta Lab. Watch for the upcoming release of our Z80 Soft Board Package which will allow you to run CP/M® based software on your SS50 computer.

\*CP/M is a registered trademark of Digital Research Corp.



COMPUTER SYSTEMS CENTER  
OF ST. LOUIS, INC.

13461 OLIVE BLVD.  
CHESTERFIELD, MO. 63017  
(314) 576-6020

February 10, 1982

Mr. Don Williams, Publisher  
68 Micro Journal  
5900 Cassandra Smith Road  
Hixson, TN 37343

Dear Don,

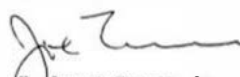
Enclosed is a copy of the latest version of DYNAMITE, which was last updated over a year ago (version 1.3:8 is still current). No bugs have been reported since then, which is quite amazing when you consider the size and complexity of DYNAMITE and the large number of copies in use. We must have done something right.

The four new files, CDLOGLM.TXT, COLORLIM.SIM, RELOCATE.CMD, and READ-HE.TXT are for the convenience of FLEX-Color Computer users. The new label files describe legal entry points, and should be valid for all versions of Color BASIC. RELOCATE is used to move DYNAMITE (or any other position-independent-code programs) to higher memory to avoid screen memory conflict problems. READ-HE describes how to do the move.

These new files will be furnished with all copies of DYNAMITE shipped beginning today. Anyone wanting an update should return his old diskette along with a check for \$10.00 to cover handling and postage.

Thanks for your continued interest in DYNAMITE. Please let me know if I can be of further assistance.

Sincerely,

  
H. Joseph Turner, Jr.  
President





technical systems  
consultants, inc.

#### PRESS RELEASE

Release Date: immediately  
Contact: Dan Vanada  
Phone: before March 6, 1982: (317) 463-2502  
after March 6, 1982: (919) 493-1451

#### Technical Systems Consultants Relocates

Technical Systems Consultants, Inc., the world's leading independent supplier of systems software for the Motorola 6800 and 6809, is relocating to North Carolina. The firm is moving its entire operation into a new 8000 square foot office facility in early March. Effective March 15, 1982, the new address and telephone number for all correspondence will be:

Technical Systems Consultants, Inc.  
111 Providence Road  
Chapel Hill, NC 27514  
(919) 493-1451

The firm will expand its staff and expects improved service for its customers.

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**COMPUWORLD Inc.**

Don Williams, Sr.  
68 Micro Journal  
5900 Cassandra Smith Rd.  
Computer Publishing Center  
P.O. box 849  
Mixon, TN 37343

Dear Mr. Williams:

We at Compuworld, Inc. would like to thank Jim Walker at Mid West Pharmaceutical Inc. for his gracious and thorough review of our new **Uniflex Payroll**.

Mr. Walker made some very good suggestions which have since acted upon:

1) Under employee maintenance, specifically the auto insertion of the new employee, information is now entered automatically with the carriage return, eliminating the need to specify "U" for update mode.

2) A new employee's name is now printed out on the payroll register along with employee number, check number, social security number, gross wages, various deductions and net amount.

In addition to these, Compuworld Inc. has added the following features to **Payroll** to further enhance the power of the package:

**Payroll** now supports multiple companies on the same diskette.

The number of employees per company is now user definable.

Taxes may be compiled through the tax tables or by a user defined percentage on the same diskette.

We certainly appreciate Mr. Walker's kind words and are doing our best to continue to make our package the best payroll package anywhere.

Sincerely,

*Marian K. Chapman*  
Marian K. Chapman  
Vice President  
Sales and Marketing

122 WHITE SPRUCE BLVD. • ROCHESTER, NY 14623 • (716) 424-6260

Don Williams, Sr., Publisher  
68 MICRO JOURNAL  
5900 Cassandra Smith Rd.,  
Mixon, TN 37343

Dear Don,

In recent correspondence with Dan Vanada of TSC, I learned that there is an error in the listing of my Global Sunrise/Sunset Calculator Program, published in last November's issue. The program still works, otherwise, I may have caught the boo-boo earlier.

Anyway, the error is on page 34, at line number 1730. It was originally:

```
1730 ND=INT((VC-1501)/100):REM ND IS NO. OF CENT.LEAP YRS.
```

and it should have been:

```
1730 ND=INT((VC-1201)/400):REM ND IS NO. OF CENT.LEAP YRS.
```

The error is an obvious one if you look at line 1728. I ran the program both ways (with and without the correction), and the corrected version is about a minute and a half different for the sunrise time calculation.

Hope I haven't inconvenienced any readers with this error. Thanks for publishing the correction, Don.

681 Whitcomb Rd., #207  
Detroit, MI 48203  
February 21, 1982

Sincerely,

*Keith Alexander*  
Keith Alexander

#### PRODUCT ANNOUNCEMENT

#### VDISK

VDISK (tm) allows FLEX(tm) users to treat extended memory as a super-fast disk drive. This "virtual" disk drive has its own directory and may contain program and data files. Files may be copied to it and from it. All FLEX(tm) utilities and user programs may read from and write to this drive, just as with any other drive.

The virtual disk operates much faster than a physical disk, however. The speed advantage apparent to the user will depend on the amount and nature of disk operations being carried out. The time required to load a binary file is negligible. For example, loading BASIC from the virtual disk takes 180 milliseconds, as opposed to 5.07 seconds with a physical disk. With operations that are less disk-intensive, the speed advantage of the virtual disk is not so dramatic, though still substantial. For example, assembling a 74 line program takes 2.17 seconds with the virtual disk, as opposed to 14.8 seconds with a physical disk. The timing comparisons presented here are for a processor speed of 1MHz with single sided, double density minifloppies. For a 2MHz processor, the virtual disk times will be halved, whereas the physical disk times would be roughly the same.

VDISK is supplied with utilities for dumping the contents of the virtual disk to a physical disk at high speed, and for reloading the virtual disk, also at high speed.

VDISK requires the FLEX(tm) operating system. The current release of VDISK requires an SWTPc(tm) compatible dynamic address translator. A future release to accommodate other extended memory addressing schemes is anticipated.

VDISK is priced at \$99 for the object version. The source for VDISK is available for an additional \$99. Further information and a list of dealers may be obtained from James Arbuckle, P.O. Box 328, Ambler, Pa. 19002 (215-643-0788).





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Don and Larry,

Here's something for a new (?) "Uniflex User Notes" column in the Journal.

We were running version 1.05 Uniflex on our S/09, using the CDS-1 harddisk as the root device, when in December we began having a lot(!) of Swap Errors. I tried every known fix (at that time), swapping memory boards, swapping interface boards, updating the processor board, and updating the MP-1D board to revision D. After all this, I still had the problem.

After talking to Wayne and Pat at SWTPC service, I took another look at the MP-1D. I ended up replacing the 74367's (IC 9 and IC 15) on it. They are hex buffer chips that mess with the address lines when they die.

This solved the problem until the harddisk power supply lost a diode. I think it died from heat/overload but needless to say, the ripple killed everything.

I think the diodes are too tight on the ARK-1D board, in the harddisk, because after only six months of operation, the board is black from heat. Again I called Wayne, and after discussing the problem with him, I decided to go back with heavier diodes (D1 thru D12). The ones that came in the machine were 1N5402's, rated at 5 amps, so I used MR824's, rated at 6 amps, as the replacements. (That was the only thing available at noon on Friday.)

Well, that brought everything up, and we have been on line since before Christmas. My software is almost completely debugged, it is in daily use by the other people in our office.

I'll try to let you know how the Service school goes when I get back from San Antonio later this month.

Jim Carter  
 3 Feb 82

## CLASSIFIED

Midwest Scientific MSI, 120K, 2 MHz, 6800, Dual FD-8 8" Disk System, Multi User SDOS Files, Multi Disk Flex, SDOS Sysgen Files, TSC 6800 to 6809 Cross Assembler, Flex Sort/Merge, TSC 6800 Debug Package, TSC 6800 Basic, Hemenway Associates Programs, TSC Business Basic, TSC Text Processor, Sort/Merge Programs, Software Library Programs, Flex Utility Program, and Manuals, with much more. New - Never been used. Please make me an offer.  
 Contact LEW 1-615-842-4601 10am-5pm.

Tano 56K Dual Serial Ports, Dual 5" Disk Drives, complete software package. Like New, Wholesale \$5300 will take \$2995. Three SWTPC 8K memory boards \$95.00 ea. PR-40 Printer, Extra Paper, And Ribbons, \$95.  
 LEW 10am-5pm 1-615-842-4601

For Sale: Tano 48K 6800 With Dual Serial Ports, Dual 5" Drives, Electric Word Processor, TSC Editor and XBasic, Flex Dos \$2395.00  
 Charles Skelton (805) 484-3162.

SWTP 6800, MPA-2, Smartbug, 32K, 2 Drives, 2 MPS, Music Software. Looks like Chieftan. \$1500.  
 (214) 352-9568.

SWTPC, 20K, MPC, MPS, AC30, Smoke BFD-2, 20 Disks, CT64 w Hitachi TV-Monitor, Teletype-Perfect \$1380.  
 Dick Hack, Annapolis, MD w(301)841-6688  
 h(301)757-2164.

SWTP MP-8M Kit. New \$125 CT-1024 Terminal \$75. MP-A CPU \$50 Panasonic TR 930 Monitors \$90.  
 James Otte D(309) 436-6607 E(309) 963-4290.

SWTP MP-68, SWTBUG, 32K RAM, MP-S, MP-P, MP-C, MP-T, CK-7(Real Time Clock), MF-68(Drives not included), AC-30, FLEX-2, TSC Disk Editor, TSC Assembler, TSC Disk Basic, TSC Utilities, TSC Text Processor, Cassette Basic, Cassette Editor/Assembler, Miniflex, Extra 6800 Chip, Mikbug Chip, Extra Disk Controller Chip FD1771, Hundreds of Hours Trouble Free, No Intermittents, Original Hardware and Software Documentation, \$1000 Buys it All!  
 Tom Gardiner, 393 El Viento, Los Alamos, NM, 87544, 1-505-662-7417 Eve.

Wanted: Disk Controller and/or Disk Drive 5"  
 Rick Michelhaugh, 211A Cairn Circle, Knoxville, TN 37923

For Sale SWTPC 6800/2 System. MPA2, BOAZ D64KB(32k), MP8M, MPS, F&D PMB1, AC30, CT1024, MPLA, PR40, MF68, DC1, MPR. Bare Boards MPS, MPLA, MPT & MP09A. Assorted Software.  
 Michael Witte 901-795-5122 Eve.

For Sale: 6809 System with GIMIX Mainframe and MP-09 CPU. Includes 56K RAM, DMAF-1 DMA Controller and two Double-Sided, Single-Density 8" Drives (1.2 Megabytes on Line). Package also includes two Serial Interface Cards, 9600 baud for terminal and 1200 baud for printer; one Parallel Interface Card set up for Centronics Interface. Also includes Microtime clock/calendar and tons of software. Just add terminal and printers(s). \$3,500 F.O.B. Morgan Hill, CA.

Call or write John Tarvin, c/o EIP Microwave, Inc., 2731 North First Street, San Jose, CA. 95134--(408) 946-5700 Ext. 216 or (408) 683-0287 Eve. for complete details and list of software.

For Sale: Assembled boards--MP-T Interrupt timer \$75; MP-N Calculator card \$75; Microworks 2708 Eprom Burner and Eprom Board with software listing and source/object on 8" Single Density disk \$175; Microworks Kluge Board \$24; Programmable sound generators (2) wire-wrapped on Microworks Kluge Board. With software listings and Source/Object on 8" Single Density disk. Just plug into your stereo for some wierd effects. \$60; SWTP 4K static memory board \$30; MP-R Eprom Programmer \$100.

Call or write John Tarvin, c/o EIP Microwave, Inc. 2731 North First Street, San Jose, CA 95134 (408) 946-5700 Ext. 216 or (408) 683-0287 Eve.

Dual 5 1/4 Disk Drives with 6800/6809 software and controller, \$500.00. 32K Dynamic Mem, 200.00. Many more \$50 items.  
 Call Ben Bezenek, 612-733-3650(D) or 612-631-2732(E).

Wanted SWTPC Interface for IBM 50 Typewriter.  
 O(919) 355-655 H(919) 756-7742 Harold Zallen

F&D PMB-1 Video Board w/FADBUG \$175., SWTP MPA2 RAM Card \$45., SWTP 4K Card w/2K POP. and 2K Parts \$35., SWTP MPC \$15., OKI 2K X 8 Ram for SWTP S32 Card (STDP, Nont) \$65., NEWTECH Music Board w/2 NEWTECH Tapes \$50.00.  
 T. Adams, 11608 Broad Oaks, Austin, TX 78759

## HELP

5905 Pratt St.  
 Alexandria, VA 22310  
 Feb. 9, 1982

Dear Don,  
 I have an Altair 680b which I haven't used in several years. I intend to adapt it to a SS-50 bus and do whatever it takes to interface it to a disk



system. I would like to know if anybody out there has done it before, and if it worked.

Yours truly,

Daniel J. Horowitz  
(703) 971-6285  
\*\*\*

Joseph G. Gay  
2102 Liberty Rd.  
Chelsea, AL 35043  
Jan. 27, 1982

Dear Don, I have enjoyed our phone conversation today- your comments were helpful.

I would appreciate any information you may have about expansion of the Heath Kit Microprocessor Trainer Model ET-3400. This is a 6800 based system with fully buffered address and data register and fully accessible plug-in connector for all microprocessor functions.

I would like to know if there is a good Basic interpreter program available (preferable in ROM) that could interface with this system. Also would like about 32K of memory-preferably from supplier of readily available boards.

A 40 Pin Connector is on board-maybe this could easily adapt to a SS-50 bus?

Joseph Gay

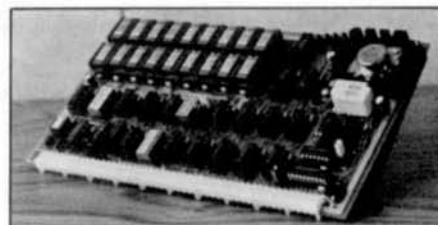
\*\*\*

HELP! Please could you explain what changes must be made to a model 1,111,11 program to run on a color computer w/ext. color basic (16K) 6809(E) CPU. Model 1,111, Z-80 (or convert 6502 language (Atari/Apple) to 6809(E).

Thank you very much,  
Peter J. Ardita  
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## QMM1

### Quarter Megabyte Memory



The **QMM1** is a "Quarter Megabyte" (256K) dynamic parity memory board for SS50-C 6809 systems with 20 bit addressing.

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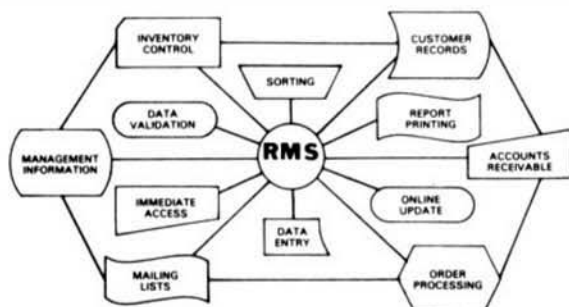
# 6809

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# RMS

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Disk-5: \*DISKFIX 1, \*DISKFIX 2, \*\*LETTER, \*\*LOVESIGN, \*\*BLACKJAX, \*\*BOWLING.

NOTE: All are as published or received by 68 Micro Journal, some have fixes and patches.

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# CP/M ON THE SS50 BUS

## Why CP/M?

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## How to Install the Z809

Plug the Z809 Board in an unused SS50 slot. On most SS50 systems, just boot the system using the CP/M disk and you are up and running.

## How does the Z809 Softboard System work?

All I/O functions of the Z809 are directed through the 68XX system CPU board. Use your current disk controller board, CPU, memory, disk drives, printer, and terminal with standard SS50 configurations. The Z809 does not affect use of the system CPU. The Z809 does not replace your CPU board. It uses your 68XX as an I/O processor. To change back to your current operating system, just boot the system with the appropriate disk.

## Other Information on the Z809

META LAB has designed the basic input output system (BIOS) of CP/M to take full advantage of the physical characteristics of your disk drive. The BIOS drivers allow you to dynamically specify all characteristics of your diskettes and drives. The software is reconfigurable when you expand your system with more memory or hard disks. **The Z809 is compatible with standard CP/M formatted disks so that software is easily exchanged or ported to other systems.** The board runs at 4 Mhz when run on a 2 Mhz SS50 system. It executes Z80/8080 object code. Minimum memory requirements are 24K, however some application programs require 56K. The Z809 supports up to 56K of system memory. The Softboard is designed to be an expandable and adaptable part of your total computer system.

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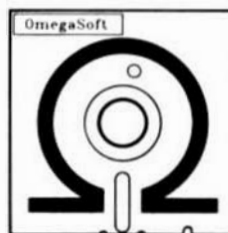
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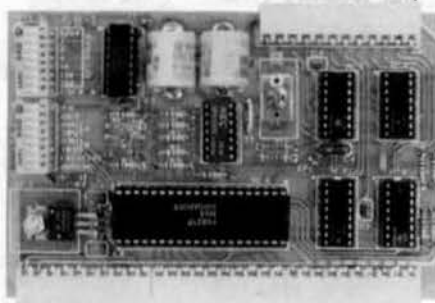


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- Using your computer as a general-purpose 300-baud terminal
- Downloading programs from other computers

The Microtext module is a program pack containing not only firmware but a second serial port so that both your printer and modem can be connected at the same time. Microtext can be configured for any serial printer that will work with the Color Computer, even if it requires line feeds! But even if you don't have a printer, you can keep a permanent copy of your data by storing to cassette tape. Also, any Radio Shack/Centronics-compatible parallel printer may be used by adding the Micro Works' P180C parallel interface.

For those of you with special terminal applications, Microtext has selectable parity; it sends odd, even, mark or space. With mark parity (which is default) you can send to computers requiring either seven or eight bits. All 128 ASCII codes can be sent. Exchange programs with other Color Computer users! Basic programs may be downloaded from other computers or timesharing systems.

You'll find many uses for this versatile module! Available in ROMPACK, ready-to-use, for \$59.95.

## EDITOR/ASSEMBLER

The Micro Works Software Development System (SDS80C) is a complete 6809 editor, assembler and monitor package contained in one Color Computer program pack! Vastly superior to RAM-based assemblers/editors, the SDS80C is non-volatile, meaning that if your application program bombs, it can't destroy your editor/assembler. Plus it leaves almost all of 16K or 32K RAM free for your program. Since all three programs, editor, assembler and monitor are co-resident, we eliminate tedious program loading when going back and forth from editing to assembly and debugging!

The powerful screen-oriented Editor features finds, changes, moves, copy and much more. All keys have convenient auto repeat (typematic), and since no line numbers are required, the full width of the screen may be used to generate well commented code.

The Assembler features all of the following: complete 6809 instruction set; complete 6800 set supported for cross-assembly; conditional assembly; local labels; assembly to cassette tape or to memory; listing to screen or printer; and mnemonic error codes instead of numbers.

The versatile ABUG monitor is a compact version of CBUG, tailored for debugging programs generated by the Assembler and Editor. It features examine/change of memory or registers, cassette load and save, breakpoints and more. SDS80C Price: \$89.95

## MACHINE LANGUAGE

**MONITOR TAPE:** A cassette tape which allows you to directly access memory, I/O and registers with a formatted hex display. Great for machine language programming, debugging and learning. It can also send/receive RS232 at up to 9600 baud, including host system download/upload. 19 commands in all. Relocatable and reentrant. CBUG Tape Price: \$29.95

**MONITOR ROM:** The same program as above, supplied in 2716 EPROM. This allows you to use the entire RAM space. And you don't need to re-load the monitor each time you use it. The EPROM plugs into the Extended Basic ROM Socket or the Romless Pak I. CBUG ROM Price: \$39.95

**SOURCE GENERATOR:** This package is a disassembler which runs on the color computer and generates your own source listing of the BASIC interpreter ROM. Also included is a documentation package which gives useful ROM entry points, complete memory map, I/O hardware details and more. A 16K system is required for the use of this cassette. 80C Disassembler Price: \$ 9.95

## LEARN 6809!

**6809 ASSEMBLY LANGUAGE PROGRAMMING,** by Lance Leventhal, contains the most comprehensive reference material available for programming your Color Computer. Price: \$16.95

# HARDWARE

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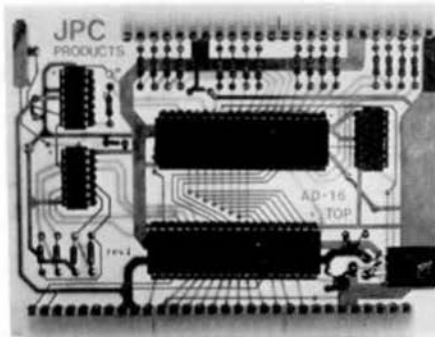
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HUMBUG is available for 6800, 6802, and 6809 CPU boards made by SWTP, Gimix, Percom, and Star-Kits. It supports a serial terminal, or video boards made by Percom, Thomas, or F&D. It comes in either 2708 or 2716 EPROMs, and in either 2K, 3K or 4K versions, at prices ranging from \$40 to \$75 which include a full manual and full source code. There are several versions, depending on your hardware configuration, and it's a good idea to get our catalog and HUMBUG spec sheet first.

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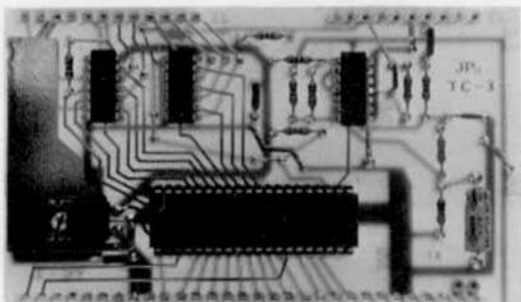
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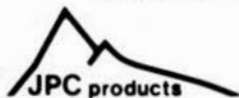


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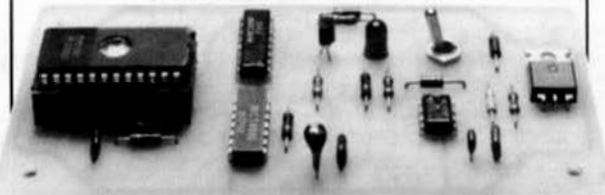
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**SCREDDITOR III DOES IT ALL.**

This ad is being prepared using the XEROX III word processor. Unlike any other editing package available for the IBM PC, the XEROX III word processor has a built-in spell checking capability. This was a very important time, for not and paste was used to prepare it (well, not actually... our printer only prints 132 columns wide). The XEROX III is a true screen editor. That you are reading this on what we call an "editor" is the result of the fact that we had to use the text editor with full editing capabilities. The XEROX III word processor has a built-in multi-column insert, delete, paragraph splitting and merging and edit capability of up to 251 characters. This means that you can define page footers and headers in a breeze! Also, you can define page footers and headers automatically, delete and then re-insert them, or change them at any time.

You can merge multiple files with **SCREENITOR III**. You can also merge multiple files into a single file. **SCREENITOR III** allows you to specify where to insert a file, and you can specify the file to be inserted. You can also specify the file to be extracted. You can extract a copy from the current file into another file. You can even do form letter printing with **SCREENITOR III**. You can also do automatic merging for custom mailings.

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SCHEIDT III is available to run under DOS/MS versions 3.0 and 4.0 and all versions of dBASE PLUS (except PLUS-3). For 100MB, PLUS-3 and OS-9, SCHEIDT III may be ordered on 5- or 8-inch disk.

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**GENERAL INFORMATION ..**

Please specify all programs (except Color Computer) on the order form. If you wish to have the program shipped on 5-inch disk for PAGE-2, PAGE-3, and OS-9, Virginia costs and shipping charges will be added.

### SCREDDITOR III DOES IT ALL...

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 The board supports DMA by either HALT or BUSREQ when a 6809 CPU is used.

DMA to the devices on the CPU card is not supported.  
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Each port is terminated at two 16 pin dip sockets, one socket configured for modem and the other socket configured for terminal or printer. RTS, CTS, DTR, DCD, DSR are appropriately implemented.

Each port has independent selection of baud rate.  
 Each port allows the interrupt request to be jumpered to the IRQ or FIRQ/NMI bus line.

Bare board: \$20.00\*      Kit: \$60.00\*      Assembled: \$80.00  
 Assembled cable (two required for each interface board): \$20.00 each

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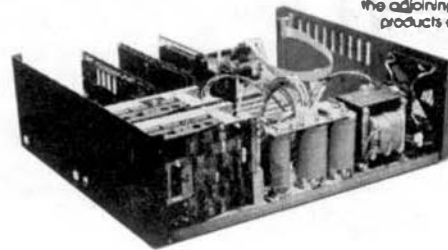
Bare board: \$20.00\*      Kit: \$60.00\*      Assembled: \$80.00  
 Assembled cable (two required for each interface board): \$20.00 each

# ELEKTRA

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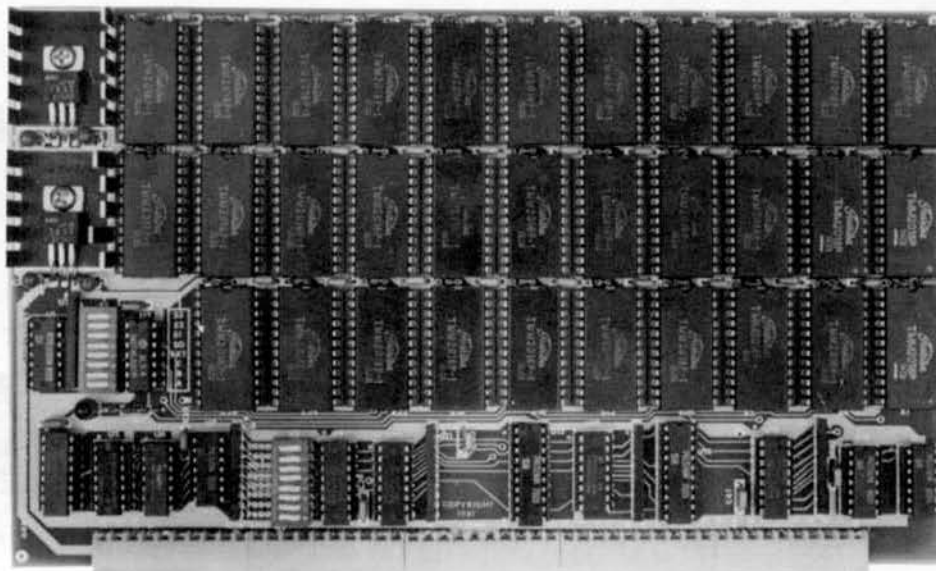
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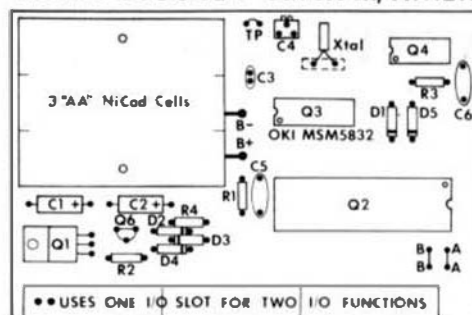
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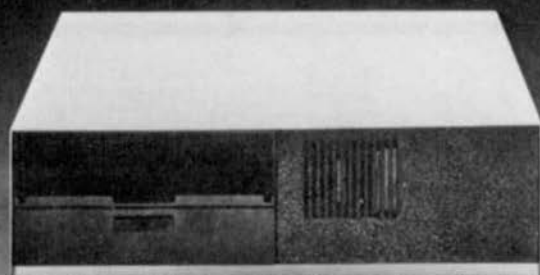


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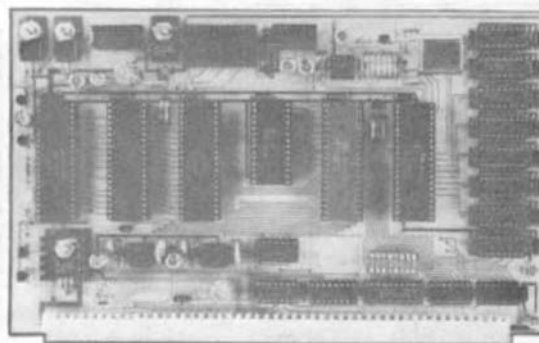
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Manual and disk with both source and object code ..... \$75.00

### STANDARD MODEM PROGRAM

Same as Super Modem Program above but without ECHO option, CR/LF for CR option, slow disk file transmit option, nor X-on/X-off option. Reception of disk files is limited to those small enough to completely fit within the receiving buffer.

Please specify 6800 SSB, 6800 FLEX™, or 6809 FLEX™, 5" or 8"

Manual with instructions, source listing, and flow chart; disk with both source and object code ..... \$45.00

### ALL IN ONE

Editor - Text Processor - Mailing Labels

Mailing Lists - Use any CRT terminal and printer

Supports Editing commands such as bottom, change, delete, find, insert (single line), input (multiple lines), list, next, overlay (with cursor editing, character deletion and insertion), overstrike (for selected darker text), print, restart, set, top, underline, up, and verify

Supports Text Processing commands such as block copy, block move, centering, margin justification (widen and narrow), paging, and tabbing

Mailing Lists and Labels. Use the same mailing list disk file (with protected areas) for both mailing labels and repeat letters. Repeat letters are personally addressed to each person or selected persons on the mailing list.

Most Powerful File Handler found in any editor. Append one file to the end of another, or insert (merge) one file into another as designated by the line pointer. Print specified lines to your printer or to a disk file. Edit files larger than the text buffer. Does not produce output files when not desired. Delete disk files from the editor.

Printer commands. Control Characters can be sent to the printer for format control either directly from the control terminal or by imbedding them in the text. The set command contains interface initialization and character output routines to support the SWTPCMP-C interface as well as the standard serial and parallel interfaces. Jumpare are also provided to user supplied printer routines. User selects the port address (0 thru 7, A or B) thereby eliminating the need for the user to install printer software routines. Editor can be initialized for either 4 or 16 addresses per port.

Editor allows exiting to either the monitor or DOS and then reenter (Warm Start) without destroying previously prepared text in the buffer. The Restart command erases contents in the buffer without the user having to reload the Editor.

The Editor allows the user to toggle between full duplex (no echo) and half duplex (echo) as needed. It responds to commands in both upper and lower case and can be used to create assembler source code and Basic programs as well as text.

Specify 6800 SSB, 6800 FLEX™, 6809 FLEX™, 5" or 8" ..... 50.00

Printed source listing is available for an additional: ..... 35.00

### Software by Technical Systems Consultants, Inc.

Flex™ (includes Editor and Assembler) ..... 150.00

UniFLEX™ (includes one year maintenance and update) ..... 450.00

Editor ..... 50.00

Assembler ..... 50.00

6800 Cross Assembler on 6809 ..... 250.00

6809 Cross Assembler on 6800 ..... 100.00

Text Processor ..... 75.00

Extended Basic ..... 100.00

Basic Precompiler (specify standard or extended) ..... 50.00

Pascal (Flex™) ..... 200.00

Pascal (UniFLEX™) (Add \$75.00 for one year's maintenance and update) ..... 5.00

Sort/Merge Package ..... 75.00

6809 Flex™ Utilities ..... 75.00

6800 Flex™ Utilities ..... 100.00

Debug Package ..... 75.00

Diagnostic Package ..... 75.00

### Software by Microware Systems Corp.

OS-9™ Level One Operating System ..... 75.00

OS-9™ Level Two Operating System ..... 75.00

BASIC09™ ..... 75.00

OS-9™ Macro Text Editor ..... 300.00

OS-9™ Interactive Assembler ..... 300.00

OS-9™ Interactive Debugger (Disk version) ..... 100.00

CIS Cobol Compiler ..... N/A

Pascal Compiler ..... N/A

SWTPC ..... Kit Assembled

DMF2C Disk Controller Board (NEW) ..... 595.00

DMF2C Disk Controller Board (Used, very limited quantity) ..... 525.00

SWTPC FLEX™ Disk and manual ..... 35.00

SWTPC FLEX™ Disk without manual ..... 10.00

OC-2 Disk Controller (Limited Quantity) ..... N/A

4K RAM Board (Limited Quantity) ..... N/A

SBUG-E (2716 compatible) ..... 19.95

SWTBUG (6830 compatible, limited quantity) ..... 19.95

SWTBUG (2716 compatible) ..... 19.95

MP-S2 Serial interface (dual port) ..... N/A

MP-LA Parallel interface (dual port; limited quantity) ..... 40.00

MP-L2 Parallel interface (dual port) ..... 60.00

MP-R Single voltage 2716 prom programmer ..... N/A

MP-N Calculator board ..... 54.95

MP-T Interrupt timer ..... 92.00

MP-8M 8K 4044 Memory board (limited quantity of kits) ..... 150.00

S32 Universal Static Memory Board ..... N/A

MP-09 6809 CPU board ..... N/A

69 Chassis, P.S., 6809 CPU, 8K, RAM, One Serial Port ..... N/A

Universal 68XX Bare Motherboard, 6800/6809, 4 16 address, 8 per port, 8 50 pin/8 30 pin slots, baud rate generator, 15 1/8" x 9 3/8" ..... 80.00

F & D (bare) Motherboard I, 12 50 pin/8 30 pin slots, 4/8 (modifiable to 16) addresses per port, complete address decoding, 18" x 9" ..... 55.00

Connectors (10 pin, Titanium-Tin plated 5 microns for near gold quality) ..... each 50

Male with square cross section pins ..... each 75

Female ..... each 75

SMOOTH™ and ELEKTRA™ are trademarks of AAA Chicago Computer Center

FLEX™ and UNIFLEX™ are trademarks of Technical Systems Consultants, Inc.

OS-9 and BASIC09 are trademarks of Microware Systems Corp.

GIMEX™ and GHOST™ are registered trademarks of GIMIX Inc.

## ELEKTRA™ SS50 Computer Products

ELEKTRA Dual drive cabinet for 5 1/4" drives with power supply, time cord, fuse, power switch, and power cable to drives ..... 125.00

ELEKTRA Dual drive cabinet, power supply, power cable to drives ..... 350.00

Cabinet for dual 8" drives only ..... 25.00

Power supply for dual 8" drives only ..... 1.00

PS cables only (Specify brand and type of 8" drives) ..... 30.00

### Special Software

4K 6809 HUMBUG ..... 75.00

4K 6800 HUMBUG IRAM needed at \$A000 and \$D000 ..... 65.00

2K 6800 HUMBUG I with cassette LOAD and PUNCH ..... 40.00

2K 6800 HUMBUG (Extra commands instead of cassette software) ..... 40.00

Other HUMBUG versions including video versions are available

Spell'n Fix by Peter Stark ..... 89.95

Dynadisc Disassembler ..... 80.00

SUPER SLEUTH Disassembler System ..... 9.00

### DISK DRIVES 30 day guarantee, single and double density capability

5 1/4", single head, 40 tracks ..... \$250.00

5 1/4", single head, 40 tracks, floppy ..... 260.00

5 1/4", double head, 40 x 2 tracks ..... 335.00

5 1/4", single head, 80 tracks ..... 335.00

5 1/4", single head, 80 tracks, floppy ..... 345.00

5 1/4", double head, 80 x 2 tracks ..... 470.00

MPI - Service Manual ..... 20.00

Siemens Manual ..... 10.00

8", single head, 77 tracks ..... 375.00

8", single head, 77 tracks, Thin-Line ..... 450.00

8", double head, 77 x 2 tracks ..... 4.50

8", double head, 77 x 2 tracks, Thin-Line ..... 529.00

Microtime 6800 Calendar and Clock Board (assembled and tested) ..... 105.00

Bareboard, connector, and documentation only of above ..... 35.00

(See review Feb 1980 66 Micro Journal)

Microtime I ..... 89.95

Data Mart 16K EPROM bareboard (270 8 chips) ..... 30.00

### Printers

Epson MX-80 (Centronics compatible, parallel interface) ..... 475.00

(with Serial RS-2 interface option) ..... add 75.00

Spare Print Head ..... 39.95

Spare ribbon cartridge ..... 15.00

C. Itoh Comet I 125 cps, 9 x 7, bidirectional, serial or parallel ..... 425.00

OkiData Microline 82A 120 cps, 9 x 9, bidirectional, serial and parallel ..... 4.50

Tractor for 82A ..... 70.00

Optimal Technology, Inc. EP-2A-79 Eprom Programmer ..... 169.00

(Personality Modules extra for above programmer)

Optimal Technology, Inc. 30 pin parallel I/O board for EP-2A-79 ..... 35.00

Software package for EP-2A-79 (Specify 6800 or 6809) ..... 29.95

EP-2A-79-M-01 Eprom Programmer I/O configurations and supplies power) ..... 79.95

### Smoke Signal Broadcasting

Monitor on 2716 with manual (Specify Chetlain or SWTPC) ..... 75.00

DCB-4A Double Density Controller Board for 5" and 8" with DOS ..... 449.00

DOS69D OS Update with Editor and Assembler (Specify 5" or 8") ..... 75.00

LMB-1A Motherboard ..... 299.00

SCB-69 6809 CPU Board ..... 299.00

M-16-X 16K Static Memory Board ..... 185.00

M-24-X 24K Static Memory Board ..... 295.00

M-32-X 32K Static Memory Board ..... 395.00

### GIMIX

2 MHz 6809 Plus CPU, time of day clock, battery backup, 1K NMOS RAM ..... 578.05

CMOS RAM substitution ..... 8.00

GIMIX Dynamic Address Translator ..... 35.00

SWPTC compatible OAT ..... 15.00

Missing cycle detect card ..... 38.23

Disk Controllers (All have data separators and can be used with either single or double headed drives)

5" single density controller without 1771 chip ..... 158.38

5" single density controller complete ..... 198.48

5" and 8" single density controller complete ..... 226.58

5" double density controller with variable precomp ..... 298.28

OMA 5" AND 8" double density controller with variable precomp ..... 588.68

GIMIX version of FLEX™ (without Editor and Assembler) ..... 90.00

Double disk regulator card ..... 68.22

Ribbon cable for two 5 1/4" disk drives (short) ..... 34.96

Ribbon cable for two 5 1/4" disk drives (long) ..... 44.96

Ribbon cable for two 8" disk drives (long) ..... 44.26

8" disk drive cabinet with power supply ..... 848.18

Memory ..... 75.00

CMOS WITH BAT. BACKUP ..... NMOSNO BAT. BACKUP

16K Static RAM Board with control registers\*\* ..... 368.18

32K Static RAM Board with 32K of RAM installed\*\* ..... 375.00

\*\* discontinued, limited quantity available

64K Static RAM Board with 24K of RAM installed ..... N/A

64K Static RAM Board with 32K of RAM installed ..... 348.27

64K Static RAM Board with 48K of RAM installed ..... 518.47

64K Static RAM Board with 56K of RAM installed ..... 578.57

64K Static RAM Board with 64K of RAM installed ..... 638.67

16 Socket EPROM/ROM/RAM Board ..... 238.32

8K Promboard (2708) ..... 98.34

4K PPD 4K Prom Board and 270 8 Prom Burner ..... 198.00

I/O Boards

Single port 30 pin serial interface (Requires 1 cable set) ..... 88.41

Dual port 30 pin serial interface (Requires 2 cable sets) ..... 128.43

8 port 50 pin serial interface with baud rate generator ..... 318.46

Dual port 30 pin parallel interface (Requires 2 cable sets) ..... 88.42

8 port 50 pin parallel interface with interrupt generator ..... 198.45

Cable sets for above boards (specify board) ..... 22.95

2MHz 6809 PLUS Computer System with 56K Memory\* ..... 2498.29

Above System with 48K Controller and Special Software Pkg. \* ..... 2988.59

Above System with 48K Controller and Special Software Pkg. \* with CMOS RAM and Battery Backup ..... 3248.49

Mainframe (Chassis, PS, Switches, Fan, Motherboard, Baud Rate Gen.) ..... add 300.00

Shipping and handling estimates: ..... 1198.19

Within the Continental U.S., please add 3% (\$5.00 minimum)

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Prices and inventory are subject to change without advance notice.

This is our catalog.



# UNIFLEX\* SOFTWARE

by  
**Uni Comp . . .**

## ACCOUNTS PAYABLE

UniComp Accounts Payable is designed to enable complete control over a business' obligations. The program automatically ages invoices, and generates checks.

- Maintains a detailed vendor file.
- Generates comprehensive reports instantly to facilitate cash flow management.
- Completely menu driven requiring no special programming knowledge or experience.
- Interactive with UniComp General Ledger.

**\$595**

## ACCOUNTS RECEIVABLE

UniComp Accounts Receivable is designed to enable complete control over a business' receivables. The program automatically ages accounts, and generates statements.

- Maintains a detailed vendor file.
- Generates comprehensive reports instantly to facilitate cash flow management.
- Is completely menu driven requiring no special programming knowledge or experience.
- Is interactive with UniComp General Ledger.

**\$595**

## GENERAL LEDGER

The UniComp General Ledger package is a comprehensive program enabling a business to completely integrate and automate its accounting package.

- Designed to accommodate the entire ledger function and uses Generally Accepted Accounting Principles.
- Supports multiple companies/ profit centers on the same diskette.
- Provides an optional full restriction feature.
- Is fully interactive with other UniComp accounting packages.

**\$995**

## INVENTORY/ ORDER ENTRY

The UniComp Inventory/Order Entry software package is a comprehensive program designed for global resource management. Its purpose is to integrate and automate all aspects of inventory/order entry processing.

- Generates purchase orders and reports, as well as vendor information files.
- Provides cash register operation enabling real time inventory update capabilities.
- Completely menu driven, with easy to read and understand instructions.

**\$995**

## UNIBASE

UniBase is a stand alone data base management program enabling users to design their own data base program. It is menu driven with no programming knowledge or experience necessary to use it.

- Allows up to 10 key fields, user filled.
- Automatically computes total record length and maximum number of records available based on storage available.
- May be sorted on any field or any combinations of fields.
- Allows restrictions on data access by terminal.

**\$795**

## MAILING LIST

The UniComp Mailing List program is designed to enable a business to handle all aspects of mailing list processing.

- Enables the user to maintain accurate customer/client lists and update them at any time.
- Automatically sorts records in alphabetic order or zip code order.
- User may elect to sort records using any combination of parameters.
- Supports the printing of mailing labels—one, two, or three across, user designated.

**\$495**

## PAYROLL

UniComp Payroll is designed to completely automate all aspects of payroll processing.

- It generates payroll checks and stubs, internal reports, governmental reports, W2 and other reports.
- Enables the user to maintain extensive (70 fields) employee files.
- Completely interactive with the UniComp General Ledger package.
- Supports multiple companies/subsidiaries on the same diskette.

**\$795**

### Minimum Equipment Requirements Computer/Display

A South West Technical Products computer with a minimum of 128K bytes of memory, 2 eight inch floppy double density, double sided disk drives, and at least on SWTPC 8212 or 8212W terminal.

### Printer

Any ASCII compatible printer with at least a 132 column print width.

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For further information call:  
**716-424-6260**



# THE CHIEFTAIN™ 5¼-INCH WINCHESTER HARD DISK COMPUTER

SO ADVANCED IN SO MANY WAYS . . .  
AND SO COST-EFFECTIVE . . .  
IT OBSOLETE MOST OTHER SYSTEMS  
AVAILABLE TODAY AT ANY PRICE.



## ● HARD DISK SYSTEM CAPACITY

The Chieftain series includes 5¼- and 8-inch Winchesters that range from 4- to 60-megabyte capacity, and higher as technology advances. All hard disk Chieftains include 64-k memory with two serial ports and DOS69D disk operating system.

## ● LIGHTNING ACCESS TIME

Average access time for 5¼-inch Winchesters is 70-msec, comparable to far more costly hard disk systems. That means data transfer **ten-times faster** than floppy disk systems.



## ● 2-MHZ OPERATION

All Chieftains operate at 2-MHz, regardless of disk storage type or operating system used. Compare this to other hard disk systems, no matter **how** much they cost!

## ● DMA DATA TRANSFER

DMA data transfer to-and-from tape and disk is provided for optimum speed. A special design technique eliminates the necessity of halting the processor to wait for data which normally transfers at a slower speed, determined by the rotational velocity of the disk.

## ● RUNS UNDER DOS OR OS-9

No matter which Chieftain you select . . . 5¼- or 8-inch floppy, or 5¼- or 8-inch

Winchester with tape or floppy back-up . . . they **all** run under DOS or OS-9 with **no need** to modify hardware or software.

## ● UNBOUNDED FLEXIBILITY

You'll probably never use it, but any Chieftain hard disk system can drive up to 20 other Winchesters, and four tape drives, with a single DMA interface board!

## ● SMOKE SIGNAL'S HERITAGE OF EXCELLENCE

This new-generation computer is accompanied by the same **Endurance-Certified** quality Dealers and end-users all over the world have come to expect from Smoke Signal. And support, software selection and extremely competitive pricing are very much a part of that enviable reputation.

## 20-Megabyte Tape Streamer Back-Up Option

Available with all Chieftain hard disk configurations. This cartridge tape capability provides full 20-megabyte disk back-up in less than five minutes with just one command, or copy command for individual file transfers. Transfers data tape-to-disk or disk-to-tape. Floppy back-up is also available in a variety of configurations.

## The Chieftain Computer Systems:

Here are the Chieftain 6809-based hard disk computers that are destined to change the data processing industry . . .

### ☐ CHIEFTAIN 95W4

4-megabyte, 5¼-inch Winchester with a 360-k floppy disk drive (pictured).

### ☐ CHIEFTAIN 95XW4

4-megabyte, 5¼-inch Winchester with a 750-k octo-density floppy disk drive.

### ☐ CHIEFTAIN 98W15

15-megabyte, 5¼-inch Winchester with a 1-megabyte 8-inch floppy disk drive.

### ☐ CHIEFTAIN 9W15T20

15-megabyte, 5¼-inch Winchester with a 20-megabyte tape streamer.

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...so why write a program? This and more are easy with...

#### DMS2/VM DATA MANAGER \$100.00

A complete Data Management System which permits files up to 1000K, precision BCD arithmetic, Multi-key access, selection and sorting. DMS2/VM employs a virtual memory access method under which programs "think" that entire files are in memory and directly accessible. The system supports alphanumeric, numeric, decimal, integer, coded and hexadecimal field types. Up to 24 fields and 12 levels per file may be defined by the user. A simple high-level command language allows a variety of data manipulation including reformatting, calculations, inquiry, key-merge, summation, print and display of database data.

#### ACC2/VM ACCOUNTING SYSTEM \$350.00

All essential accounting and bookkeeping functions including journal, ledger, income statement and balance sheet. The user defines accounts, products and transactions to the system and thus tailors it to his own retail, wholesale or service environment. The system operates under DMS2/VM which permits custom reports of product movement or account status to be generated. Accounts receivable and payable are integral to the system as is point-of-sale capability.

#### MFT1 MULTI FIXED TASK O/S \$100.00

Allows user definition of region size for up to eight terminal/tasks and emulates PLEX O/S for simultaneous execution of each task. Allows cross scheduling (eg: printer regions) and inter-task communications.

#### UTILITIES "A LA CARTE" - \$5.00 EACH

DUMP - Output any section of memory in hex and ASCII. DDBK - Output any disk sector in dump format. DMAP - Output track & sector chain of any file. PMAP - Output load map of command files. LISTD - Output all directory info on files. LISTDS - Output selected directory info in three columns. LISTF - List file with disk id & date heading. KILL - delete files without "are you sure" prompt. FIND - Output all file records containing a given string. MCOPT - Copy files between disks using one drive. All load at \$5.00 and output may be directed to CRT, printer or disk. MINIMUM ORDER - \$25.00. All 10 for \$40.00.

All software is written in modular assembler and runs on SWTPC 6809 with FLEX O/S and 8" disk. DMS2/VM Ver 1.3 requires 56K or more RAM. Ver 2.0, available soon, runs in 16K or more and is MFT1 compatible. Manuals available. DMS2/VM \$10., Accounting \$15., deduct from order. Add P&H \$2.50, Foreign \$5.00, N.Y. State add sales tax. No C.O.D. Send Purchase Orders on letterhead or Check orders to!

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Consultant 914-941-2552 (4-11 P.M. EST)  
 Sales Dept 212-899-1410 (VISA, P.C. orders, Catalog requests)

FLEX is a trademark of Technical Systems Consultants, Inc.



6809

DUB

## UNIFLEX\* BASIC DECOMPILER

DUB WILL CREATE SOURCE  
STATEMENTS FROM PROGRAMS  
COMPILED WITH TSC BASIC AND  
RUNS UNDER TSC UNIFLEX.

- SAVE \$'s: DO YOUR OWN PROGRAM CHANGES
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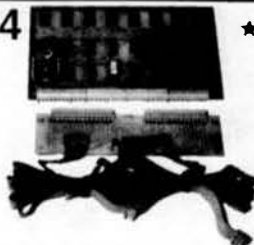
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★ CT-64

★ CT-1024



### ★ DMA VIDEO ADAPTER FOR YOUR TERMINAL

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J.B.I. adapter with memory \$142.50, J.B.I. adapter without memory \$129.50, Source Code on Disk \$5.00 — Tape \$3.50

Provide your system configuration and software.  
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### Johnson Micro Computer

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# THOMAS INSTRUMENTATION

## THE MACHINE TOOL, INDUSTRIAL SPECIALISTS IN BUSINESS ON A FULL TIME BASIS FOR 10 YEARS

### NEW PRODUCT:

### S-R/R

#### 48K 2MHz STATIC RAM/ROM CARD

- \*24 2K blocks memory mapped on any 2K boundary
- \*uses low power 2016P-2 (2128) RAM and/or 2716 ROM
- \*mix 4K blocks of RAM and ROM
- \*6800 and 6809 compatible
- \*use on SS-50 and SS-50C buss
- \*decoded for extended addressing
- \*5 volts only
- \*low power consumption (typ. 1/2 amp with 48K RAM)
- \*gold connectors

Bare Board \$49.00 2716 1MHZ \$9.95 2016 P-2 2MHZ \$16.50

A/T with 16K \$250.00; with 32K \$375.00; with 48K \$495.00

A/T without memory chips \$120.00

### NEW ACCESSORIES FOR 68XX USERS:

SS-50/SS-50C EXTENDER CARD

\$35.00

SS-30 EXTENDER CARD

\$25.00

- \*Both cards assembled with a built in logic aid & gold edge connectors

SS-30 WIRE-WRAP/PROTOTYPE BOARD (board only)

\$20.00

- \*Pad spacing permits most standard sockets from 8 to 64 pins

- \*Provision has been made for voltage regulators

### FEATURED PRODUCT: SP-1 Bare card \$49.00 Asm. + tested \$195.00

- \*A super prototype board

- \*Card design includes

(3) 6821 6 parallel ports

(4) 6850 4 serial ports

(1) 6840 3 16 bit counter/timers

which are fully buffered and decoded

- \*Accommodates a mix of 38, 14 & 16 pin wire wrap sockets

- \*Pad spacing permits most standard sockets from 8 to 64 pins

### MODEM CARD

special parts kit

B/C \$ 49.00

A/T without extra features

\$195.00

A/T with extra features

\$395.00

- \*SUPER CPU assembled with source listing

\$325.00

Software obj. & src. on FLEX disk

\$ 10.00

without 2K EPROMS (2-2708)

\$235.00

### BACKPLANES AND MOTHERBOARDS

- \*Monitor in two 2708 EPROMS

\$ 29.00

\*16 position SS-50

\$80.00

- \*CPU bare card, doc., & src.

\$ 59.00

\*12 position SS-50

\$60.00

- \*VIDEO RAM asm. 7x9 chars 64x16

\$195.00

\* 8 position SS-50

\$40.00

- \*VIDEO RAM bare, doc, Xtal, src.

\$ 49.00

\* 6 position SS-50

\$30.00

- \*PARALLEL I/O asm 100 I/O lines

\$ 49.00

\* 4 position SS-50

\$20.00

incl. 5 PIA's for 10 ports

\$139.00

\* 8 position SS-30

\$39.00

- \*PARALLEL I/O bare card & doc.

\$ 49.00

\*\*Connectors:

- \*SS-50 WIRE-WRAP/PROTOTYPE bare

\$ 39.00

GOLD \$1.60 ea. (M or F)

- \*TRANSITION CARD asm.

\$ 95.00

TIN M \$.40 ea. F \$.50 ea.

- \*TRANSITION CARD bare

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### DEALERS FOR SWTPC, GIMIX, AND TSC

\*All Thomas Instrumentation's cards come with full documentation including software source listings where applicable \*All assembled cards are burned in at 150F and fully tested with Gold conn. \*Bare card prices do not include edge connectors

\*See previous ads, write, or call for more detailed information.

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To satisfy in-depth questions regarding our products send \$20.00 to receive full documentation, schematics, & source listings for all boards currently in production





## JUDGE THE REST, THEN BUY THE BEST

Only GIMIX offers you SOFTWARE SWITCHING between MICROWARE's OS-9 and TSC's FLEX. Plus you get the power of the GMXBUG system monitor with its advanced debugging utility, and memory manipulation routines. A wide variety of languages and other software is available for these two predominant 6809 Disk Operating Systems.

You can order a system to meet your needs, or select from the 6809 Systems featured below.

## JUDGE THE FEATURES AND QUALITY OF GIMIX 6809 SYSTEMS

GIMIX' CLASSY CHASSIS™ is a heavyweight aluminum mainframe cabinet with back panel cutouts to conveniently connect your terminals, printers, drives, monitors, etc. A 3 position keyswitch lets you lock out the reset switch. The power supply features a ferro-resonant constant voltage transformer that supplies 8V at 30 amps, +15V at 5 amps, and -15V at 5 amps to insure against problems caused by adverse power input conditions. It supplies power for all the boards in a fully loaded system plus two 5 1/4" drives (yes! even a Winchester) that can be installed in the cabinet. The Mother board has fifteen 50 pin and eight 30 pin slots to give you the most room for expansion of any SS50 system available. 11 standard baud rates from 75 to 38.4K are provided and the I/O section has its own extended addressing to permit the maximum memory address space to be used. The 2 Mhz 6809 CPU card has both a time of day clock with battery back-up and a 6840 programmable timer. It also contains 1K RAM, 4 PROM/ROM/RAM sockets, and provides for an optional 9511A or 9512 Arithmetic Processor. The RAM boards use high speed, low power STATIC memory that is fully compatible with any OMA technique. STATIC RAM requires no refresh timing, no wait states or clock stretching, and allows fast, reliable operation. The system includes a 2 port RS232 serial interface and cables. All GIMIX boards use gold plated bus connectors and are fully socketed. GIMIX designs, manufactures, and tests in-house its complete line of products. All boards are twice tested, and burned in electrically to insure reliability and freedom from infant mortality of component parts. All systems are assembled and then retested as a system after being configured to your specific order.

### 56KB 2MHZ 6809 SYSTEMS WITH GMXBUG/FLEX/OS-9 SOFTWARE SELECTABLE

With #58 single density disk controller .....	\$2988.59
With #68 DMA double density disk controller .....	\$3248.49
to substitute Non-volatile CMOS RAM with battery back-up, add .....	150.00
for 50 Hz export power supply models, add .....	30.00

Either controller can be used with any combination of 5" and/or 8" drives, up to 4 drives total, have data recovery circuits (data separators), and are designed to fully meet the timing requirements of the controller I.C.s.

### 5 1/4" DRIVES INSTALLED IN THE ABOVE with all necessary cables

	SINGLE DENSITY		DOUBLE DENSITY		
	Formatted	Unformatted	Formatted	Unformatted	
40 track (48TPI) single sided	199,680	250,000	341,424	500,000	2 for \$700.00
40 track (48TPI) double sided	399,360	500,000	718,848	1,000,000	2 for 900.00
80 track (96TPI) single	404,480	500,000	728,064	1,000,000	2 for 900.00
80 track (96TPI) double	808,960	1,000,000	1,456,128	2,000,000	2 for 1300.00

Chart shows total capacity in bytes for 2 drives.

Contact GIMIX for price and availability of 8" floppy disk drives and cabinets; and 5" and 8" Winchester hard disk system.

### 128KB 2Mhz 6809 DMA Systems for use with TSC's UNIFLEX or MICROWARE's OS-9 Level 2

(Software and drives not included) .....	\$3798.39
to substitute 128KB CMOS RAM with battery back-up, add .....	300.00
for each additional 64KB NMOS STATIC RAM board, add .....	638.67
for each additional 64KB CMOS STATIC RAM board, add .....	798.64
for 50 Hz export power supply, add .....	30.00

NOTE: UNIFLEX can not be used with 5" minifloppy drives.

GIMIX has a wide variety of RAM, ROM, Serial and Parallel I/O, Video, Graphics, and other SS50 bus cards that can be added now or in the future. Phone or write for more complete information and brochure.

## THE SUN NEVER SETS ON GIMIX USERS

GIMIX Systems are found on every continent, except Antarctica. (Any users there? If so, please contact GIMIX so we can change this.) A representative group of GIMIX users includes: Government Research and Scientific Organizations in Australia, Canada, U.K., and in the U.S.; NASA, Oak Ridge, White Plains, Fermilab, Argonne, Scripps, Sloan Kettering, Los Alamos National Labs, AURA, Universities: Carleton, Waterloo, Royal Military College, in Canada; Trier in Germany; and in the U.S.; Stanford, SUNY, Harvard, UCSD, Mississippi, Georgia Tech. Industrial users in Hong Kong, Malaysia, South Africa, Germany, Sweden, and in the U.S.: GTE, Becton Dickinson, American Hoechst, Monsanto, Allied, Honeywell, Perkin Elmer, Johnson Controls, Associated Press, Aydin, Newkirk Electric, Revere Sugar, HI-G/AMS Controls, Chevron, Computer mainframe and peripheral manufacturers, IBM, Oki, Computer Peripherals Inc., Oume, Floating Point Systems. Software houses: Microware, T.S.C., Lucidata, Norpak, Talbot, Stylo Systems, AAA, HHH, Frank Hogg Labs, Epstein Associates, Softwest, Dynasoft, Research Resources U.K., Microworks, Analog Systems, Computerized Business Systems.



GIMIX Systems are chosen by the Pros because of quality, reliability and features.

# GIMIX inc.

The Company that delivers Quality Electronic products since 1975.

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(312) 927-5510 • TWX 910-221-4055

#### TO ORDER BY MAIL

SEND CHECK OR MONEY ORDER OR USE YOUR VISA OR MASTER CHARGE. Please allow 3 weeks for personal checks to clear.

U.S. orders add \$5 handling if order is under \$200.00. Foreign orders add \$10 handling if order is under \$200.00.

Foreign orders over \$200.00 will be shipped via Emery Air Freight COLLECT, and we will charge no handling. All orders must be prepaid in U.S. funds. Please note that foreign checks have been taking about 8 weeks for collection so we would advise wiring money, or checks drawn on a bank account in the U.S. Our bank is the International Bank of Chicago, account #73-32033 Visa or Master Charge also accepted.

GIMIX inc. reserves the right to change pricing and product specifications at any time without further notice.

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Consultants Inc. OS-9 is a trademark of Microware Inc. See their ads for other GIMIX compatible software.



# E X A T R O N FLEX & RS COLOR COMPUTER

The '6000 NEWS' is now here, the popular and very powerful FLEX™ disk system is now running on the Radio Shack Color Computer™!

The system can be ordered in parts as needed. OR AS A COMPLETE RUNNING SYSTEM, all you furnish is a color or B&W TV or monitor, and a desire to learn and use the powerful FLEX™ disk system and the Motorola 6809 microprocessor. The necessary software includes the special Technical System Consultants's 6809 General FLEX™ (\$150 including the Editor and Assembler). Note that the Editor and Assembler normally sell for \$50 each, which means that FLEX™ (special) is only \$50 as packaged. Also to mate up the system we offer the following software and hardware packages.

**F-MATE™:** (For Radio Shack system and Exatron) a set of software supplied on 5 inch diskettes. F-mate generates the interface between the disk controller\*, FLEX™, editor, assembler, word processor\*\* and the color computer. Simple step-by-step instructions detail the creation of a 'Sysgen' disk and then a final 'bootable' FLEX™ disk for the Color Computer. Included is: EXBOOT which is the power-up boot on disk, EXLOADER is used to load a virgin FLEX.COR when performing a Sysgen operation (the supplied assembler is required). The disk controller ROM is the interface at this stage of the operation. Also included is GETOFF which unloads and offsets needed programs to avoid conflicts in memory utilization. GETOFF can be used as a normal FLEX™ program once the bootable disk is finished. PATCHES is furnished to complete the interface between the new FLEX™ system and the Color Computer.

**PATCHES:** patch and make functional various standard TSC utilities and programs including APPEND ASMB COPY EDIT PUTLDR SAVE and others. A special NEWDISK (single side, single density, 35 track) routine allows disks made on the Color Computer to be read or written on other FLEX™ systems, insuring complete transfer of disks from Standard \$50 Bus computers.

Additional new software patches are being developed for existing Standard \$50 Bus FLEX™ software. They will be advertised as soon as reasonable debugging sessions indicate they function as expected.

\*\*\* TSC's Extended BASIC (XBASIC) patches are now running. Others to follow; call for current information.

## COLOR BASIC SAVE & LOAD

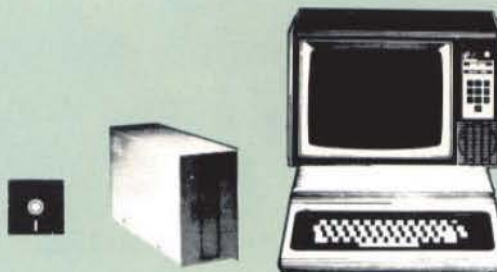
A set of utilities will be announced soon. Included in this package will be utilities to LOAD and SAVE the Microsoft BASIC programs you write for the Color Computers ROM BASIC. Fact is that this FLEX™ system does not in any way change the normal Radio Shack BASIC operation. All Radio Shack game-paks and other hardware and software can be used when desired. Also you have the powerful and very useful Radio Shack BASIC™, TSC XBASIC, Radio Shack Color Disk System and/or the Exatron disk system, you end up with three systems, FLEX™, Exatron™ or the Radio Shack system!!

\*\*\*\*NEW\*\*\*\*

NOTE: Also ready by the time this is published will be the F-Mate™ version for the Radio Shack Color Computer Disk System!!

VISA or MASTER CHARGE accepted

**DATA-COMP**  
**SOUTH EAST MEDIA**  
P.O. Box 794 Chattanooga TN 37443  
1-615-842-4601



## PRICES

FLEX special general version \$150.00  
Includes Editor and Assembler

F-mate™ as described above but not  
including coming utilities

\*\* Specify RS Disk System or  
Exatron Disk System \*\*  
When ordered with FLEX \$ 49.95  
Without FLEX \$ 59.95

\*Special Exatron Disk Controller with  
32K RAM expansion \$299.95

NOTE: This unit required  
for the above system.

Radio Shack Color Disk System  
Single drive with controller  
and power supply \$579.50

Screen-Clean™ - RFI hash eliminator  
for the standard Exatron Expansion  
and disk controller (removes  
most of the monitor screen hash)  
Wired and tested..... \$ 39.95

Single Disk Drive with ten-  
closure - single side, 40  
track - double density \$329.95

Dual Disk Drive with enclosure -  
single side, 40 track  
double density \$649.95

Single drive cable \$ 24.95  
Double drive cable \$ 34.95

Radio Shack 16K Color Com-  
puter, with Extended BASIC  
and ready for above items \$595.00

With the above as a package you have  
a FULL 48K RAM plus ROM computer with  
FLEX™ and disk(s) for LESS THAN \$1500  
and with C O L O R !!

Additional Disk drives with enclosure  
and power supply \$329.95

\*\* Coming soon  
+ With Power Supply

Add \$25.00  
Shipping & Handling  
For Complete Set



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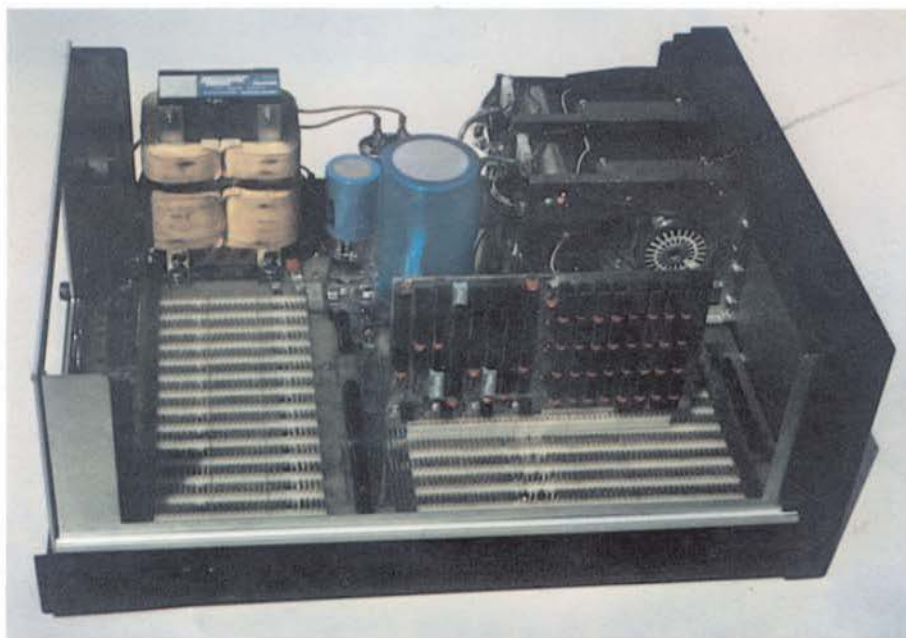
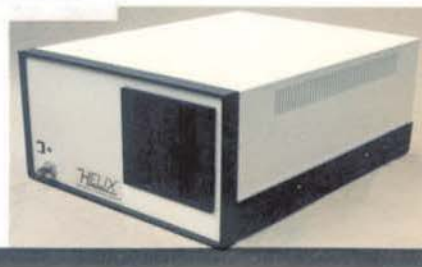
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# HELIX™



## THE MAINFRAME

- Industry Standard Optima™ Cabinet
- Largest Constant Voltage Power Supply in the industry
- S-64 Bus gives 16 Bit Power and S-50 Bus Compatibility
- 10 Main (S-64) Slots
- 14 I/O (S-30) Slots plus 2 On-board
- On-board Baud Rate Generator to 38.4Kb
- Space and Power for two 5 1/4" Disk Drives
- Full Address Decoding for I/O Slots
- Two RS-232 Serial and Two parallel Ports On-board
- Single Board Construction for Reliability
- Faraday Shielded Bus Lines give "Text Book Clean" Signals

## THE PROCESSORS

- 6809**
- Standard 2 MHz Operation
  - Standard DAT Compatible with GIMIX and SWTPC
  - Standard 6840 Interval Timer
  - Standard 1K Scratchpad RAM
  - Standard Clock/Calendar with Battery
  - Provision for Programmers Console
- 68000**
- Standard 8 MHz Operation
  - Memory Management Hardware
  - Provision for Programmers Console
  - 16 Bit Power and 8 Bit Compatibility



The HELIX™ computer system represents the latest advance in S-50 bus computer systems. Relying on the physical nature of S-50 bus connectors to guarantee compatibility, the HELIX adds 14 bus lines (becoming S-64) to allow a 68000 processor to operate with full 16 bit data transfer and 24 bit addressing, while at the same time providing full interchangeability with existing S-50 components.

Offered with a selection of processors, memories, and peripheral controllers, a HELIX system can be configured for applications ranging from advanced hobbyist to multiterminal time-sharing.

Designed to offer the utmost in speed, reliability, and utility at a reasonable price, it represents a new standard of quality for those who require a professionally designed computer for professional use.

## THE POWER SUPPLY

- Ferro-resonant Transformer for Line Noise and Under-Voltage Protection
- Conservative 25 Amps at 8.5 Volts
- Conservative 5 Amps at  $\pm 16$  Volts
- Conservative Component Rating for Reliability

## THE COMPONENTS

- Fully Socketed
- Gold Plated Bus Connectors
- Only "B" Series 68XX Components Used
- Only Top Grade Logic Circuits Used
- Industrial Grade Components Throughout

## THE MEMORIES

- DM-64**
- Field Proven
  - Proprietary Memory Control Logic
  - Fully Transparent Refresh
  - Tested at 2.5 MHz Operation
- DM-512**
- 512K Bytes on a Single S-64 Board
  - 16 Bit Power and 8 Bit Compatibility
  - Runs in Existing S-50 Systems where Physical Space Allows
  - Full 24 Bit Addressing
  - Fully Transparent Refresh

## THE PRICES

Because of the variety of configurations possible, full pricing cannot be given. Representative prices are:

- 64K 6809 HELIX ..... \$1995
- 64K 68000 HELIX ..... \$2595
- 512K 6809 HELIX ..... \$4450
- 512K 68000 HELIX ..... \$4995

# HAZELWOOD COMPUTER SYSTEMS

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(314) 837-3466

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